

**ADDRESSES**

**BOARD 85**

**J. Rypko**

**Nemo 3**

**NUMERICAL ASICS, REGISTERS ADDRESSES**

name	mode	position bits					addresses
		5	4	3	2	1	
cpt1 A	R	0	0	0	0	0	00
cpt2 A	R	0	0	0	0	1	02
cpt3 A	R	0	0	0	1	0	04
cpt4 A	R	0	0	0	1	1	06
cpt1 Kh	R	0	0	1	0	0	08
cpt2 Kh	R	0	0	1	0	1	0A
cpt3 Kh	R	0	0	1	1	0	0C
cpt4 Kh	R	0	0	1	1	1	0E
cpt1 $\alpha$ MSB	R	0	1	0	0	0	10
cpt2 $\alpha$ MSB	R	0	1	0	0	1	12
cpt3 $\alpha$ MSB	R	0	1	0	1	0	14
cpt4 $\alpha$ MSB	R	0	1	0	1	1	16
cpt1 Kb	R	0	1	1	0	0	18
cpt2 Kb	R	0	1	1	0	1	1A
cpt3 Kb	R	0	1	1	1	0	1C
cpt4 Kb	R	0	1	1	1	1	1E
threshold reg	R/W	1	0	0	0	0	20
injec A/K data	W	1	0	0	0	1	(test) 22
injec A/K go	W	1	0	0	1	0	(test) 24
RAZ	W	1	0	0	1	1	26
internal test	W	1	0	1	0	0	28
external test	W	1	0	1	0	1	2A
test AAA	W	1	0	1	1	0	2C
test 555	W	1	0	1	1	1	2E
cpt1 $\alpha$ LSB	R	1	1	0	0	0	30
cpt1 $\alpha$ LSB	R	1	1	0	0	1	32
cpt1 $\alpha$ LSB	R	1	1	0	1	0	34
cpt1 $\alpha$ LSB	R	1	1	0	1	1	36
reg status	R	1	1	1	0	0	38
not used		1	1	1	0	1	
not used		1	1	1	1	0	
not used		1	1	1	1	1	

**ASICS ADDRESSES**

name	mode	position bits				addresses	
		9	8	7	6		
asic 00	R/W	0	0	0	0	/CSA0	000
asic 01	"	0	0	0	1	/CSA1	040
asic 02	"	0	0	1	0	/CSA2	080
asic 03	"	0	0	1	1	/CSA3	0C0
asic 04	"	0	1	0	0	/CSA4	100
asic 05	"	0	1	0	1	/CSA5	140
asic 06	"	0	1	1	0	/CSA6	180
asic 07	"	0	1	1	1	/CSA7	1C0
asic 08	"	1	0	0	0	/CSA8	200
asic 09	"	1	0	0	1	/CSA9	240
reg status prélim R		1	0	1	0	/CSSP	280
raz card		1	0	1	1	broadcast with A15,/RTC	2C0
start stop		1	1	0	0	broadcast with A15,/SSC	300
not used		1	1	0	1		
.....							
not used		1	1	1	1		

**CARDS ADDRESSES**

name	position bits						addresses
	15	14	13	12	11	10	
carte 00	0	0	0	0	0	0	0000
carte 01	0	0	0	0	0	1	0400
carte 02	0	0	0	0	1	0	0800
carte 03	0	0	0	0	1	1	0C00
carte 04	0	0	0	1	0	0	1000
carte 05	0	0	0	1	0	1	1400
carte 06	0	0	0	1	1	0	1800
carte 07	0	0	0	1	1	1	1C00
carte 08	0	0	1	0	0	0	2000
carte 09	0	0	1	0	0	1	2400
carte 10	0	0	1	0	1	0	2800
carte 11	0	0	1	0	1	1	2C00
carte 12	0	0	1	1	0	0	3000
carte 13	0	0	1	1	0	1	3400
carte 14	0	0	1	1	1	0	3800
carte 15	0	0	1	1	1	1	3C00
carte 16	0	1	0	0	0	0	4000
carte 17	0	1	0	0	0	1	4400
carte 18	0	1	0	0	1	0	4800
carte 19	0	1	0	0	1	1	4C00
not used	0	1	0	1	1	0	
.....							
not used	0	1	1	1	1	1	

<i>name</i>	<i>position bits</i>								<i>addresses</i>								
	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	
	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0	
w broadcast cards raz	1	0	0	0	0	0	1	0	1	1	0	0	0	0	0	0	82C0
w broadcast cards start/stop	1	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	8300

**CAGES ADDRESSES**

are generated by the VMV VIC controller