

**MOTHER BOARD PIN OUT - J1 VME STANDARDISED,  
Board 164 - P' NO STANDARDISED**

|    | J 1      |          |           | P'         |            |            |
|----|----------|----------|-----------|------------|------------|------------|
|    | A        | B        | C         | A          | B          | C          |
| 01 | D00      | BBSY*    | D08       | STOP A*    | STOP TO*   | STOP MES*  |
| 02 | D01      | BCLR*    | D09       | GND        | GND        | GND        |
| 03 | D02      | ACFAIL*  | D10       | START MES* |            |            |
| 04 | D03      | BG0IN*   | D11       | ADC < 4 >  | GND        |            |
| 05 | D04      | BG0OUT*  | D12       | ADC < 3 >  | GND        |            |
| 06 | D05      | BG1IN*   | D13       | ADC < 2 >  | GND        |            |
| 07 | D06      | BG1OUT*  | D14       | ADC < 1 >  | GNG        |            |
| 08 | D07      | BG2IN*   | D15       | ADC < 0 >  | GND        |            |
| 09 | GND      | BG2OUT*  | GND       |            |            |            |
| 10 | SYSCLOCK | BG3IN*   | SYSFAIL*  |            |            |            |
| 11 | GND      | BG3OUT*  | BERR*     |            |            |            |
| 12 | DS1*     | BR0*     | SYSRESET* |            |            |            |
| 13 | DS0*     | BR1*     | LWORD*    | HIT0 < 0 > | HIT0 < 1 > | HIT0 < 2 > |
| 14 | WRITE*   | BR2*     | AM5       | HIT0 < 3 > | HIT1 < 0 > | HIT1 < 1 > |
| 15 | GND      | BR3*     | A23       | HIT1 < 2 > | HIT1 < 3 > | HIT2 < 0 > |
| 16 | DTACK*   | AM0      | A22       | HIT2 < 1 > | HIT2 < 2 > | HIT2 < 3 > |
| 17 | GND      | AM1      | A21       | HIT3 < 0 > | HIT3 < 1 > | HIT3 < 2 > |
| 18 | AS*      | AM2      | A20       | HIT3 < 3 > | HIT4 < 0 > | HIT4 < 1 > |
| 19 | GND      | AM3      | A19       | HIT4 < 2 > | HIT4 < 3 > | HIT5 < 0 > |
| 20 | IACK*    | GDN      | A18       | HIT5 < 1 > | HIT5 < 2 > | HIT5 < 3 > |
| 21 | IACKIN*  | SERCLK   | A17       | HIT6 < 0 > | HIT6 < 1 > | HIT6 < 2 > |
| 22 | IACKOUT* | SERDAT*  | A16       | HIT6 < 3 > | HIT7 < 0 > | HIT7 < 1 > |
| 23 | AM7      | GND      | A15       | HIT7 < 3 > | HIT7 < 3 > | HIT8 < 0 > |
| 24 | A07      | IRQ7*    | A14       | HIT8 < 1 > | HIT8 < 2 > | HIT8 < 3 > |
| 25 | A06      | IRQ6*    | A13       | HIT9 < 0 > | HIT9 < 1 > | HIT9 < 2 > |
| 26 | A05      | IRQ5*    | A12       | HIT9 < 3 > |            |            |
| 27 | A04      | IRQ4*    | A11       |            |            |            |
| 28 | A03      | IRQ3*    | A10       | AVDD       | AVDD       | AVDD       |
| 29 | A02      | IRQ2*    | A09       |            |            |            |
| 30 | A01      | IRQ1*    | A08       | GND        | GND        | GND        |
| 31 | -12 VDC  | +5 STDBY | +12 VDC   |            |            |            |
| 32 | + 5 VDC  | +5 VDC   | + 5 VDC   | VDD        | VDD        | VDD        |