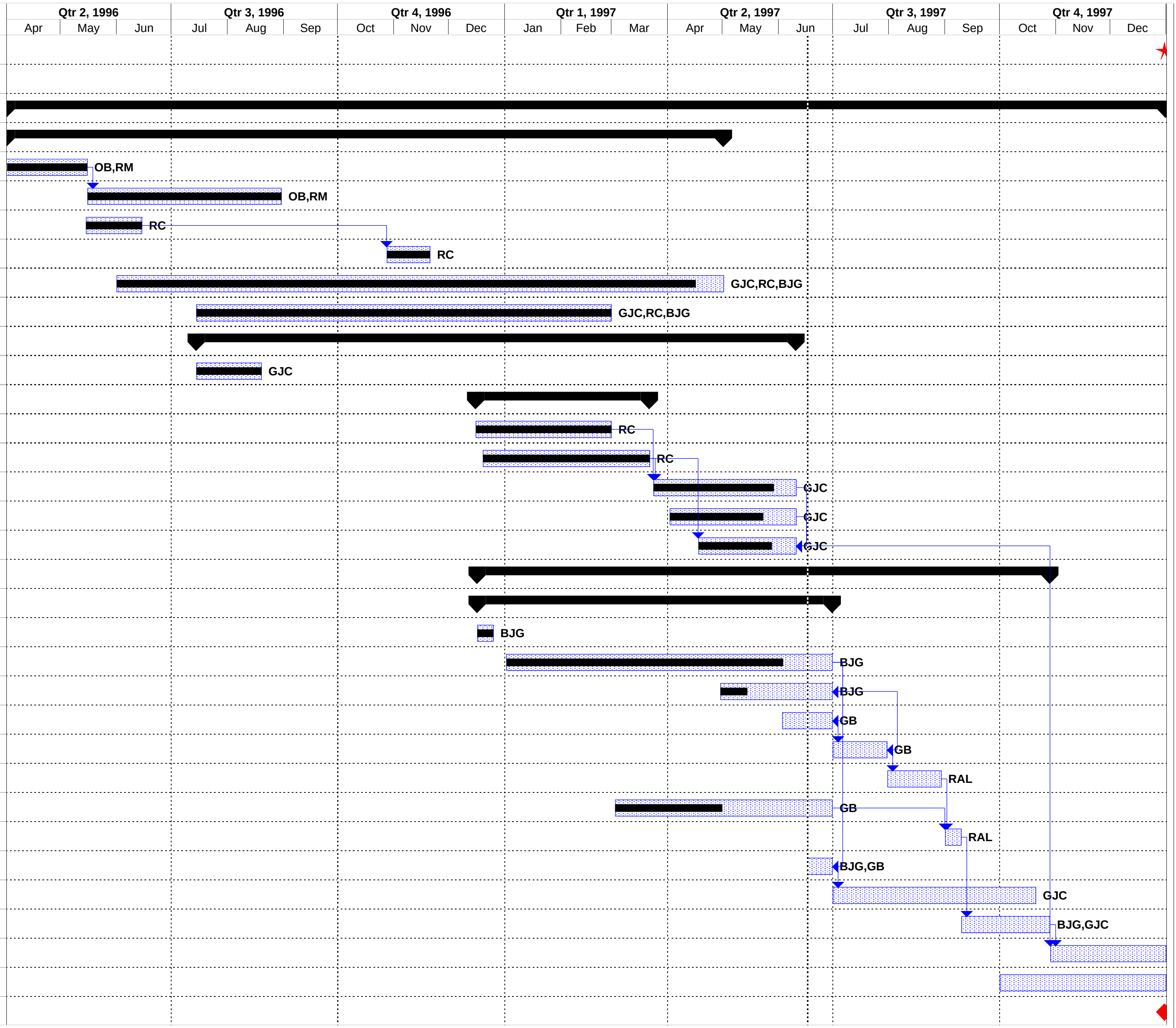
ID	Task Name
1	Provide fully tested/documented module
2	
3	**ROB-IN**
4	**Design module**
5	Produce URD
6	Update URD
7	Produce Outline Design
8	Produce Product Brief
9	List & decide design issues
10	Choose processor
11	**Develop software**
12	Design software
13	Obtain emulation components
14	Obtain i960 evaluation kit
15	Obtain PCI-based PC
16	Produce buffer manager code
17	Produce emulation environment code
18	Test/debug software in emulation environment
19	**Build hardware**
20	Design hardware
21	Convert input fifo to PLD
22	Combine/convert existing logic to MACH5
23	Enter schematic data
24	Prepare parts outline for CAD
25	Layout PCB
26	Manufacture PCB
27	Order parts
28	Populate board
29	Document hardware
30	Produce test software
31	Test and debug hardware
32	**Test and debug module**
33	**Produce user documentation**
34	Fully tested/documented module



Project: ROB-IN Date: Tue 6/17/97	Task		Milestone	◆	Rolled Up Task		Rolled Up Progress
	Progress		Summary		Rolled Up Milestone ◇		

5 **Produce URD**
Describe functionality of ROB-IN in terms of user requirements.
URD=User Requirements Document
Depends on ROB URD

7 **Produce Outline Design**
Describe overall functionality and interfacing philosophy.

8 **Produce Product Brief**
Product Brief is a description of the product for the benefit of potential users/customers.

9 **List & decide design issues**
This list is to be maintained on the ROB-IN development WWW page and should refer to all significant design issues re functional components, interfacing and operation of the ROB-IN.

10 **Choose processor**
Investigate options for the on-board buffer manager processor in light of the evolving ROB-IN specification. It is not intended to perform in-house studies; sources of information will be processor specs, performance reports, and any existing in-house experience.

12 **Design software**
Design ROB-IN buffer manager software.

17 **Produce emulation environment code**
To drive buffer manager processor in evaluation kit format