



VME Buffers

J3 Backplane ROD Signals Output

FPGA1 XC2S200E (VME Interface)

ROD Backplane PECL Drivers

FPGA2 XC2S600E (TIM Functions)

Stand-alone Clocks & Delay Lines

NIM/ECL Signals I/O

TTCrq Module

TTC Fibre-optic Input

TIM-3C Functional Layout