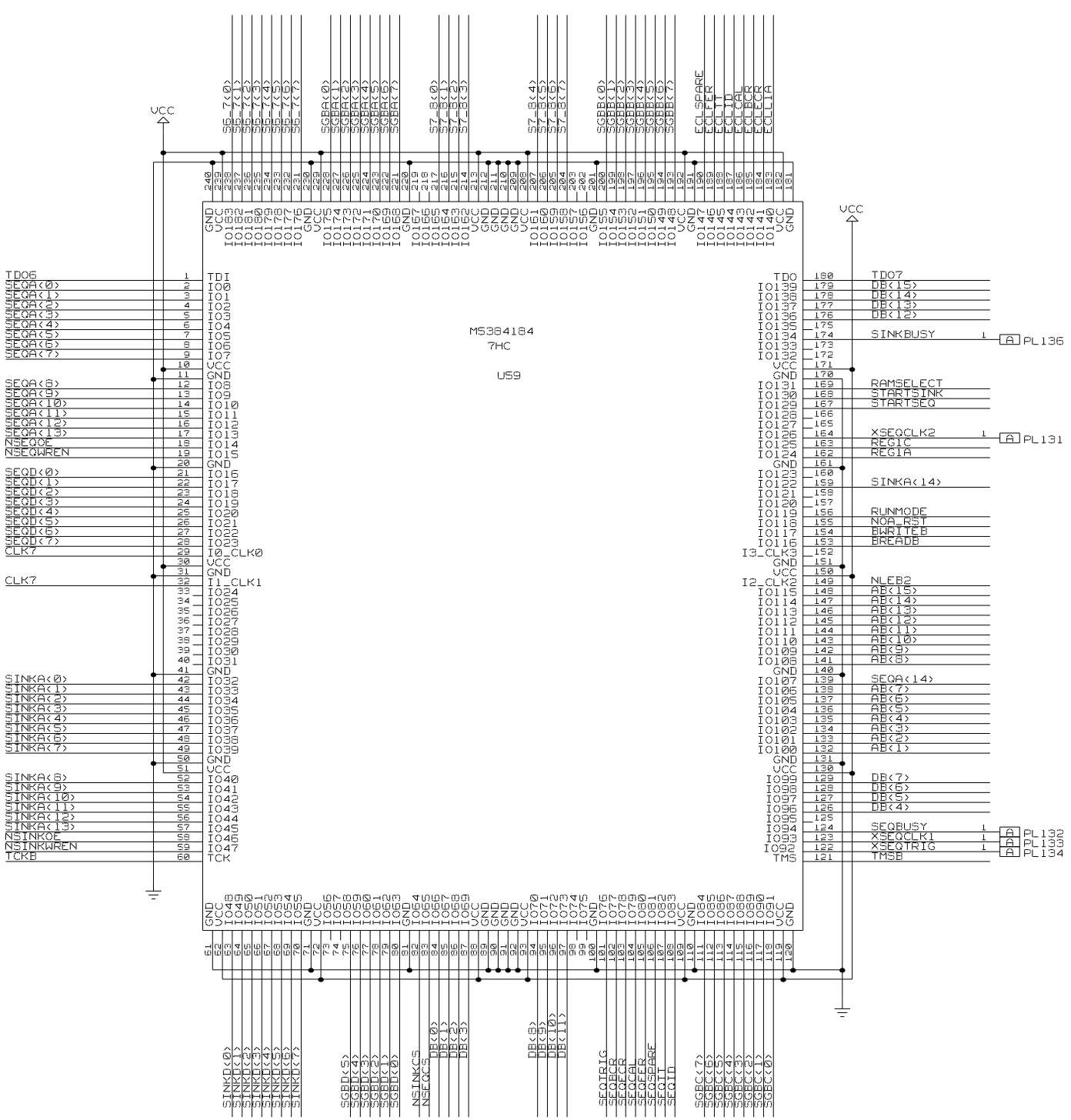
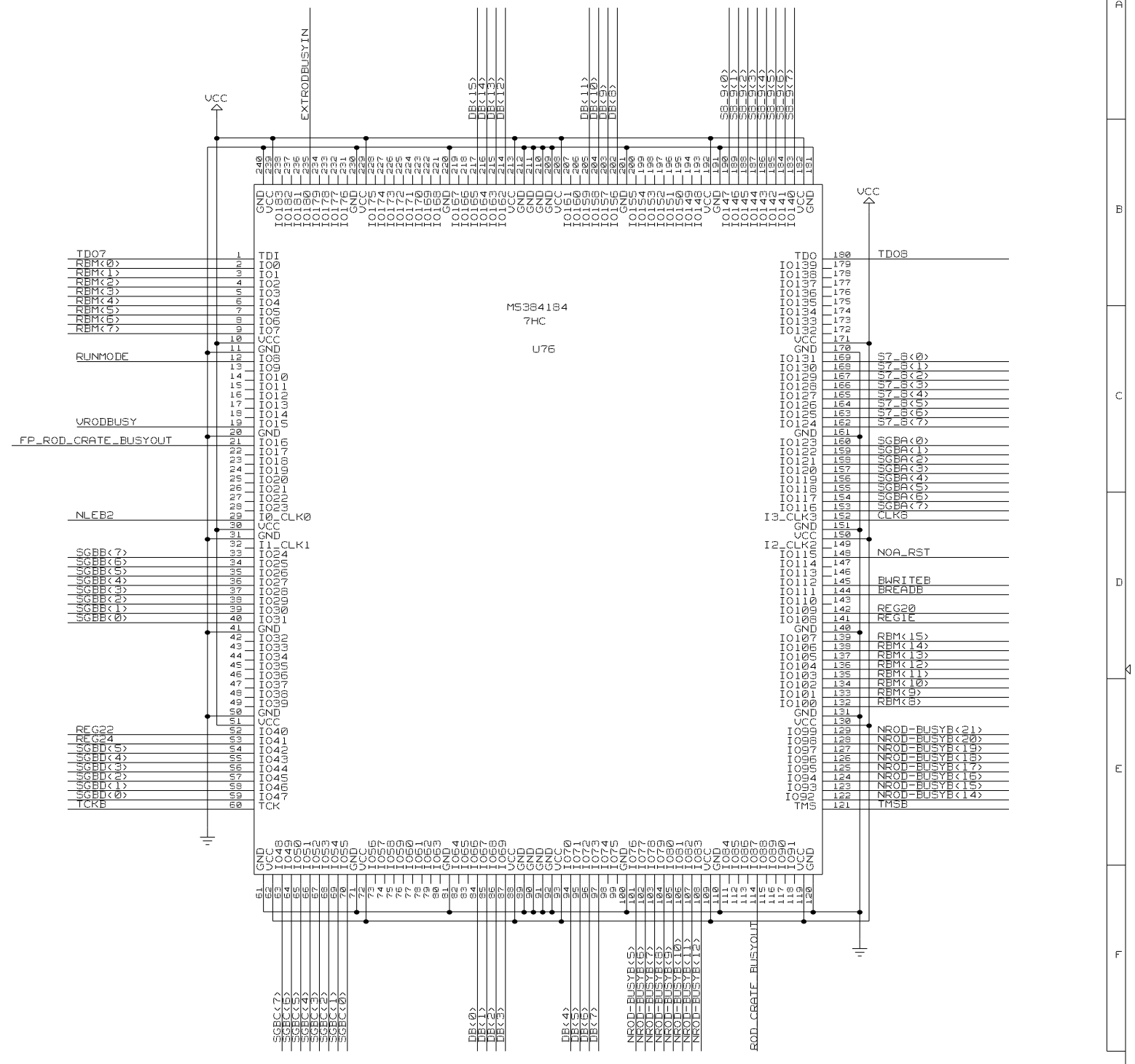
A	2-TE-0102-023-05-B	THIS DRAWING CONFORMS TO B.S. 3939

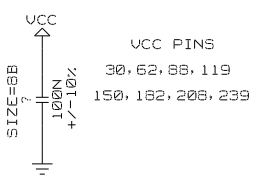
PLD-7 SEQ. RAM



PLD-8 ROD BUSY

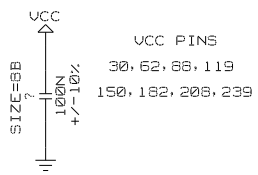


DECOUPLING



VCC PINS
30, 62, 88, 119
150, 182, 208, 239

DECOUPLING



VCC PINS
30, 62, 88, 119
150, 182, 208, 239

PROVISIONAL

ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS
B	17-05-2001	TE 198	D. J. BECKETT	M. POSTRANECKY	M. POSTRANECKY	
A	14-12-1999	- - - -	D. J. BECKETT	M. POSTRANECKY	M. POSTRANECKY	

USED ON ATLAS SCT

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COUNCIL FOR THE CENTRAL LABORATORY OF THE RESEARCH COUNCILS
RUTHERFORD APPLETON LABORATORY, CHILTON, OXON OX11 0QX.

TITLE

PC3162M CIRCUIT

TTC INTERFACE MODULE (TIM-1) 02-01

A 2-TE-0102-023-05-B

TOTAL NO. OF SHEETS 17

A 2-TE-0102-023-05-B