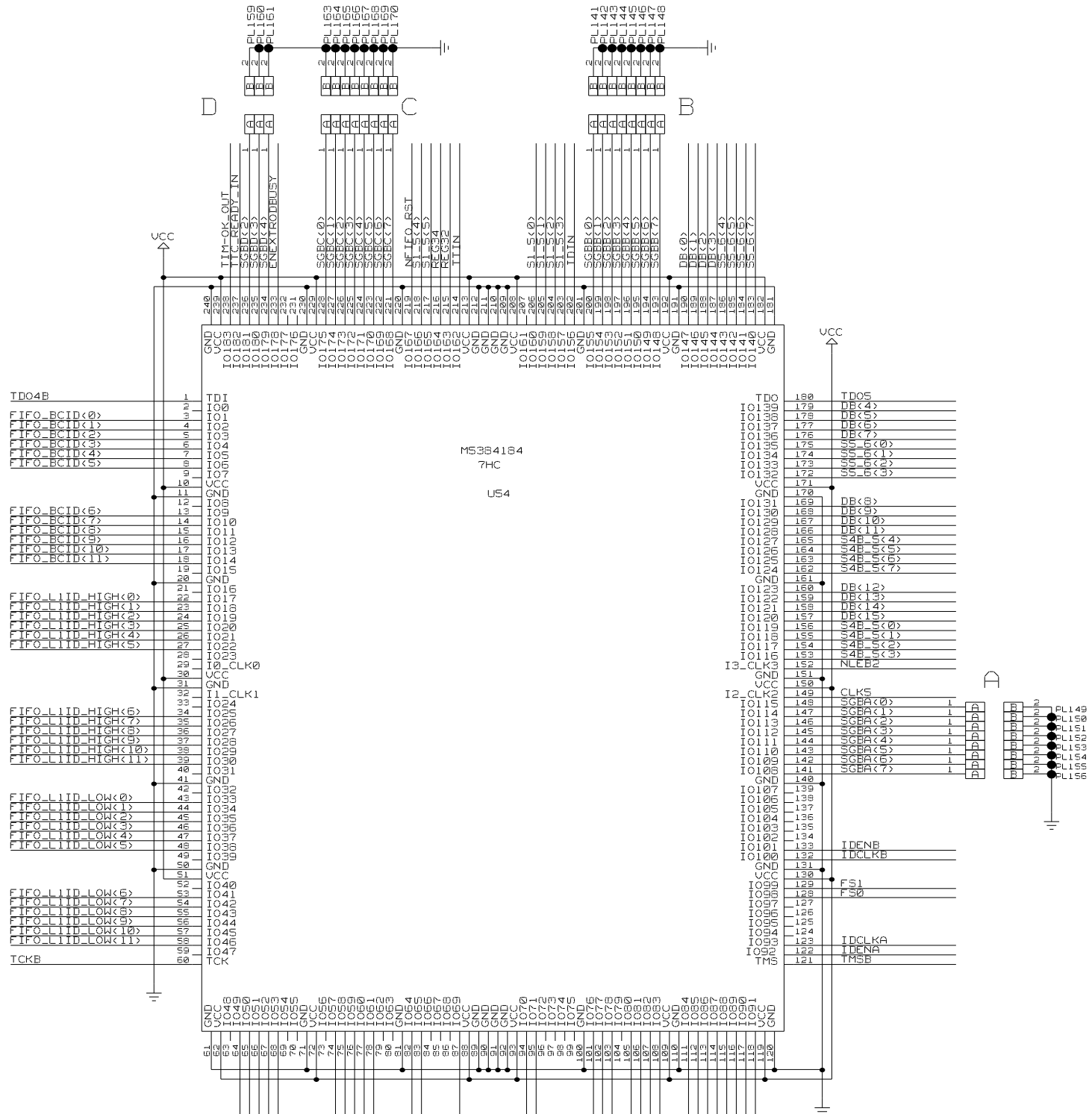
A	2-TE-0102-023-04-C	THIS DRAWING CONFORMS TO B.S. 3939

PLD-5 SERIALISERS



DECOUPLING

VCC

SIZE=8B ?

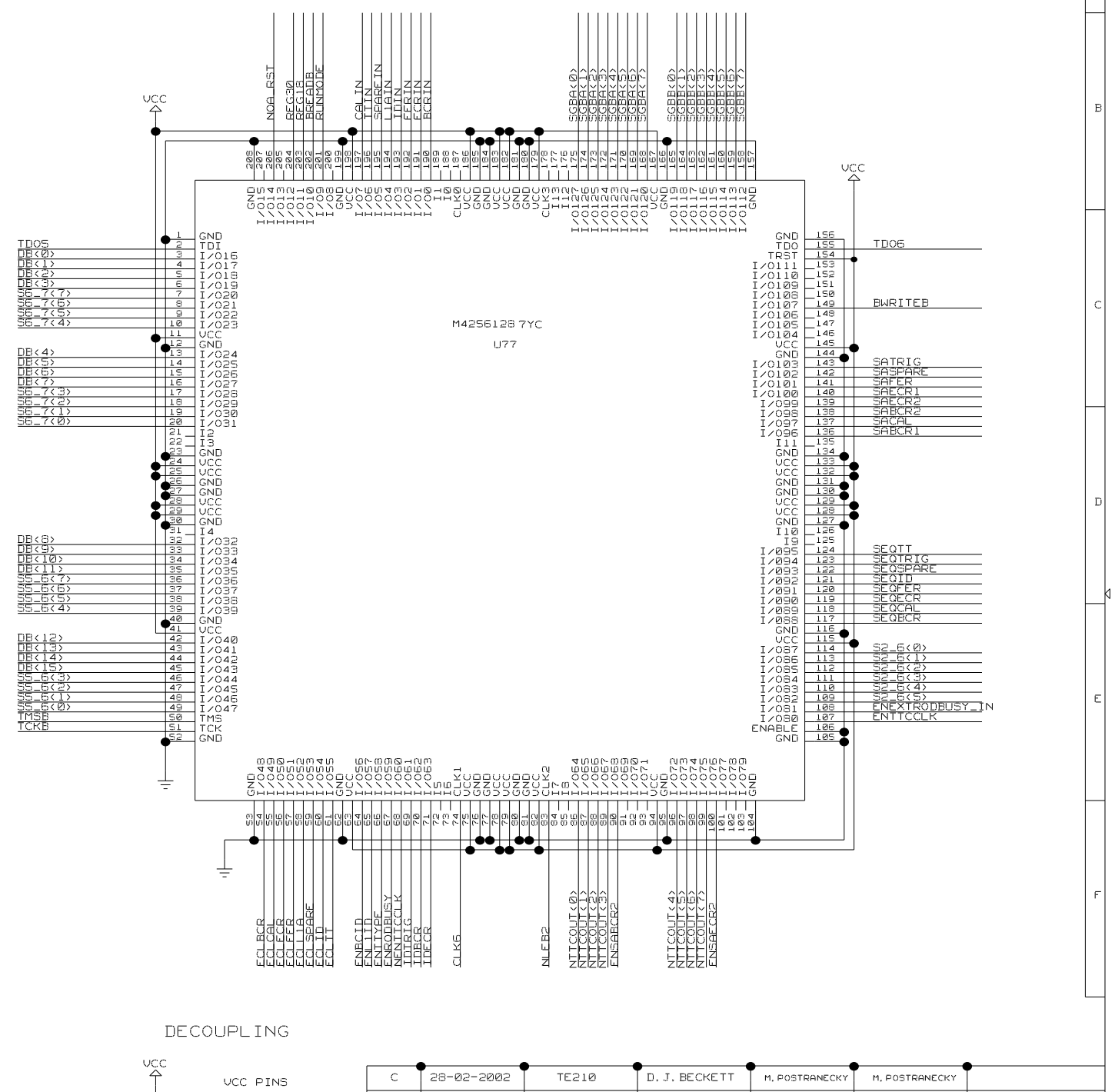
100nF +/-10%

VCC PINS

30, 62, 88, 119

150, 182, 208, 239

PLD-6 BACKPLANE MAP



DECOUPLING

VCC

SIZE=6B ?

100nF +/-10%

VCC PINS

24, 28, 78

128, 132, 182

ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS
C	28-02-2002	TE210	D.J. BECKETT	M. POSTRANECKY	M. POSTRANECKY	
B	31-07-2001	TE198	D.J. BECKETT	M. POSTRANECKY	M. POSTRANECKY	
A	14-12-1999	- - - -	D.J. BECKETT	M. POSTRANECKY	M. POSTRANECKY	

USED ON ATLAS SCT

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COUNCIL FOR THE CENTRAL LABORATORY OF THE RESEARCH COUNCILS
RUTHERFORD APPLETON LABORATORY, CHILTON, OXON OX11 0QX.

TITLE

PC3162M CIRCUIT

TTC INTERFACE MODULE (TIM-1) 02-02

A 2-TE-0102-023-04-C

TOTAL NO. OF SHEETS 17

2-TE-0102-023-04-C