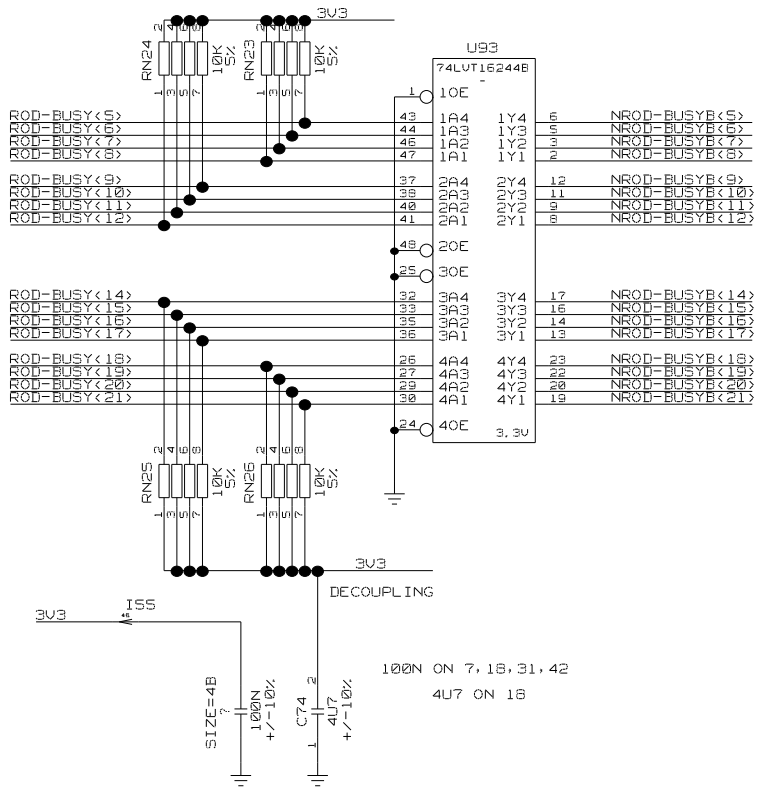
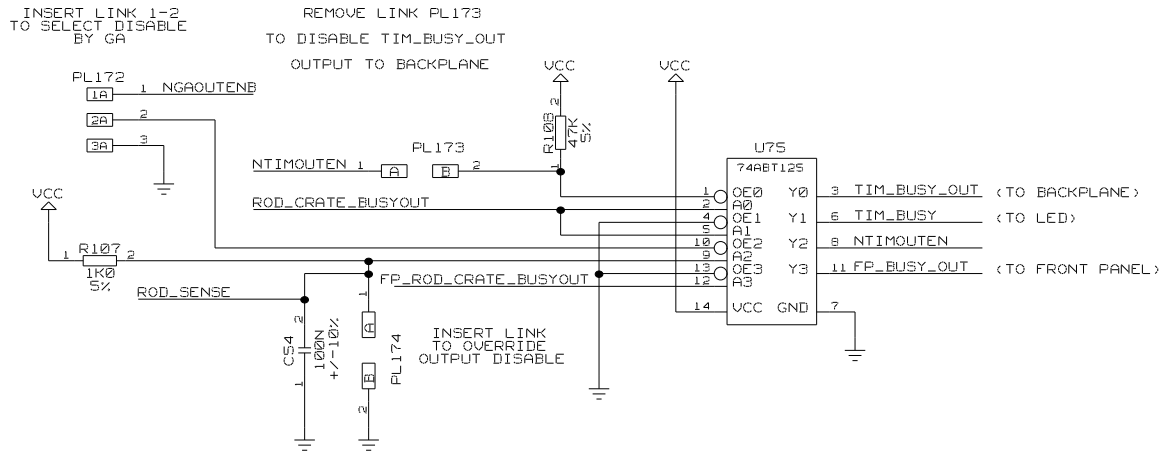
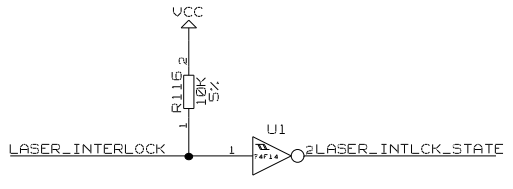
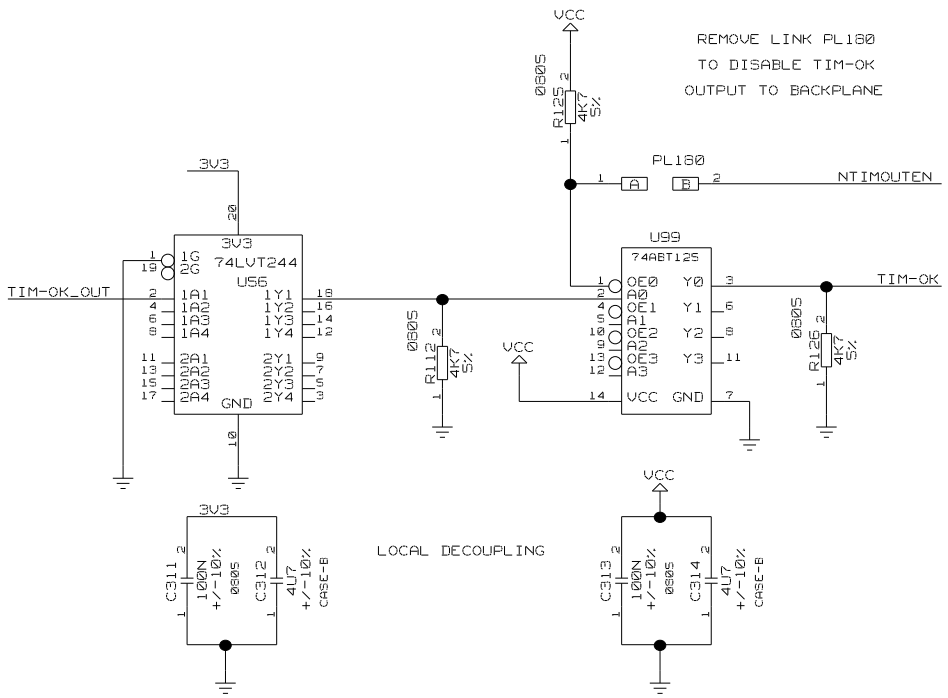
BUSY RECEIVER + DRIVER









ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS
C	28-02-2002	TE210	D. J. BECKETT	M. POSTRANECKY	M. POSTRANECKY	
B	31-07-2001	TE 198	D. J. BECKETT	M. POSTRANECKY	M. POSTRANECKY	
A	14-12-1999	- - - -	D. J. BECKETT	M. POSTRANECKY	M. POSTRANECKY	

USED ON ATLAS SCT

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RUTHERFORD APPLETON LABORATORY, CHILTON, OXON OX11 0QX.

TITLE

PC3162M CIRCUIT

TTC INTERFACE MODULE (TIM-1)

02-02

A | 2-TE-0102-023-14-C

TOTAL NO. OF SHEETS 17

A | 2-TE-0102-023-14-C