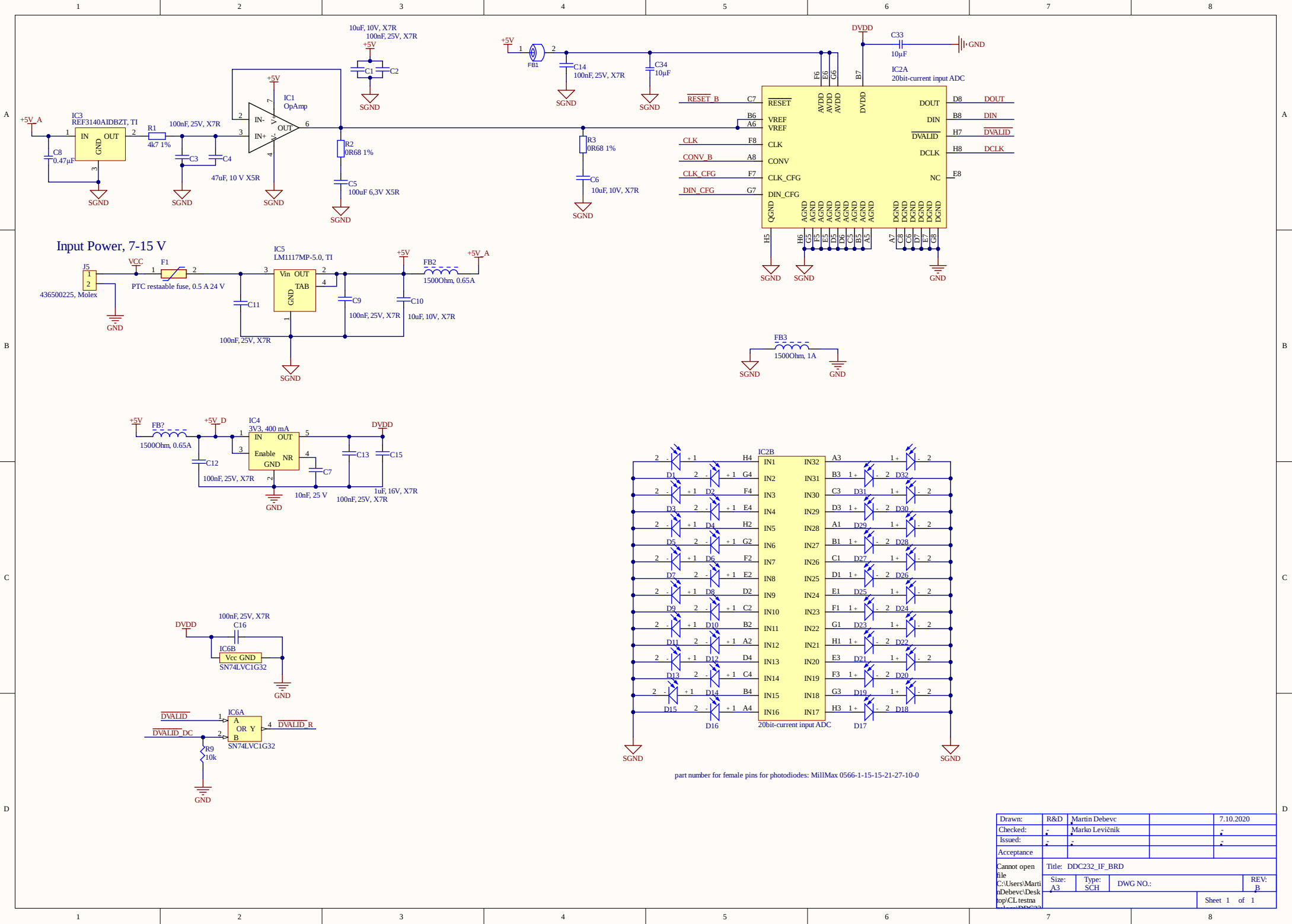
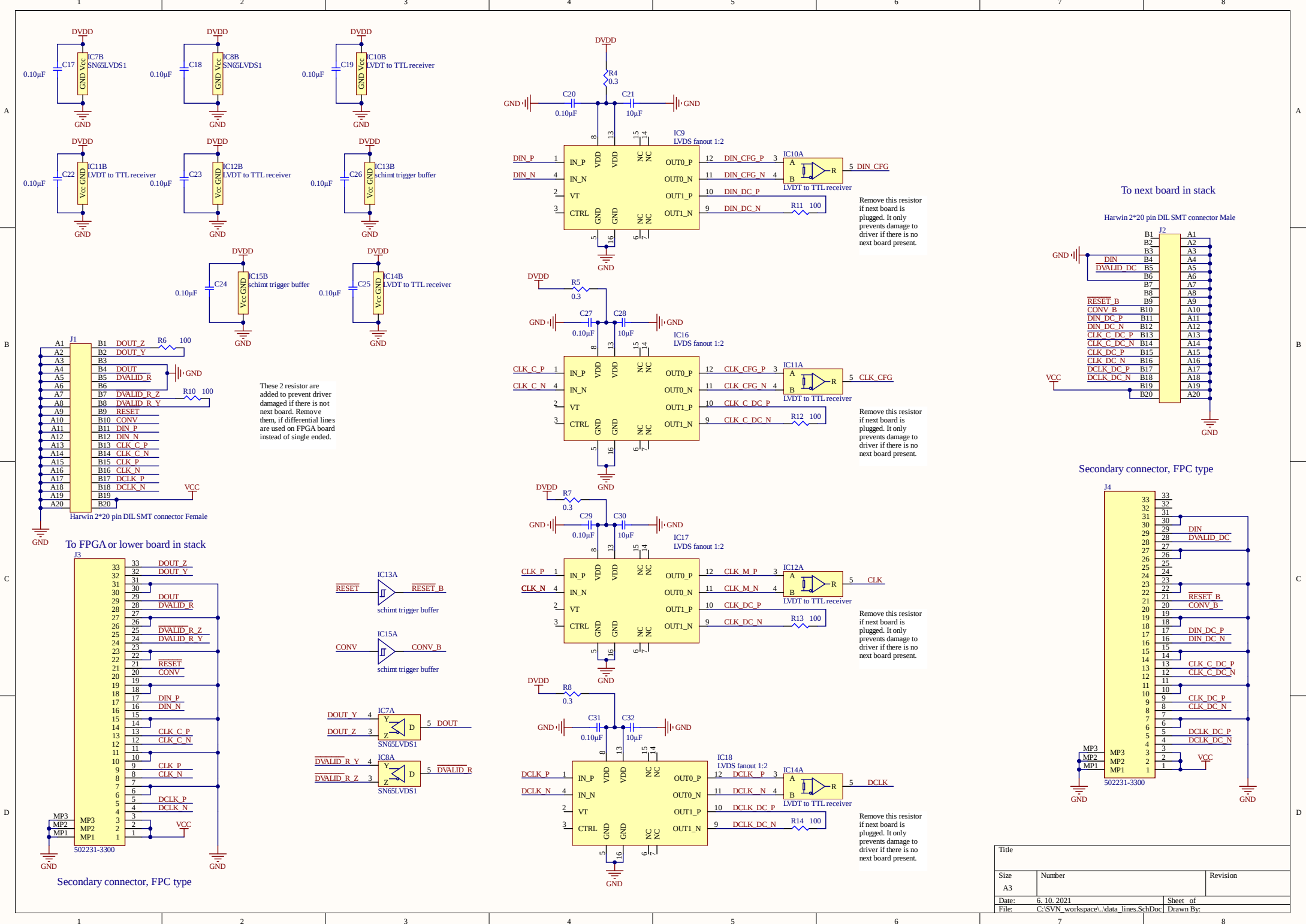




Drawn:	R&D	Martin Debevč		7.10.2020
Checked:	Marko Levičnik			
Issued:				
Acceptance				
Cannot open file	Title: DDC232_IF_BRD			
C:\Users\Martin Debevč\Desktop\CL testna	Size: A3	Type: SCH	DWG NO.:	REV: B
				Sheet 1 of 1



These 2 resistor are added to prevent driver damaged if there is not next board. Remove them, if differential lines are used on FPGA board instead of single ended.

Remove this resistor if next board is plugged. It only prevents damage to driver if there is no next board present.

Remove this resistor if next board is plugged. It only prevents damage to driver if there is no next board present.

Remove this resistor if next board is plugged. It only prevents damage to driver if there is no next board present.

Remove this resistor if next board is plugged. It only prevents damage to driver if there is no next board present.

To next board in stack

Harwin 2*20 pin DIL SMT connector Male

Secondary connector, FPC type

To FPGA or lower board in stack

Secondary connector, FPC type

Title		
Size	Number	Revision
A3		
Date:	6.10.2021	Sheet of
File:	C:\SVN_workspace\..data_lines.SchDoc	Drawn By:

