TIM Schedule

- Complete layout & schematics done.
- Full simulation of PLD’s – Ongoing.
- 2 prototypes submitted for manufacture – 26^{th} July.
- Board return date (fast turnaround) – 22^{nd} August.
- 2 TIMs tested & ready for Cambridge tests – 7^{th} Oct

TIM Schedule

- Sep00–Mar01: develop stand–alone test software
- Sep00–Mar01: debug & test TIM stand–alone
- Oct 00–Mar01: debug & test TIM with TTC system
- Nov00–Mar01: user evaluation period
- Dec00 ? : manufacture more prototype TIMs

TIM Schedule

- Jan01–Mar01: incorporate TTCrx daughter–board
- Jan01–Mar01: incorporate Busy Module design ?
- Apr01 : pre–production TIM (aggressive)
- Dec02 : production complete
  SCT requires 8 TIMs in total