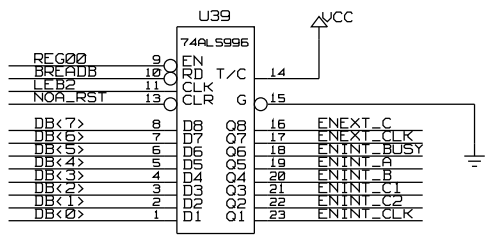
VME REGISTERS

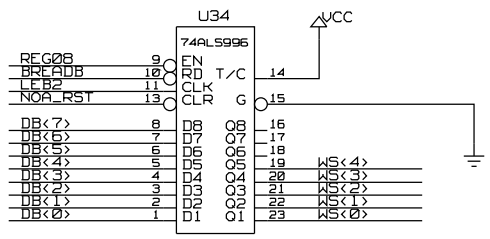
IN/OUTPUTS ENABLE REGISTER

(BASE ADDRESS + 00)



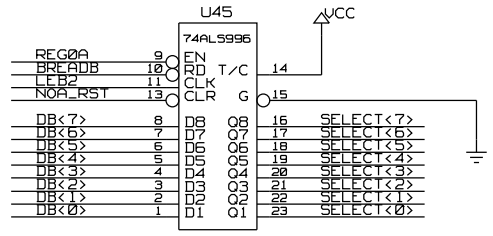
TRIGGER WINDOW SIZE AND DELAY REGISTER

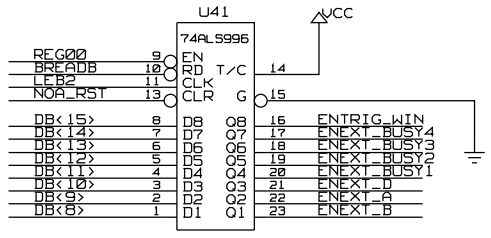
(BASE ADDRESS +08)

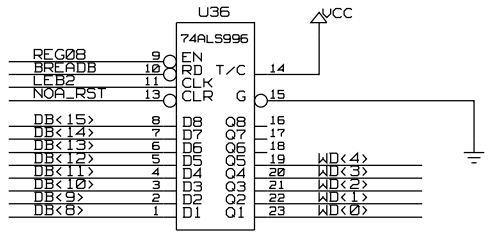


SELECT TRIGGER DELAY REGISTER

BASE ADDRESS +0A

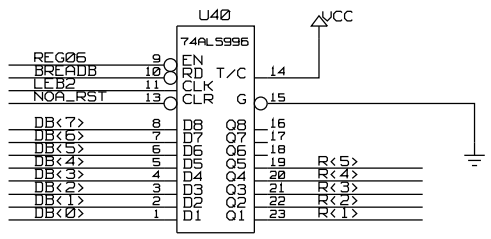






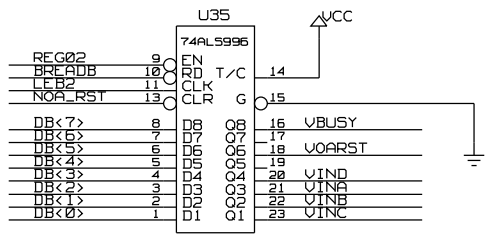
REPEAT FREQUENCY REGISTERS

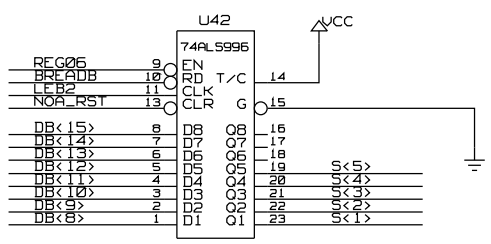
(BASE ADDRESS +06)

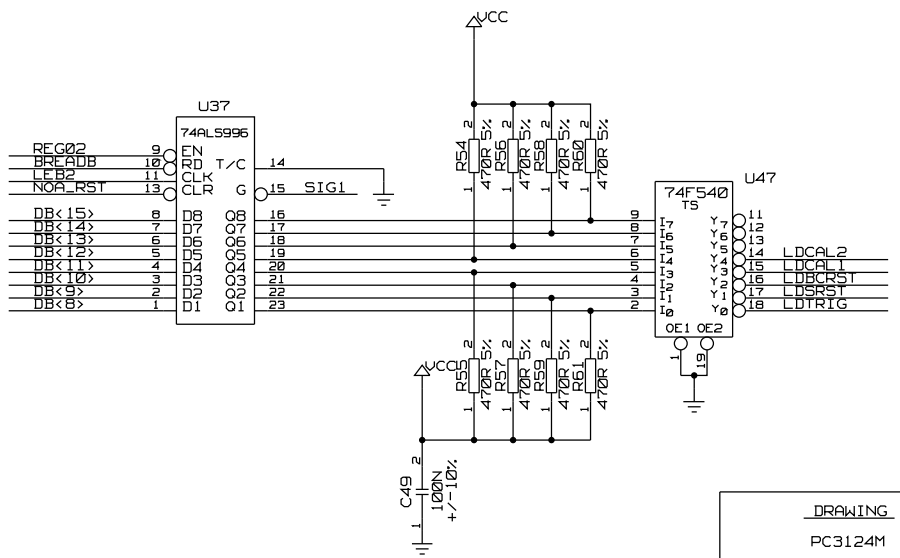


VME COMMANDS REGISTER

(BASE ADDRESS +02)







DECOUPLING

ALL CHIPS IN
DECOUPLED SOCKETS
TO VCC

A	21-05-1998	- - - -	D. J. BECKETT	M. POSTRANECKY	M. POSTRANECKY	
ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS

USED ON ATLAS

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CCLRC COUNCIL FOR THE CENTRAL LABORATORY OF THE RESEARCH COUNCILS
RUTHERFORD APPLETON LABORATORY, CHILTON, OXON OX11 0QX.

TITLE

PC3124M CIRCUIT

C+C MASTER MODULE

DRAWING
PC3124M
1798
Thu Jul 16 14:31:46 1998

A 2-TE-0095-009-05-A

TOTAL NO. OF SHEETS 7