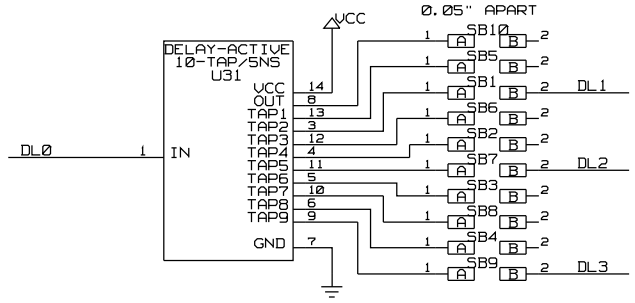
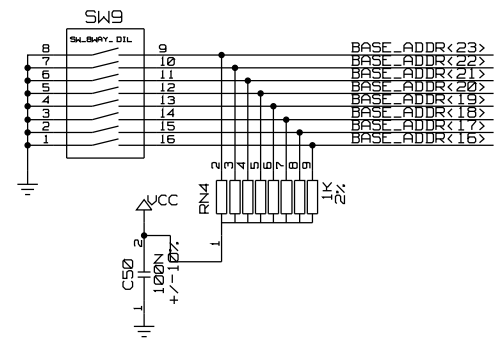
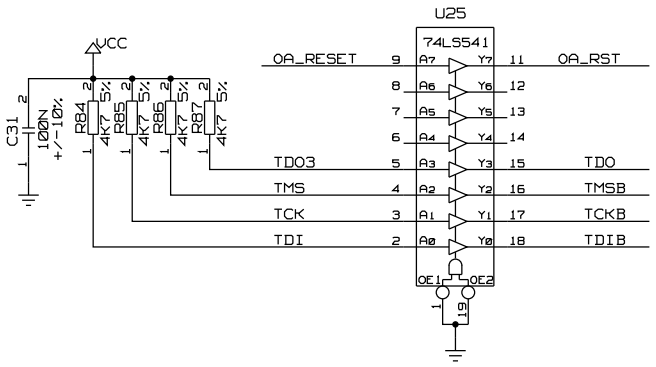
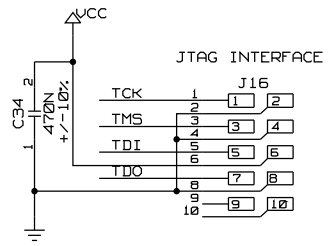
VME BASE ADDRESS SWITCH

DOUBLE LINE OF PADS
0.05" APART



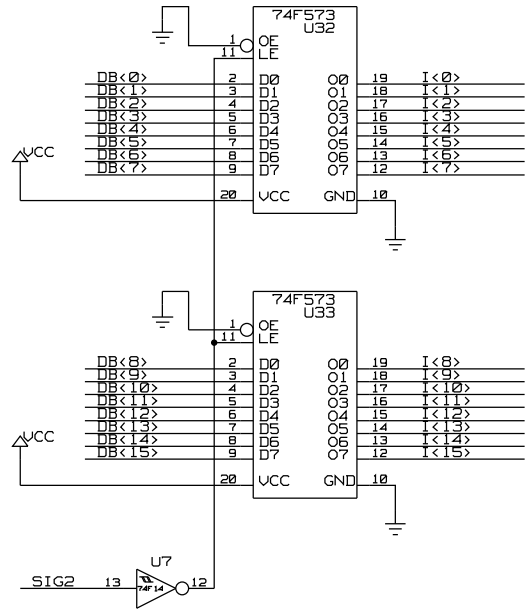




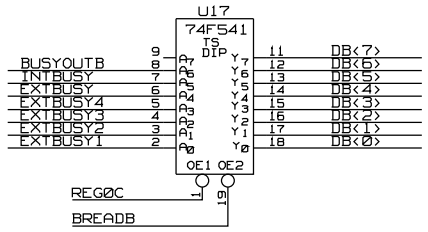


COMMAND AND BUSY REGISTERS

FAST COMMAND STRINGS REGISTER
(BASE ADDRESS +04)



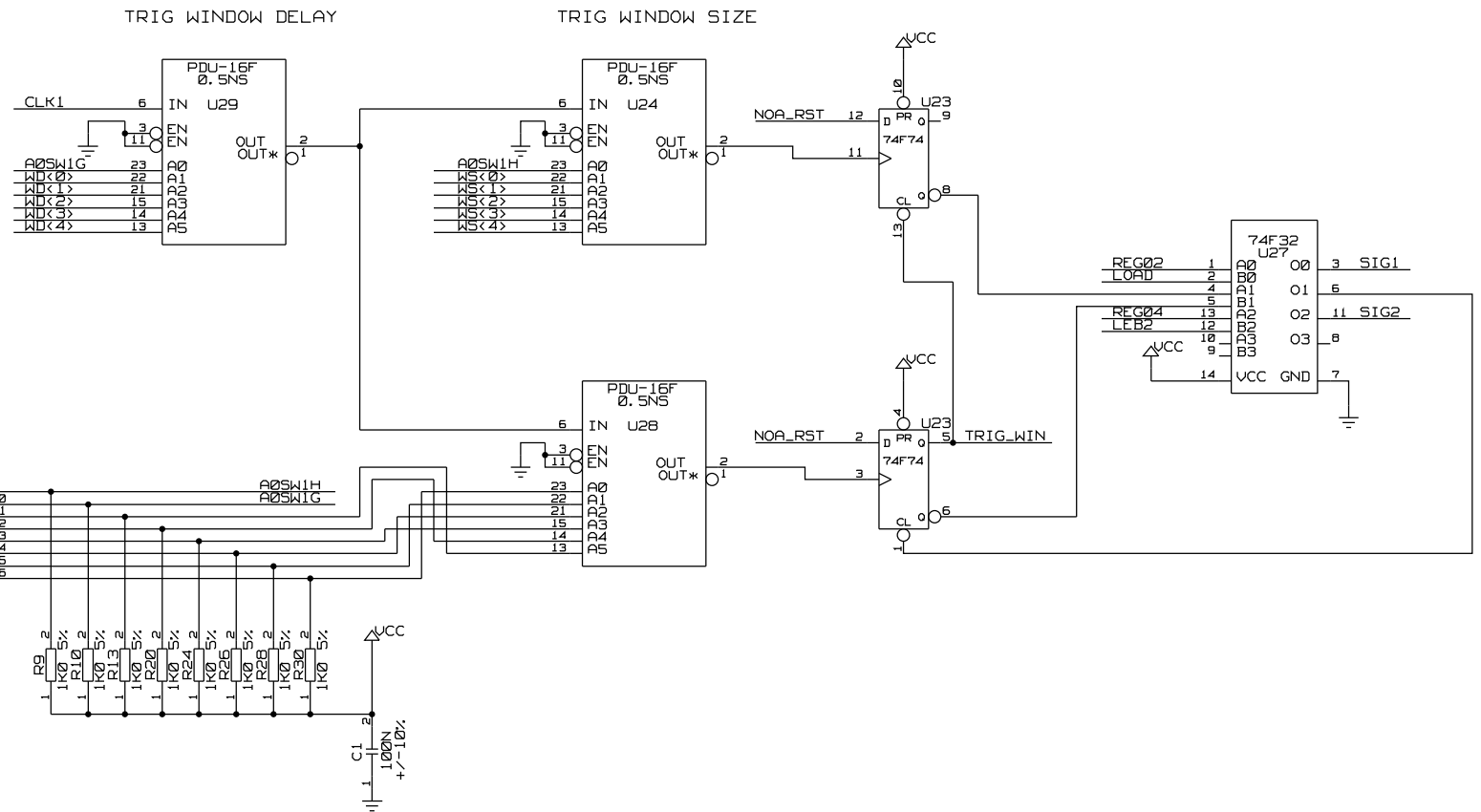
BUSY STATE REGISTER
(BASE ADDRESS +0C)



TRIGGER WINDOW

TRIG WINDOW DELAY

TRIG WINDOW SIZE



DECOUPLING

ALL CHIPS IN
DECOUPLED SOCKETS
TO VCC

A	21-05-1998	- - - -	D. J. BECKETT	M. POSTRANECKY	M. POSTRANECKY	
ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS

USED ON ATLAS

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CCLRC
COUNCIL FOR THE CENTRAL LABORATORY OF THE RESEARCH COUNCILS
RUTHERFORD APPLETON LABORATORY, CHILTON, OXON OX11 0QX.

TITLE

PC3124M CIRCUIT

C+C MASTER MODULE

DRAWING
PC3124M
1798
Thu Jul 16 14:31:50 1998

A | 2-TE-0095-009-06-A

TOTAL NO. OF SHEETS 7