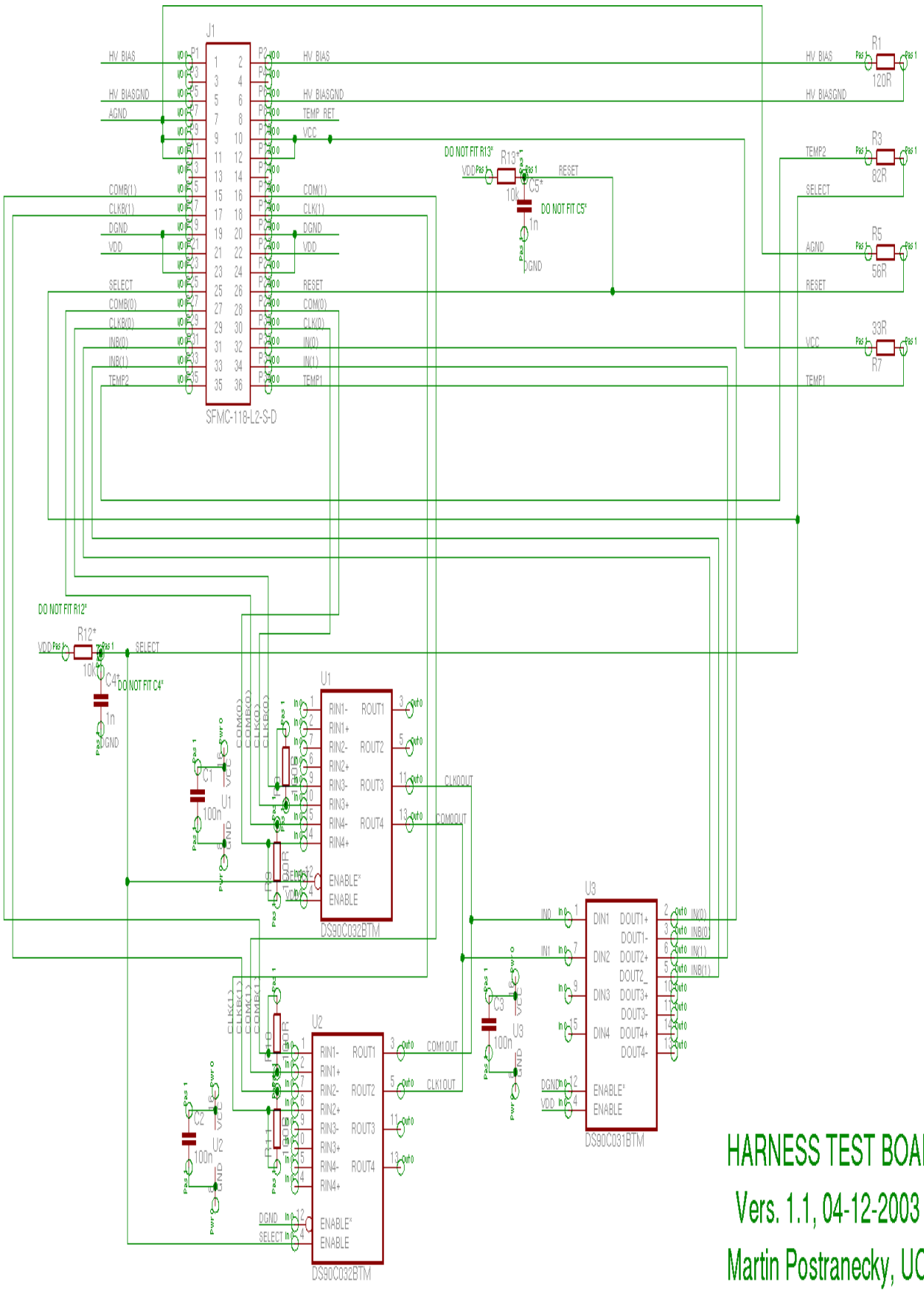
J1
SFMC-118-L2-S-D
HV BIAS
HV BIASGND
AGND
COMB(1)
CLKB(1)
DGND
VDD
SELECT
COMB(0)
CLKB(0)
INB(0)
INB(1)
TEMP2
TEMP_RET
VCC
COM(1)
CLK(1)
RESET
COM(0)
CLK(0)
IN(0)
IN(1)
TEMP1
R1
120R
R3
82R
R5
56R
R7
33R
DO NOT FIT R13*
R13*
10K
C5*
DO NOT FIT C5*
1n
R12*
DO NOT FIT R12*
C4*
DO NOT FIT C4*
U1
U2
U3
DS90C032BTM
DS90C031BTM
RIN1-
RIN1+
RIN2-
RIN2+
RIN3-
RIN3+
RIN4-
RIN4+
ROUT1
ROUT2
ROUT3
ROUT4
ENABLE*
ENABLE
CLK0OUT
COM0OUT
COM1OUT
CLK1OUT
DIN1
DIN2
DIN3
DIN4
DOUT1+
DOUT1-
DOUT2+
DOUT2-
DOUT3+
DOUT3-
DOUT4+
DOUT4-
100n
GND
VCC
HARNESS TEST BOARD
Vers. 1.1, 04-12-2003
Martin Postranecky, UCL