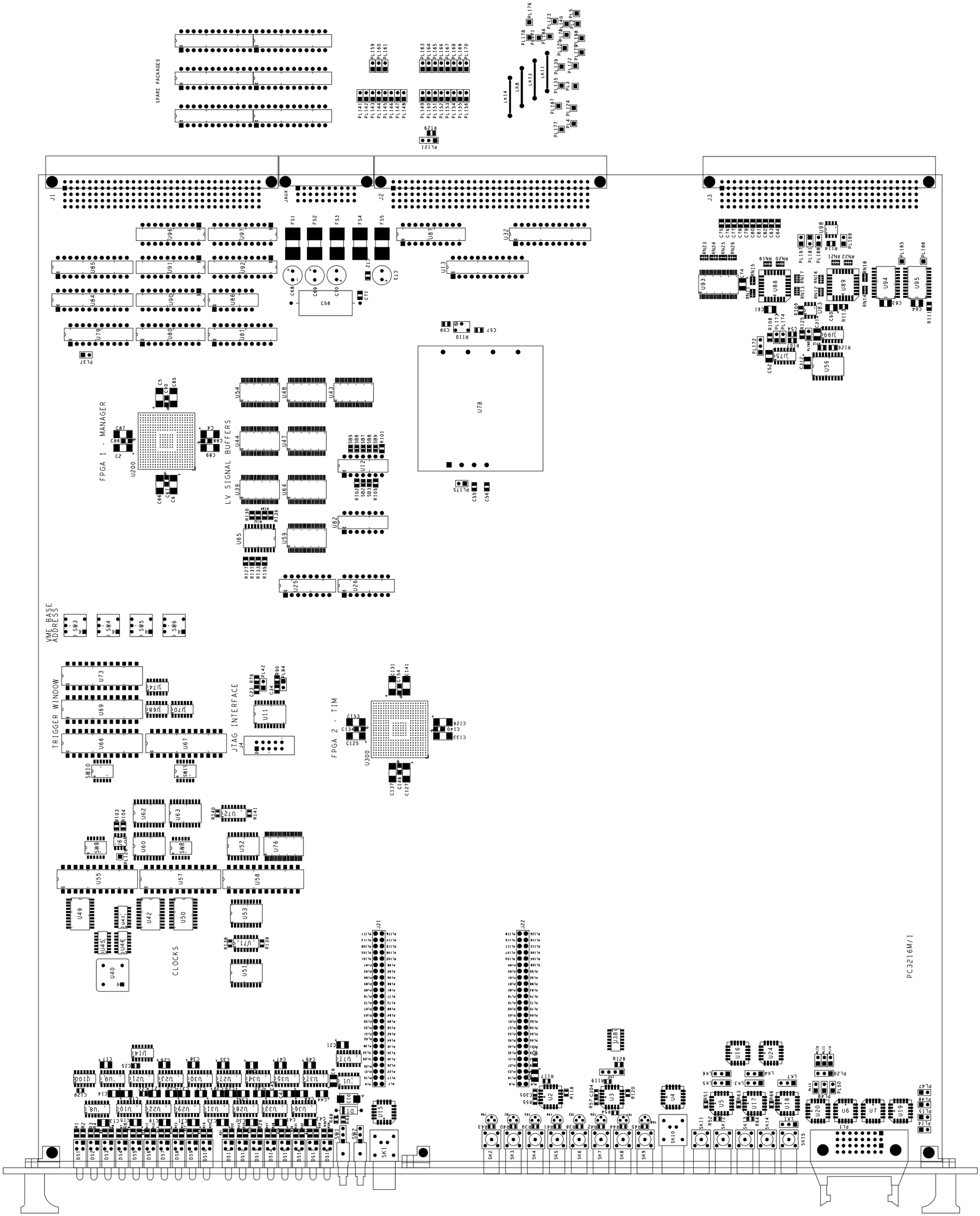
SPARE PACKAGES
J1
JAUX
J2
J3
FPGA 1 - MANAGER
U200
LV SIGNAL BUFFERS
U78
TRIGGER WINDOW
JTAG INTERFACE
FPGA 2 - TIM
U300
CLOCKS
VME BASE ADDRESS
SW3
SW4
SW5
SW6
PC3216M/1