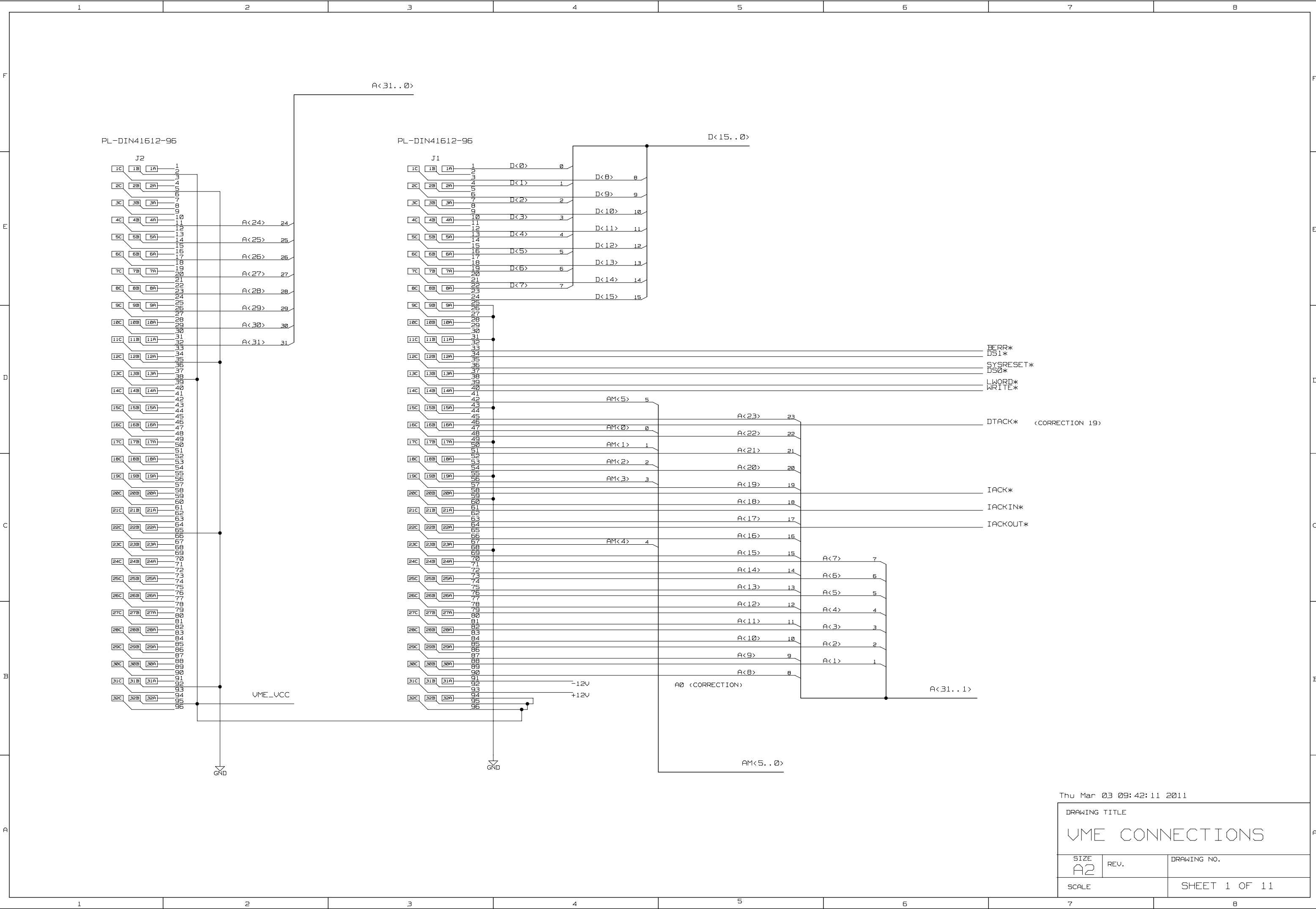
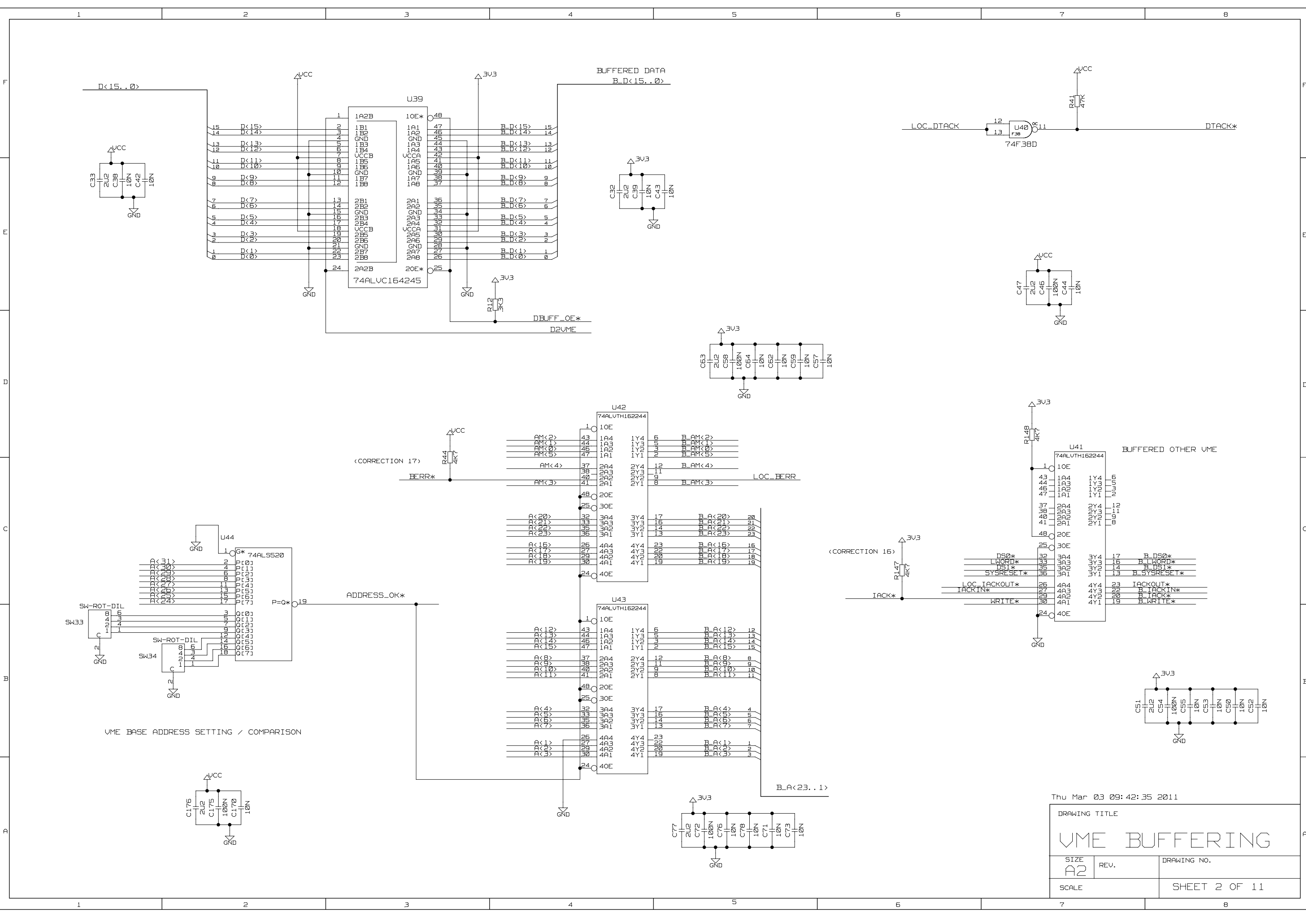






12345678

F

E

D

C

B

A

F

E

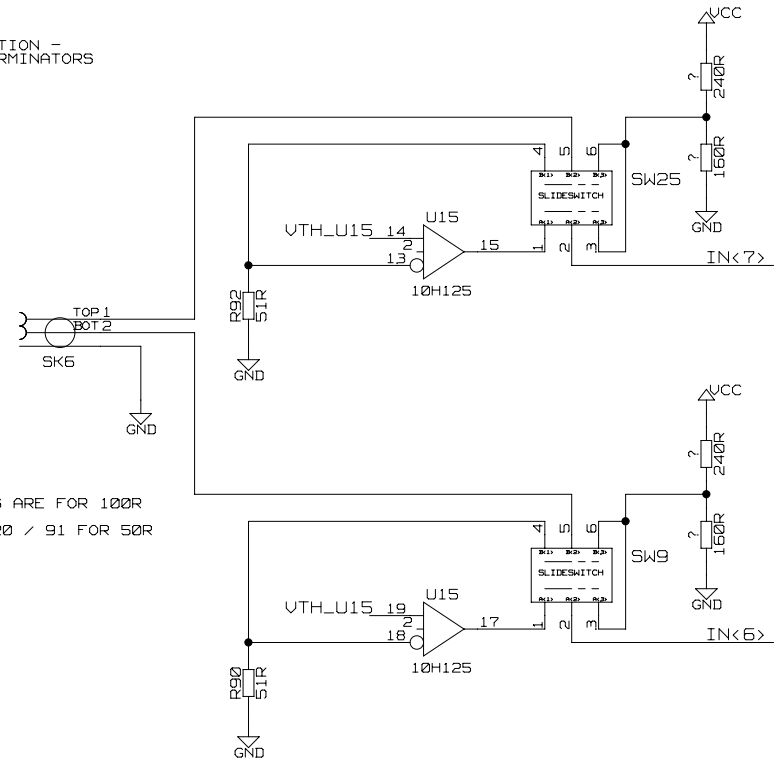
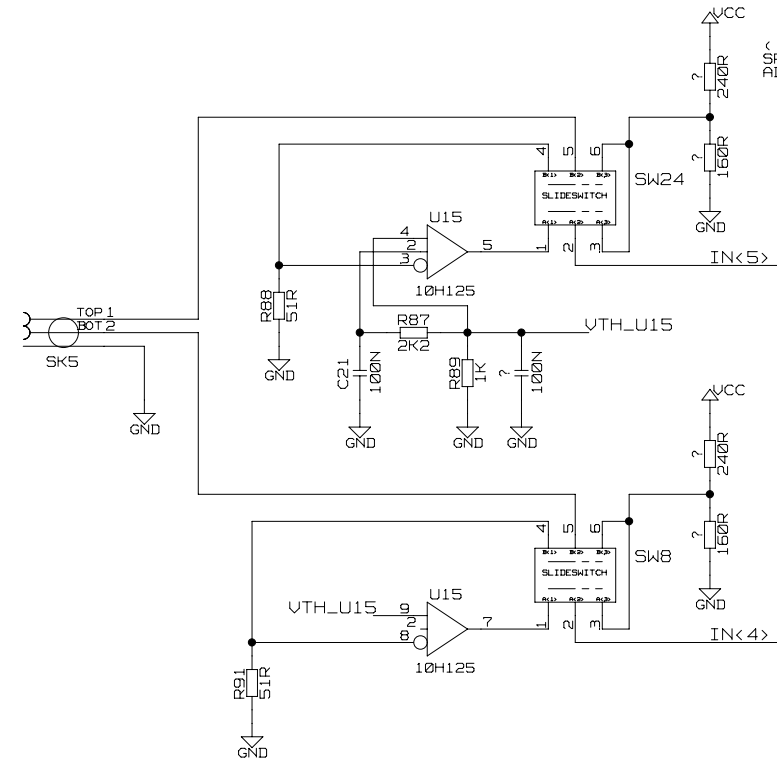
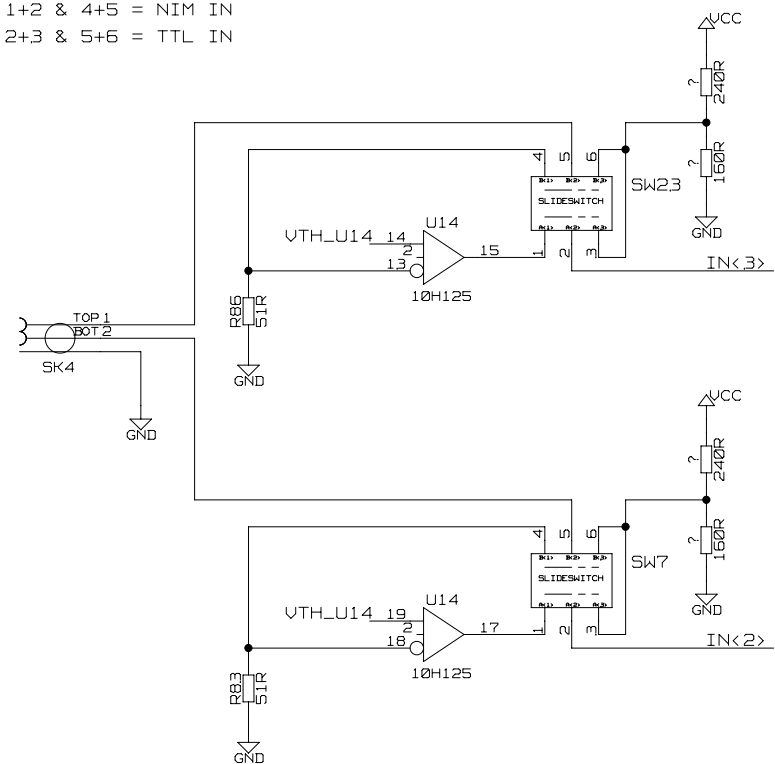
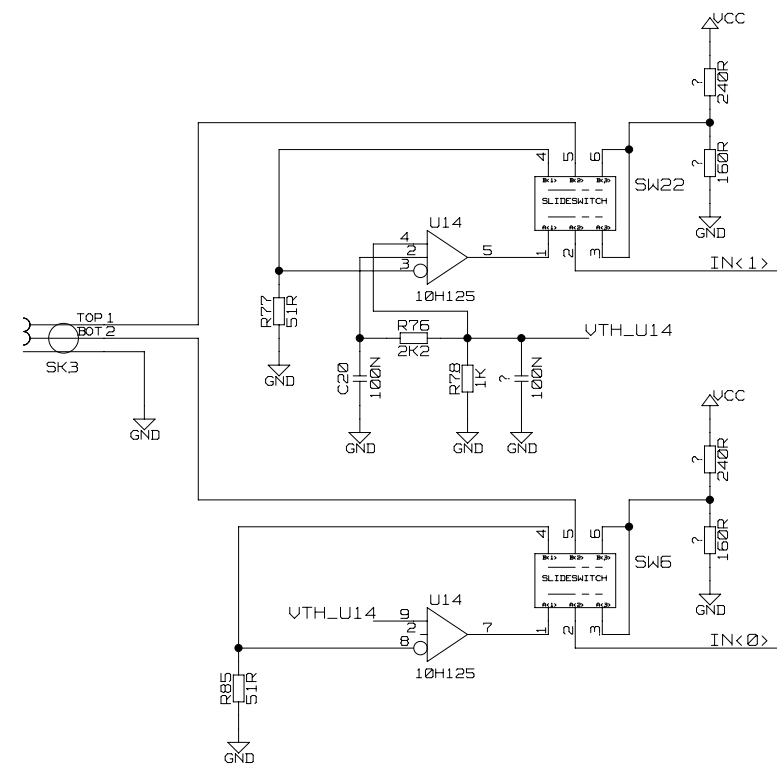
D

C

B

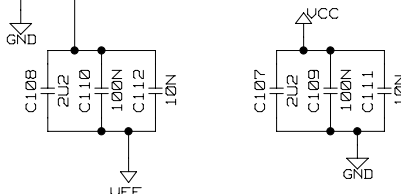
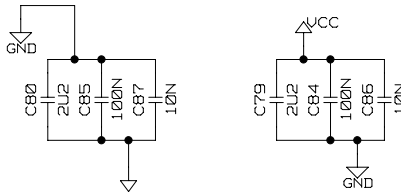
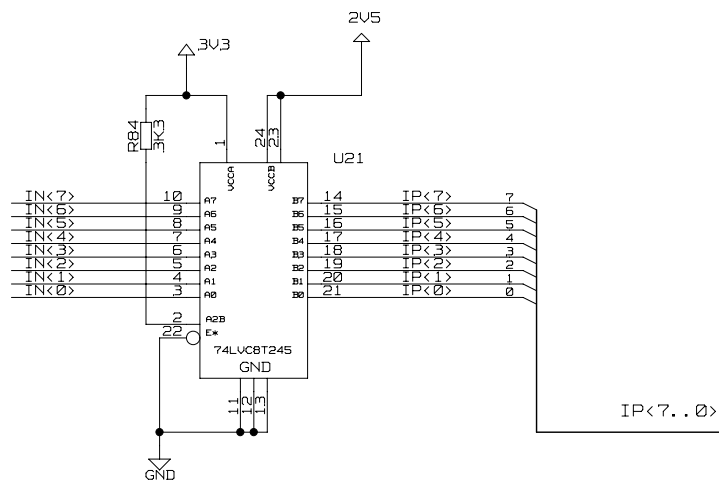
A

SWITCHES SW5 - SW12 SELECT
1+2 & 4+5 = NIM IN
2+3 & 5+6 = TTL IN



(CORRECTION -
SPLIT TERMINATORS
ADDED)

VALUES ARE FOR 100R
USE 120 / 91 FOR 50R

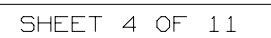


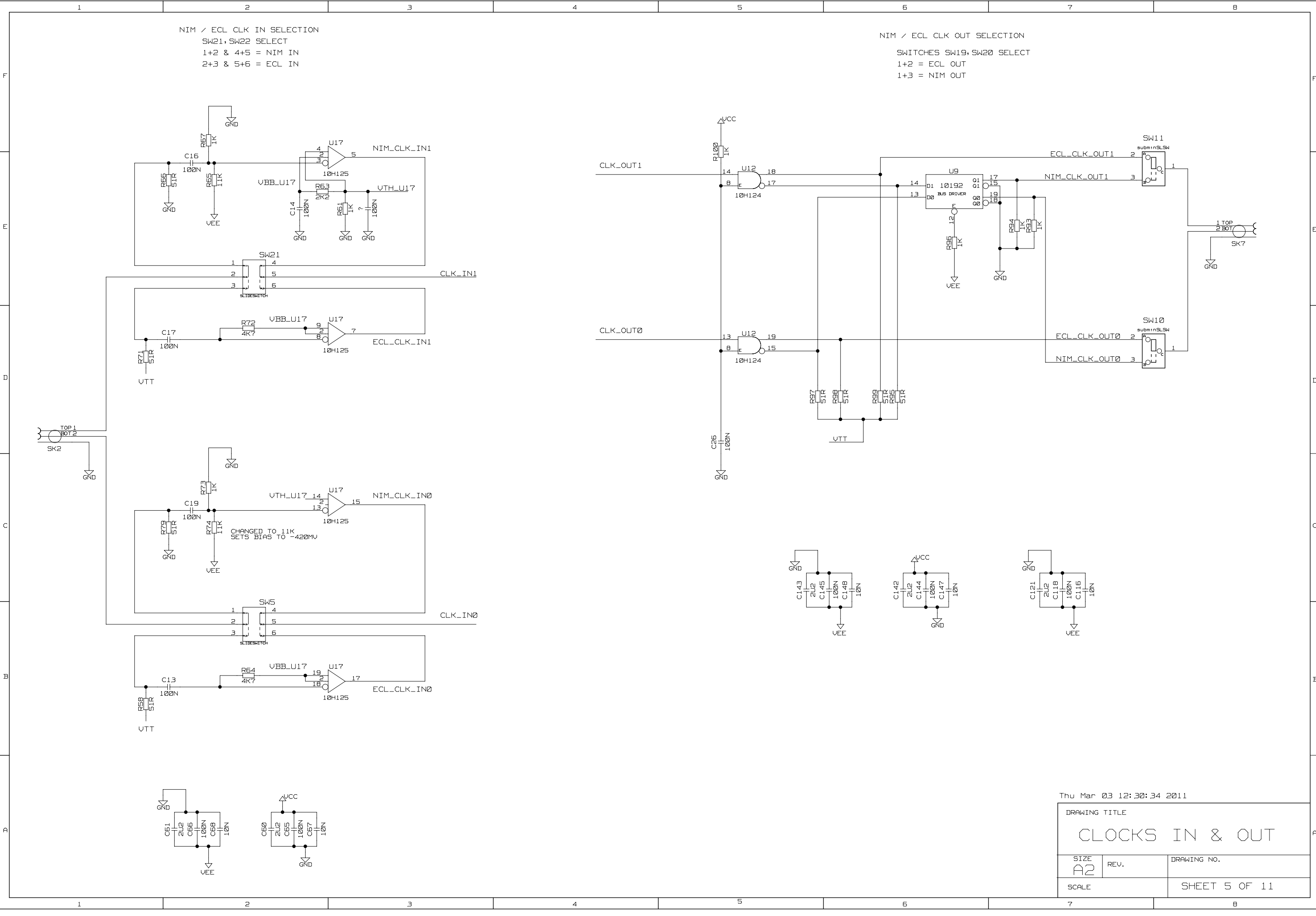
Thu Mar 03 12:41:01 2011

DRAWING TITLE
NIM / TTL INPUTS

SIZE A2	REV.	DRAWING NO.
SCALE		SHEET 3 OF 11

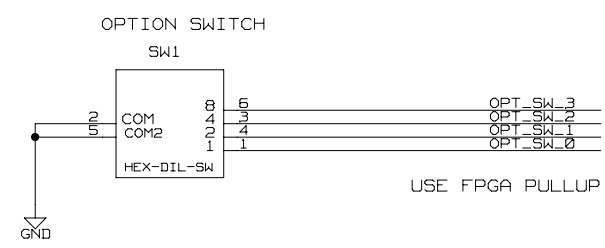
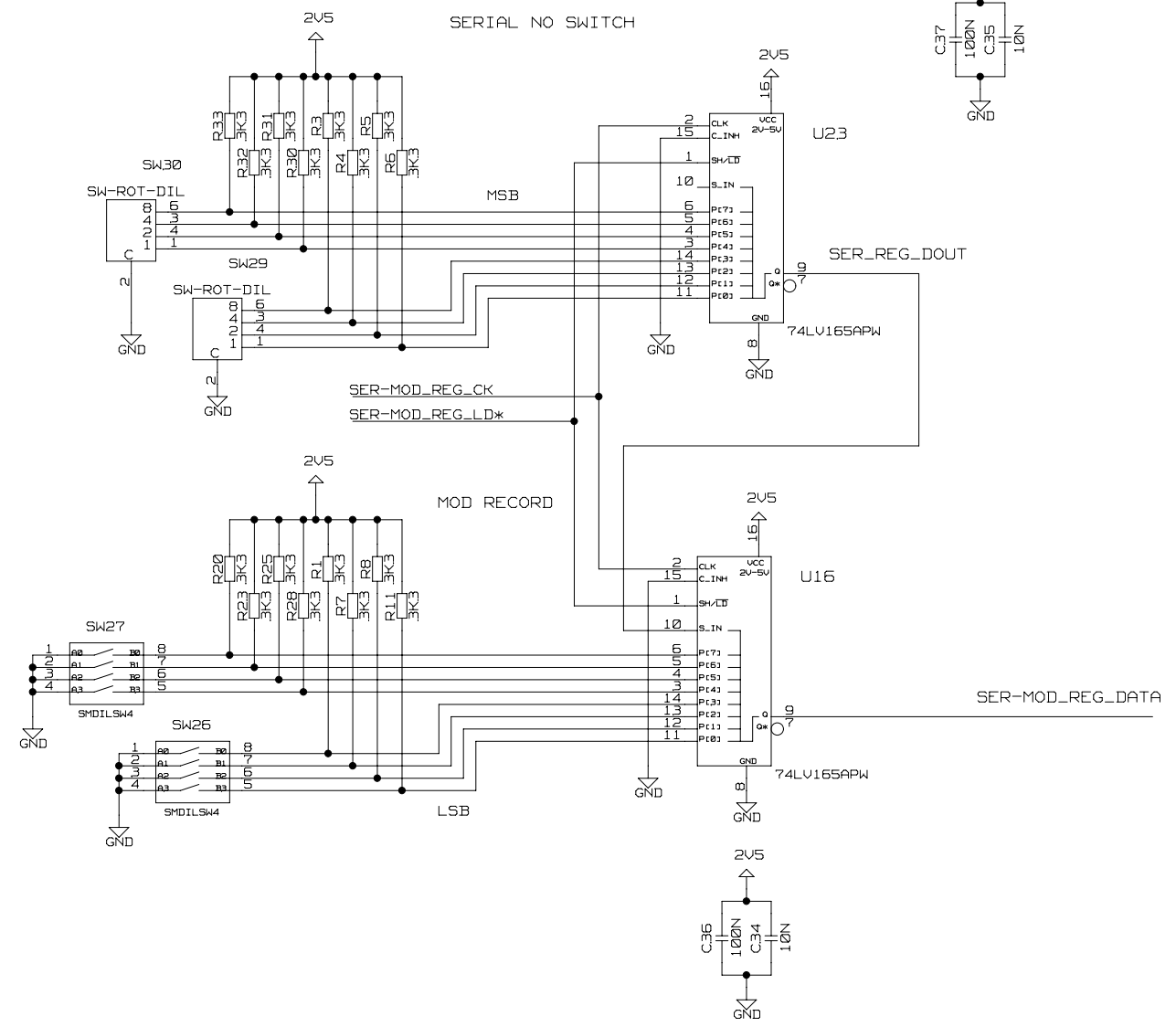
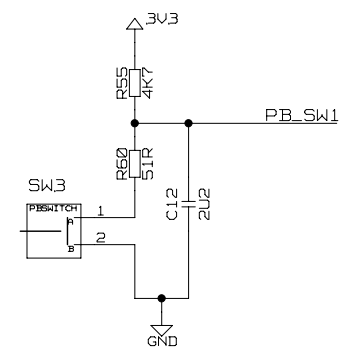
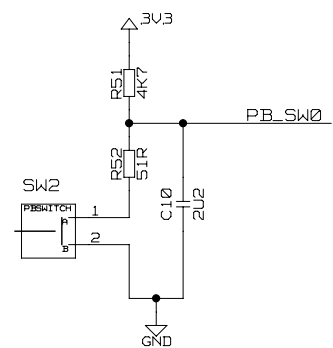
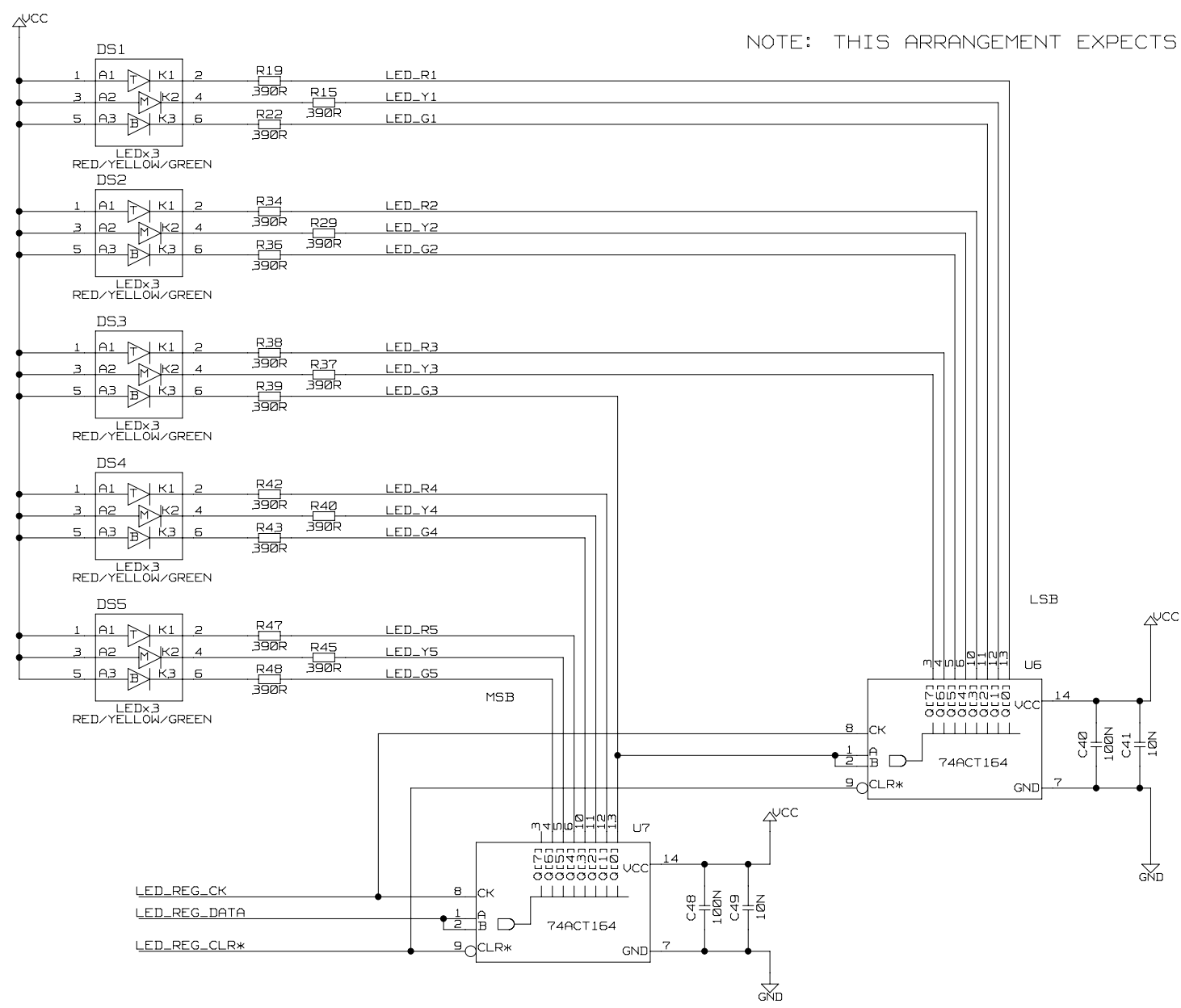
12345678





Thu Mar 03 12:30:34 2011		
DRAWING TITLE		
CLOCKS IN & OUT		
SIZE A2	REV.	DRAWING NO.
SCALE	SHEET 5 OF 11	

NOTE: THIS ARRANGEMENT EXPECTS LSB SHIFTED FIRST

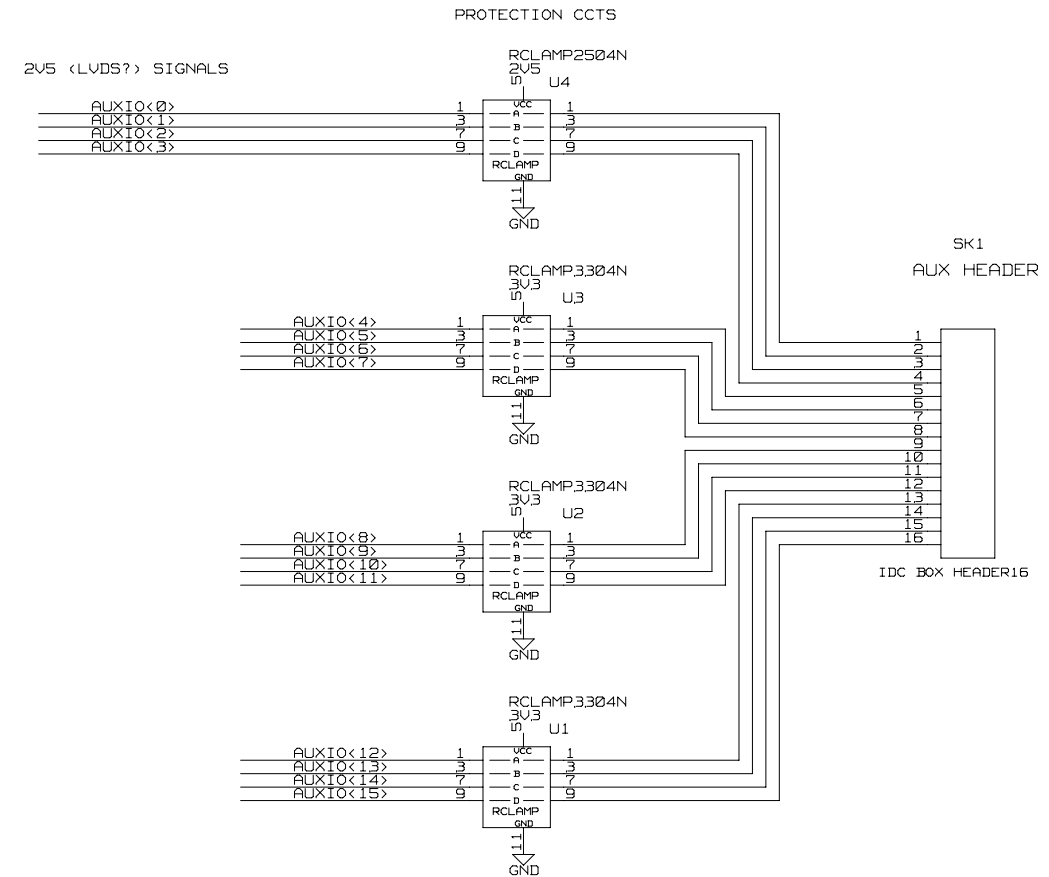
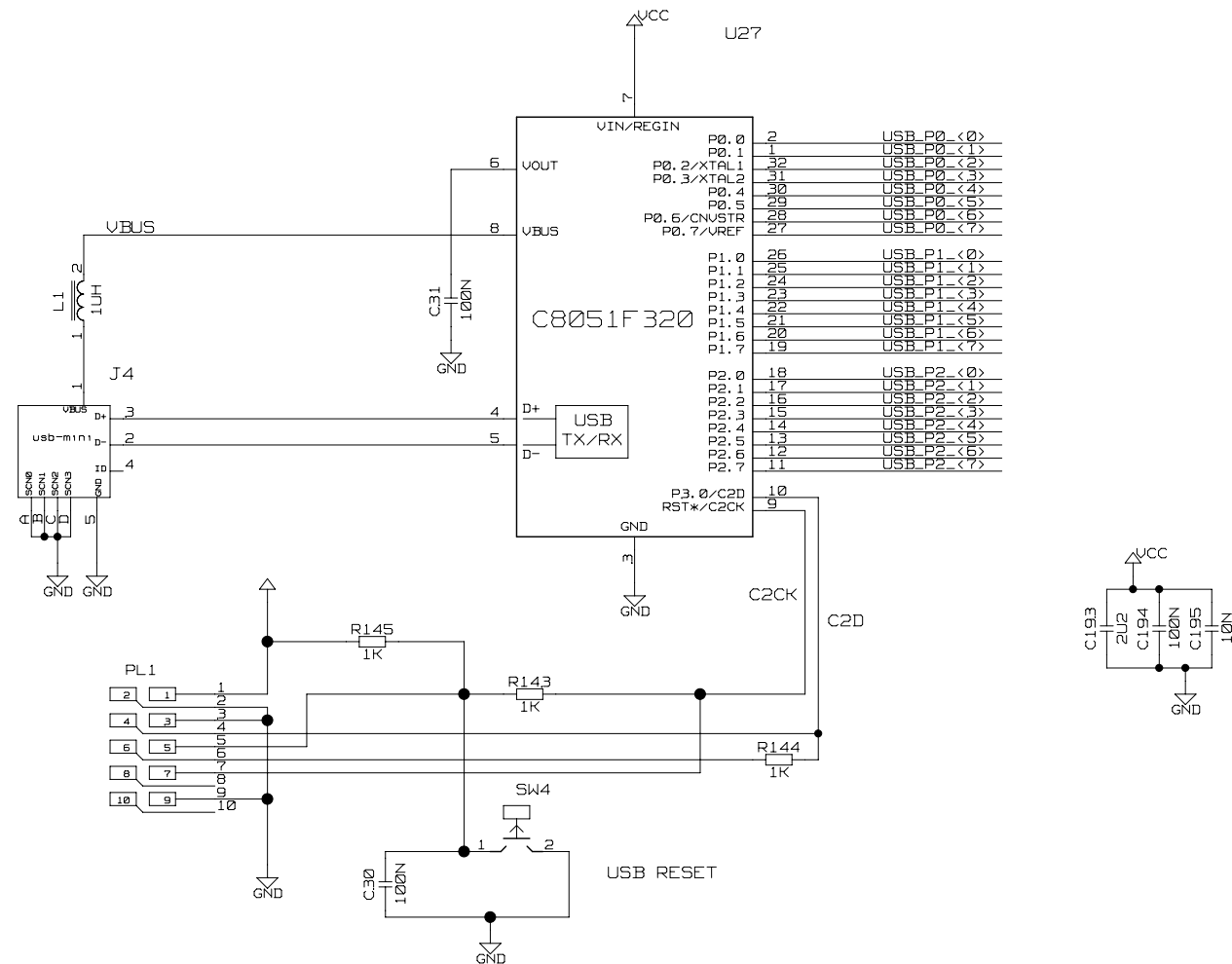
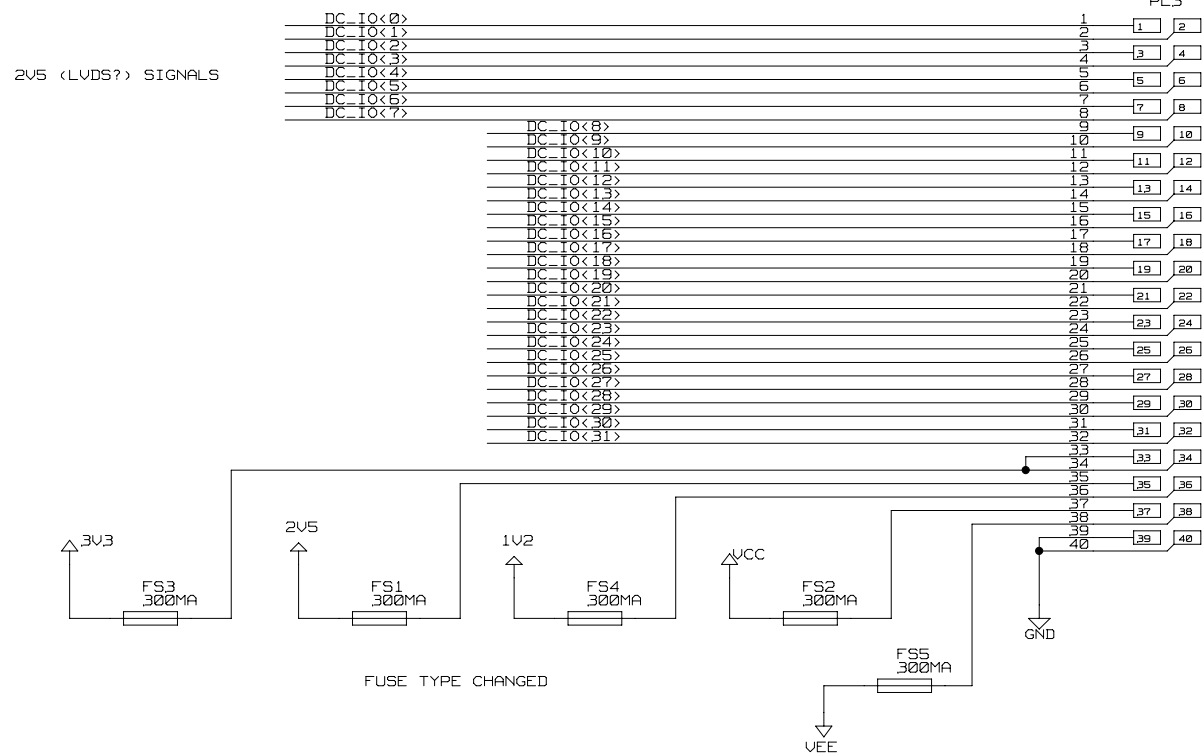


Thu Mar 03 13:23:27 2011

DRAWING TITLE		
FRONT PANEL - 1		
SIZE A2	REV.	DRAWING NO.
SCALE		SHEET 6 OF 11

THIS IS A 40 PIN DIL UNSHROUDED HEADER SUITABLE FOR A RIBBON CABLE CONNECTOR
OR STRIAIGHT TO A DAUGHTER CARD

DAUGHTER CARD I/O



Fri Mar 04 10:49:45 2011

DRAWING TITLE

FRONT PANEL - 2

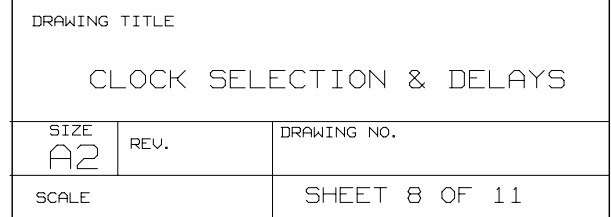
SIZE
A2

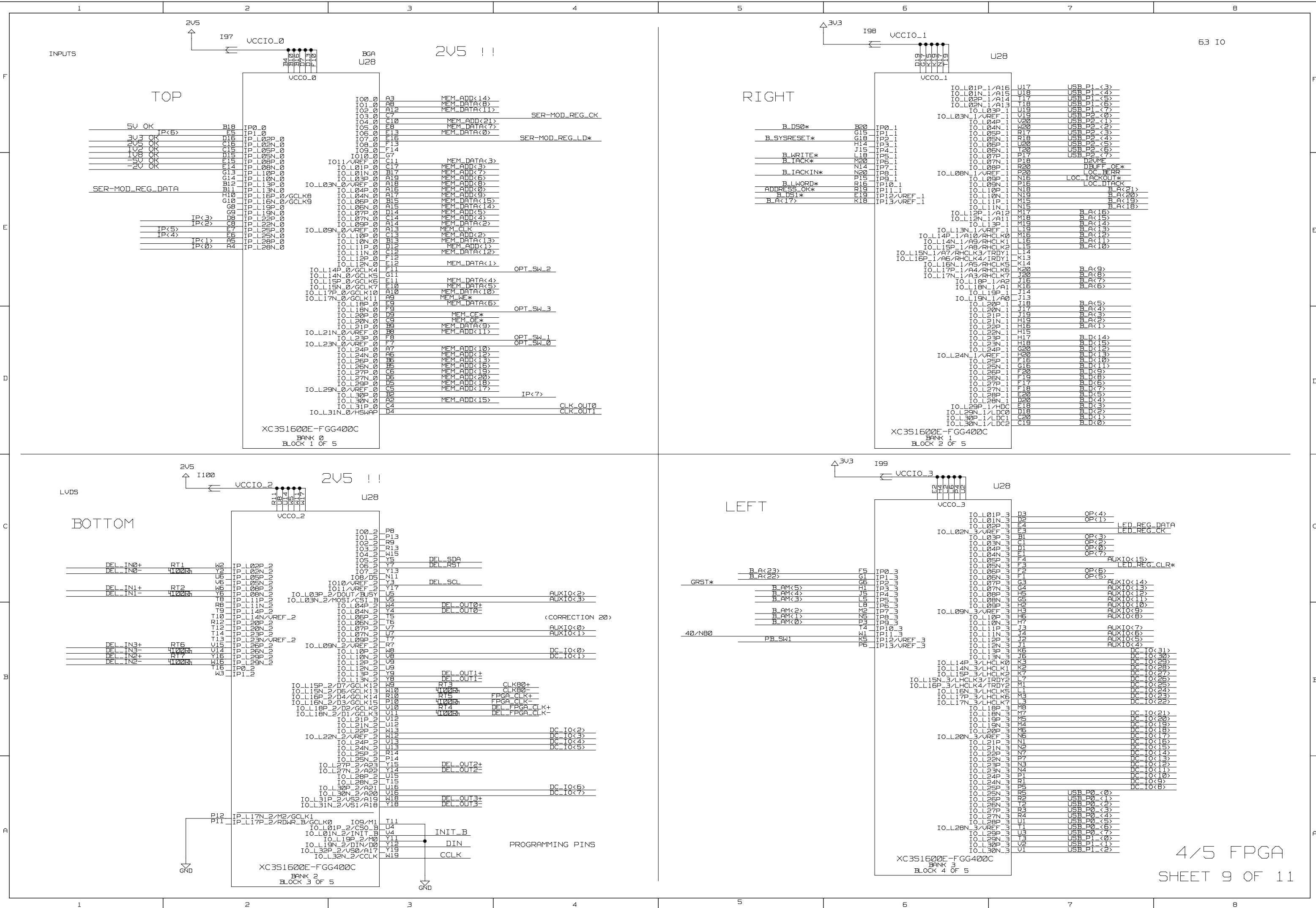
REV.

DRAWING NO.

SCALE

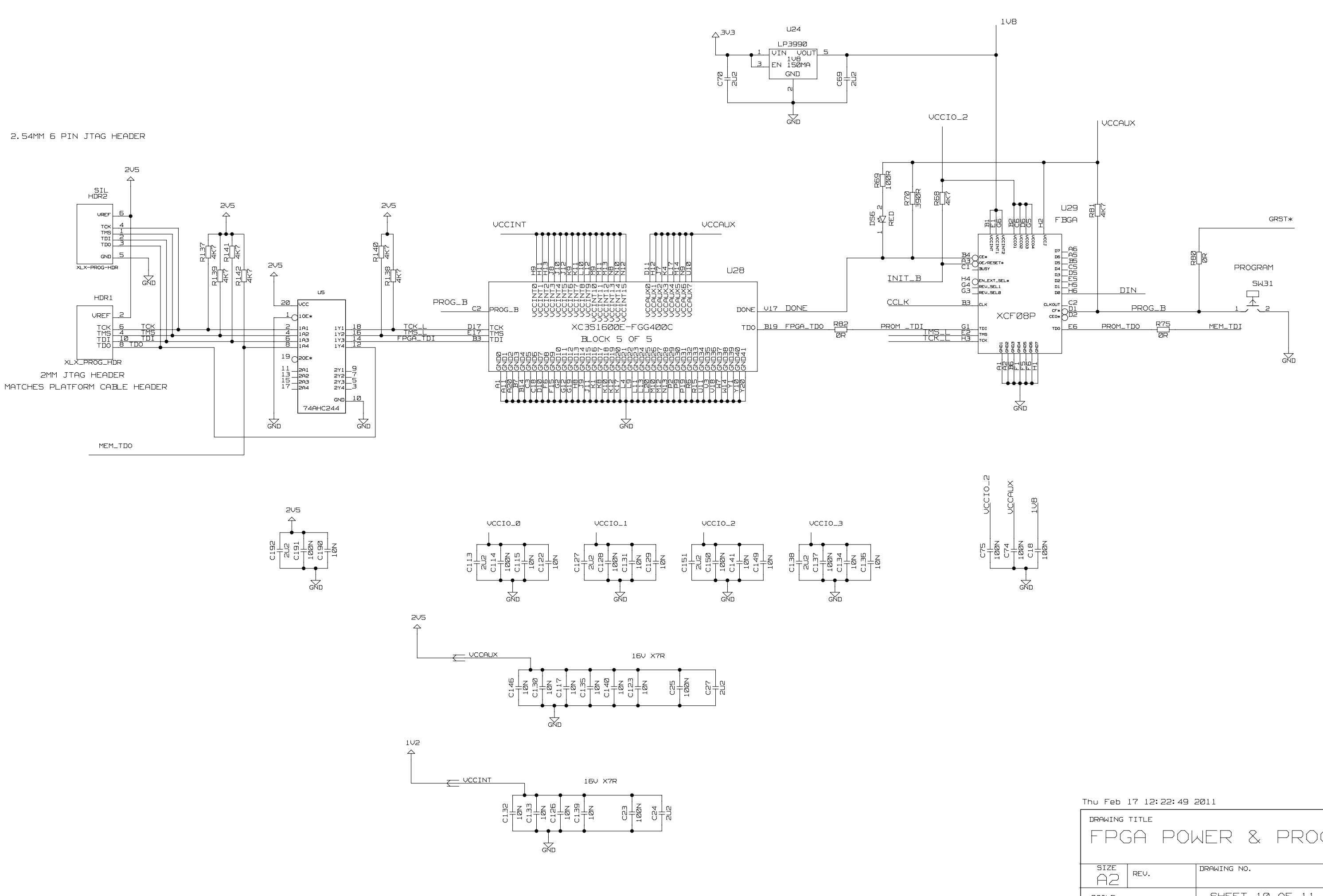
SHEET 7 OF 11



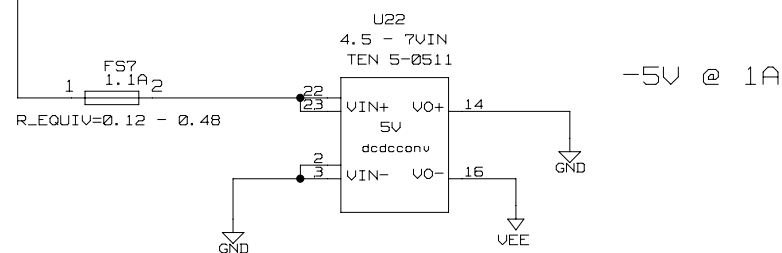
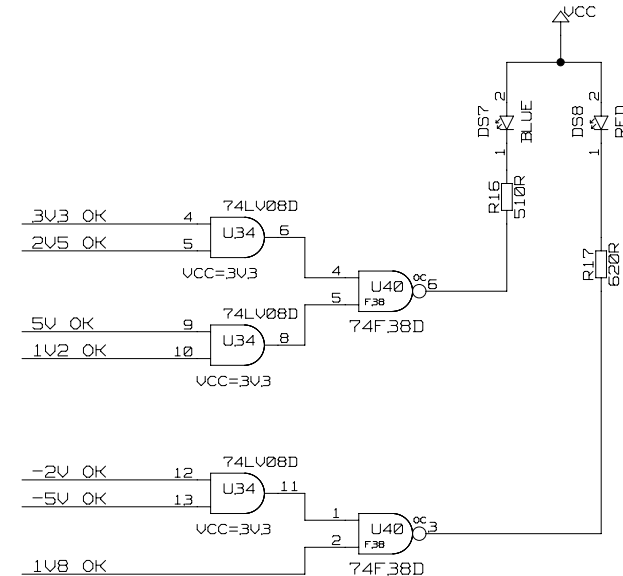
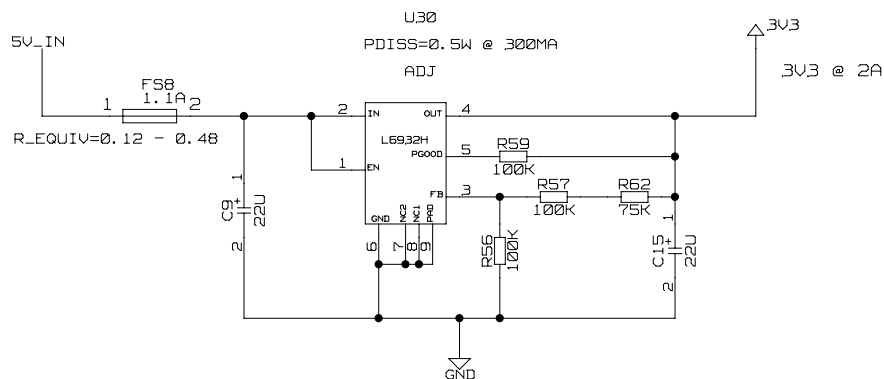
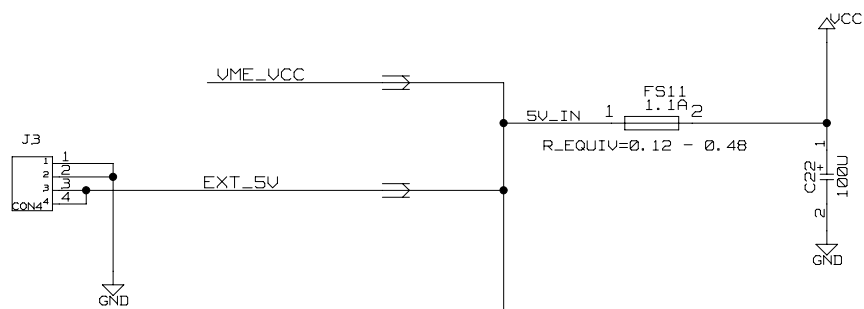


2.54MM 6 PIN JTAG HEADER

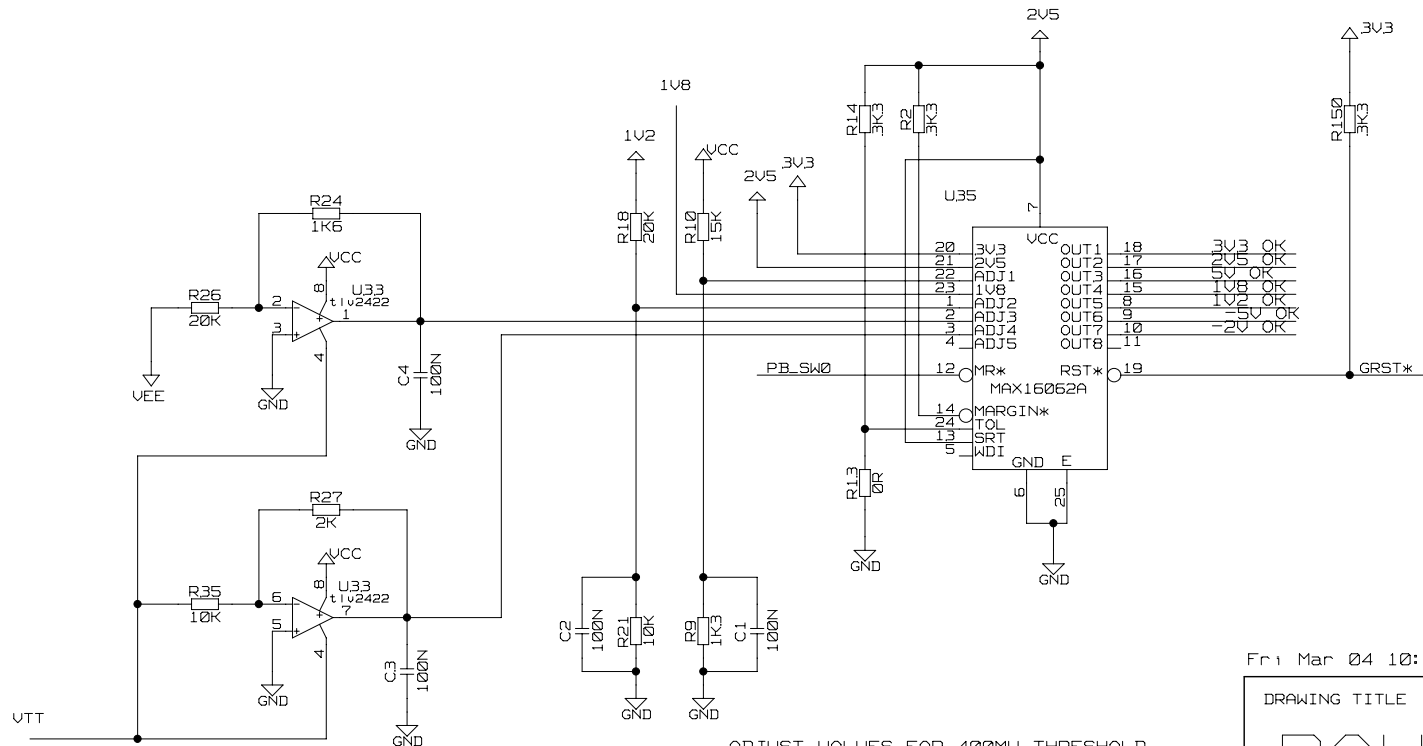
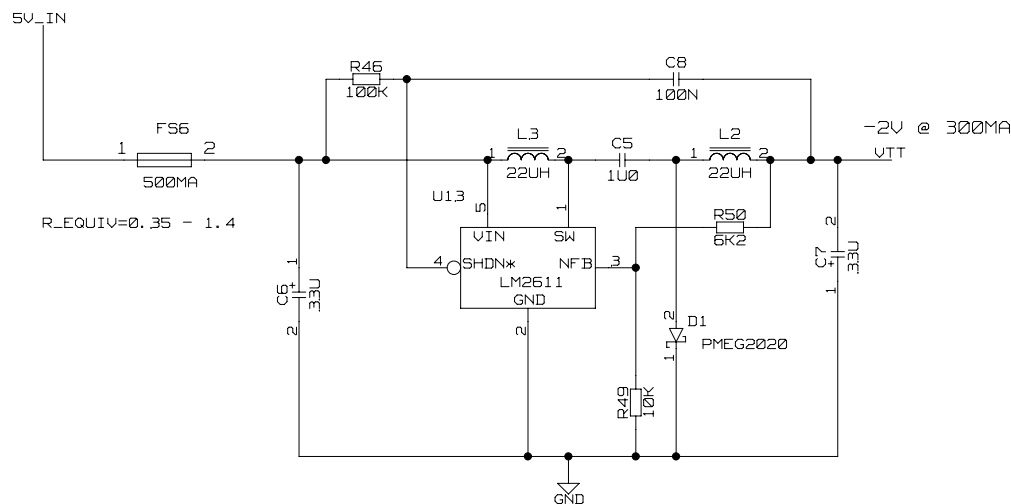
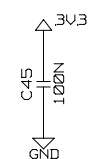
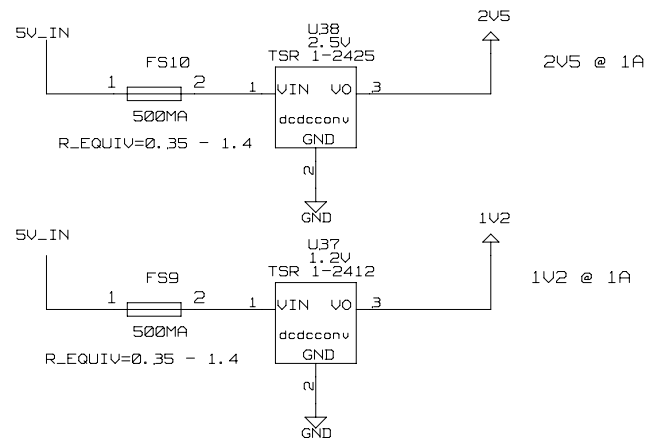
2MM JTAG HEADER
MATCHES PLATFORM CABLE HEADER



Thu Feb 17 12:22:49 2011		
DRAWING TITLE		
FPGA POWER & PROG		
SIZE	REV.	DRAWING NO.
A2		
SCALE	SHEET 10 OF 11	



FUSE TYPE CHANGED



ADJUST VALUES FOR 400MV THRESHOLD
TOL: GND = 5%, VCC = 10%

Fri Mar 04 10:51:48 2011		
DRAWING TITLE		
POWER		
SIZE A2	REV.	DRAWING NO.
SCALE		SHEET 11 OF 11