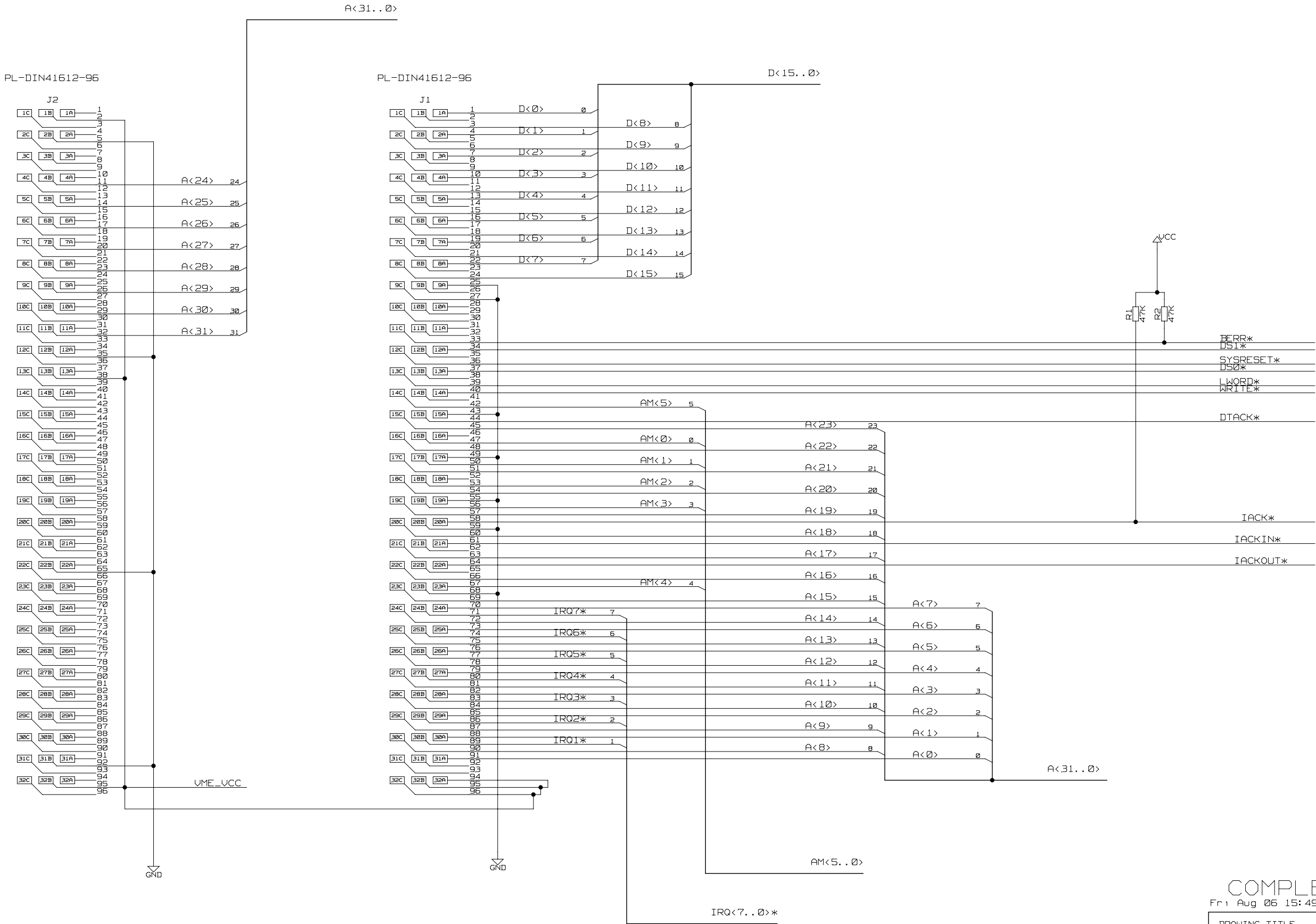
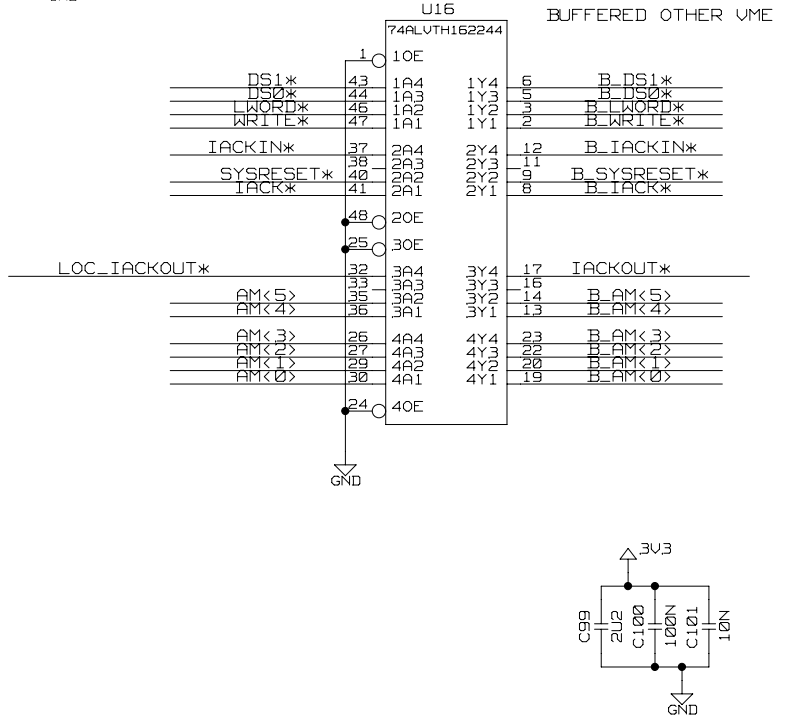
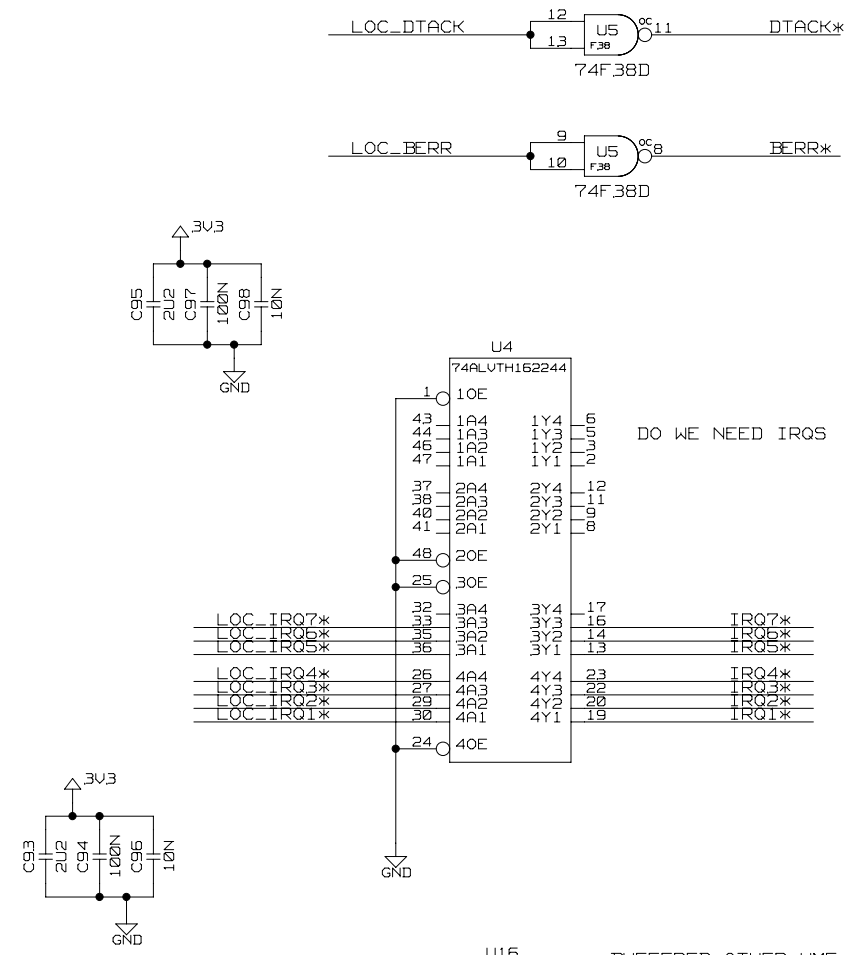
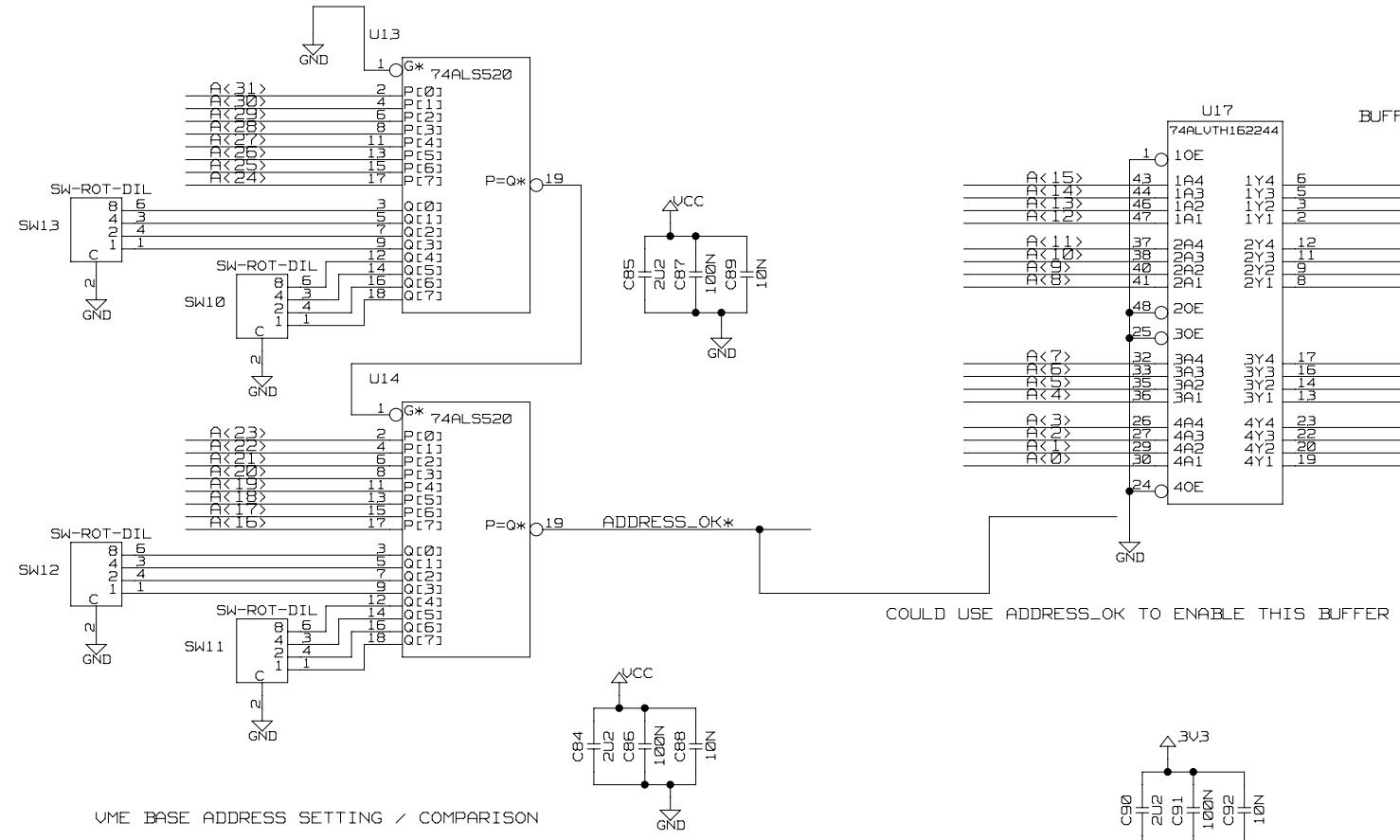
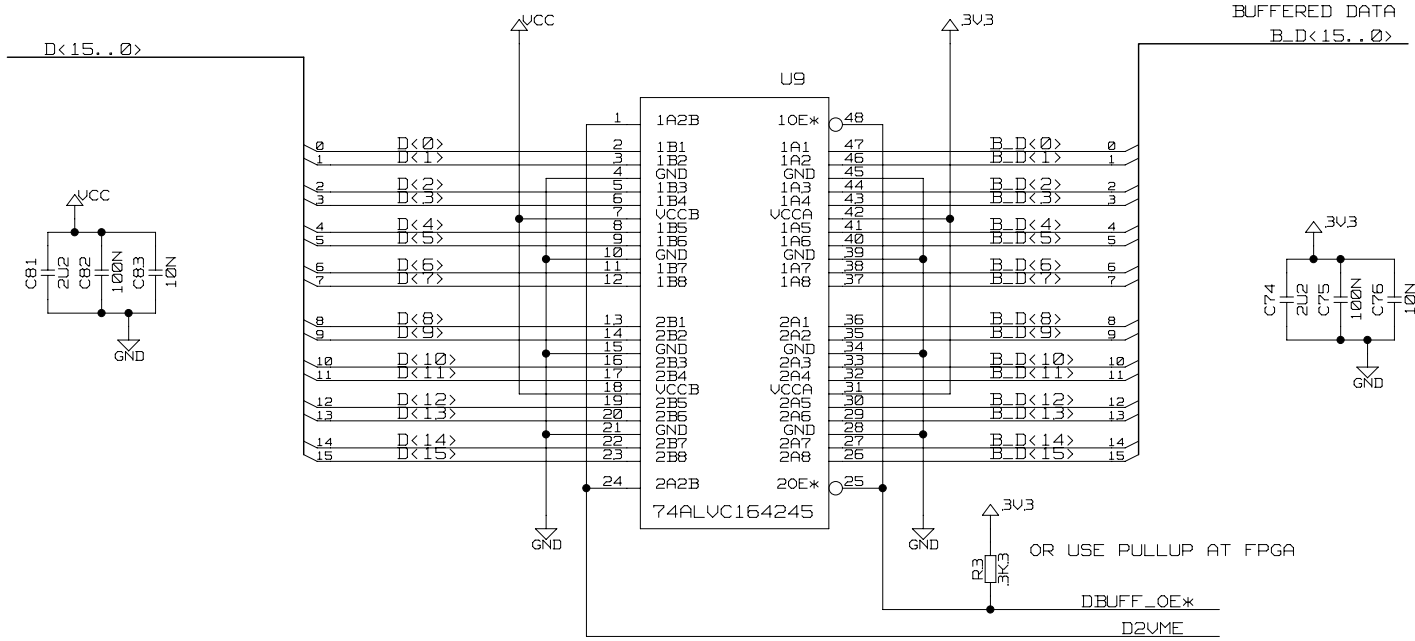


ALL SIGNALS GO TO PAGE 2



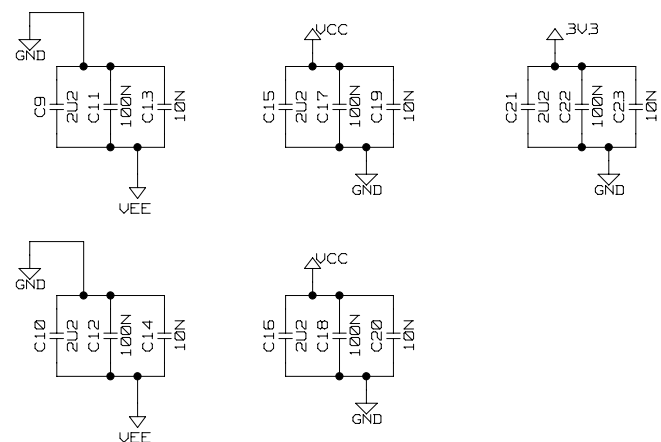
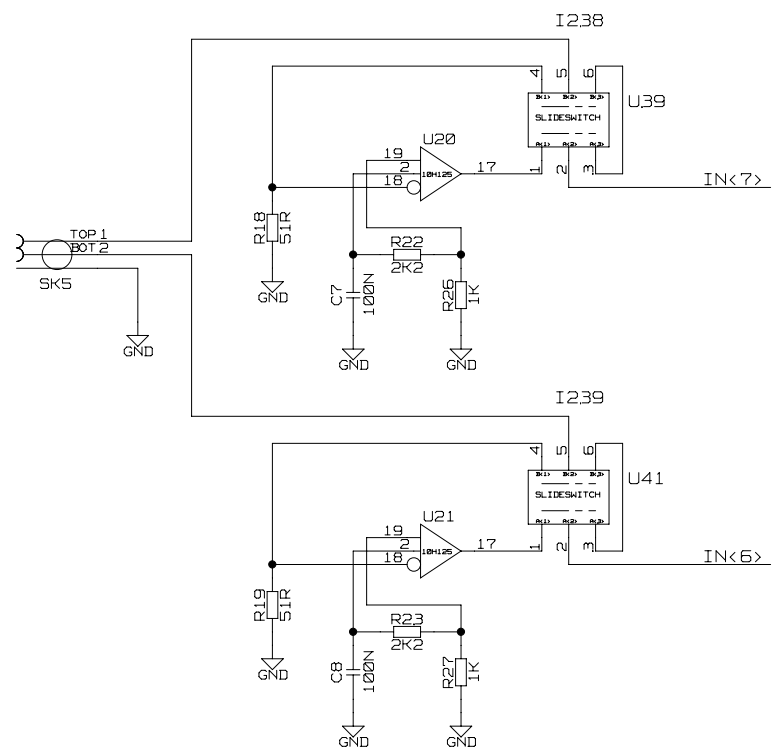
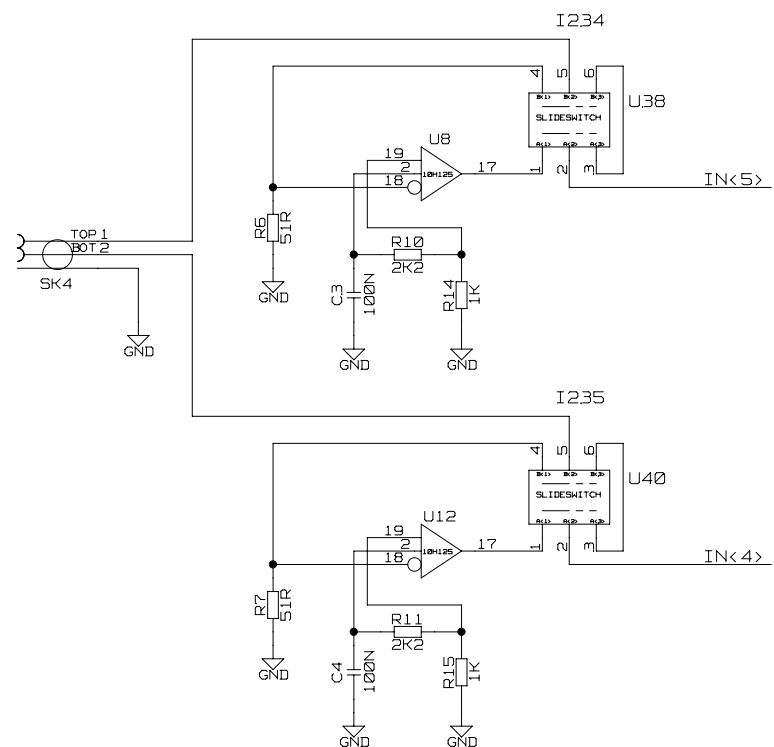
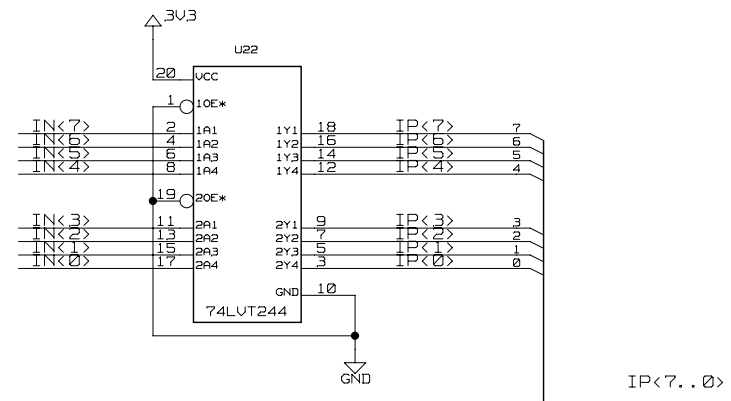
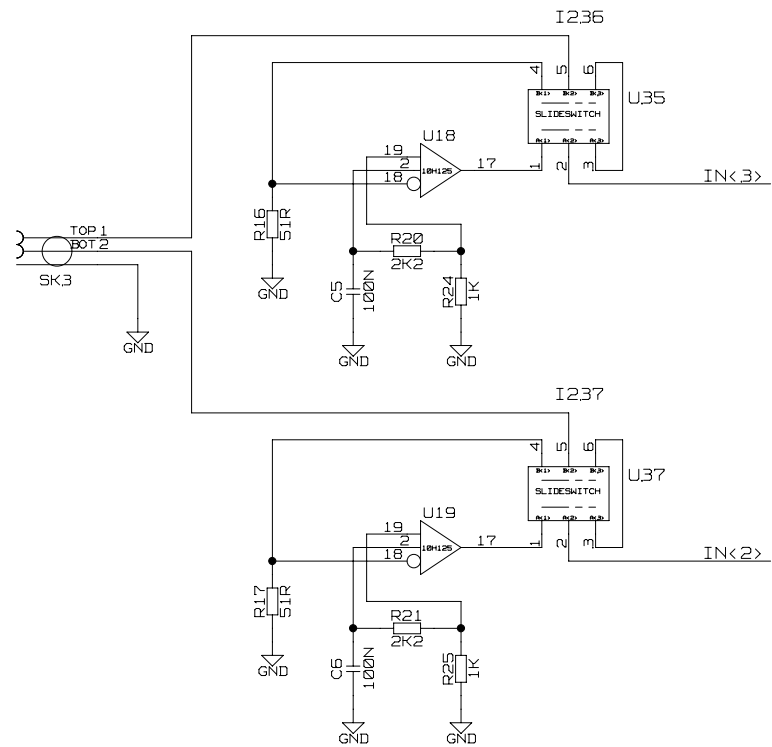
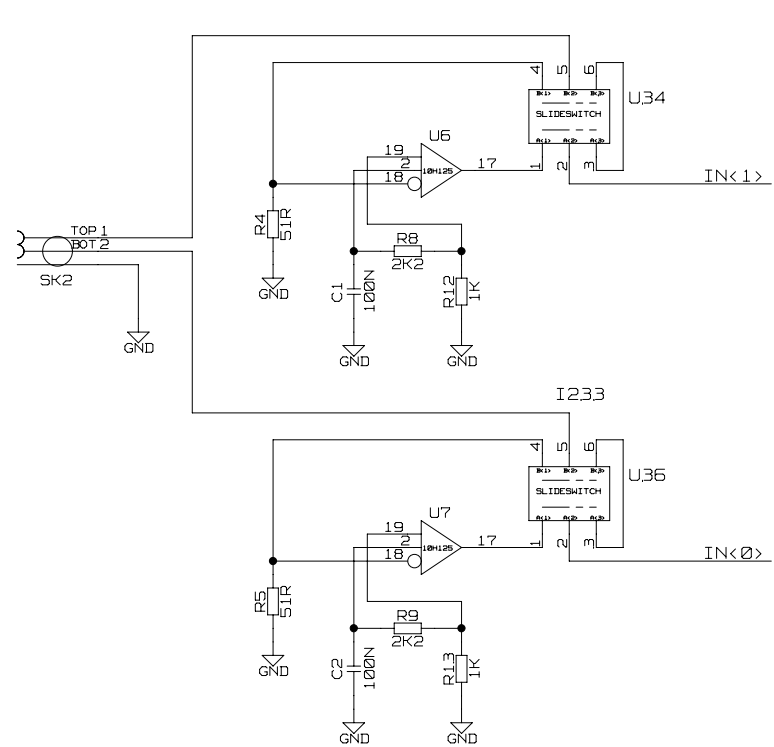
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SCALE		SHEET 1 OF 10



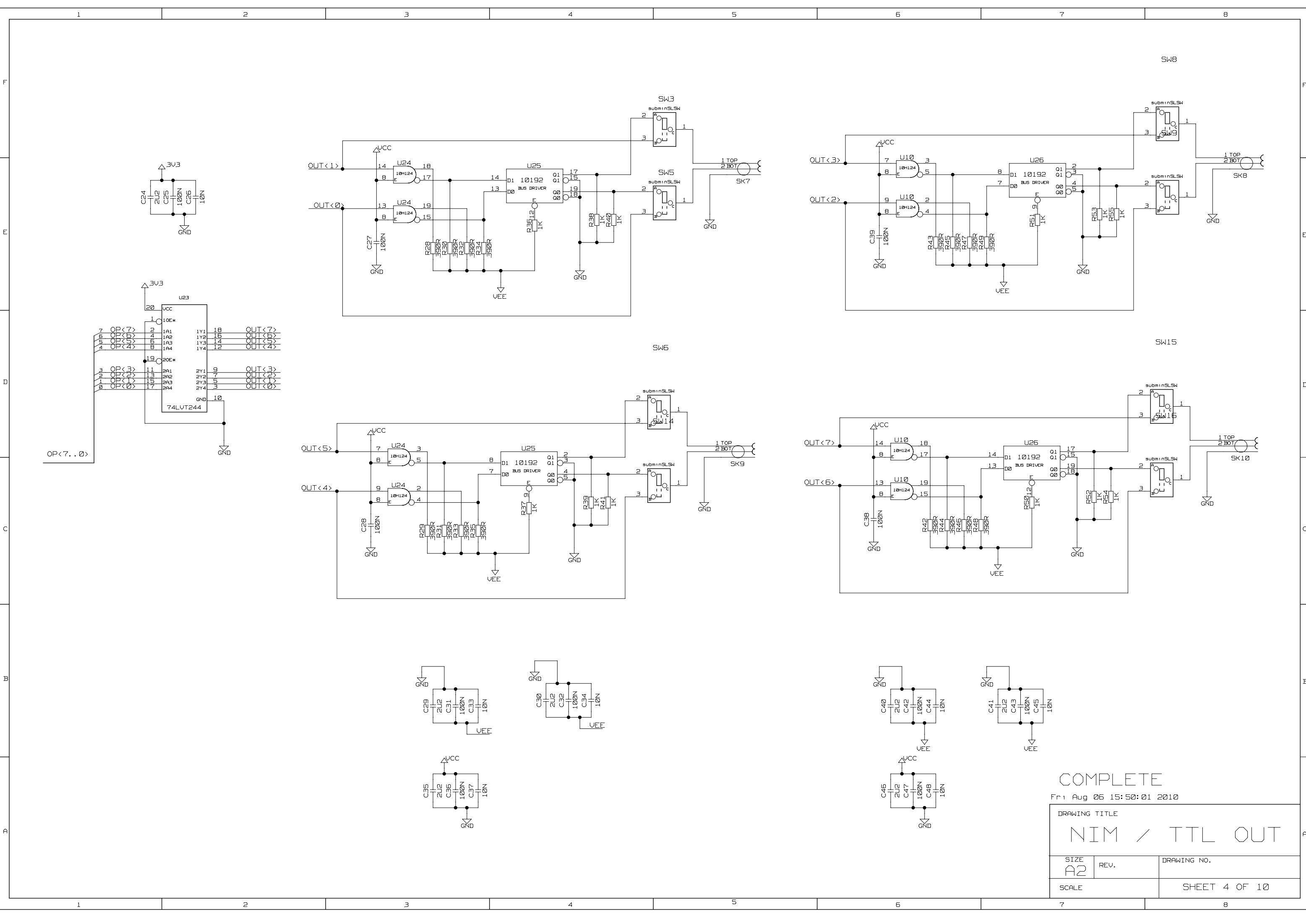
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UME BUFFERING		
SIZE A2	REV.	DRAWING NO.
SCALE		SHEET 2 OF 10



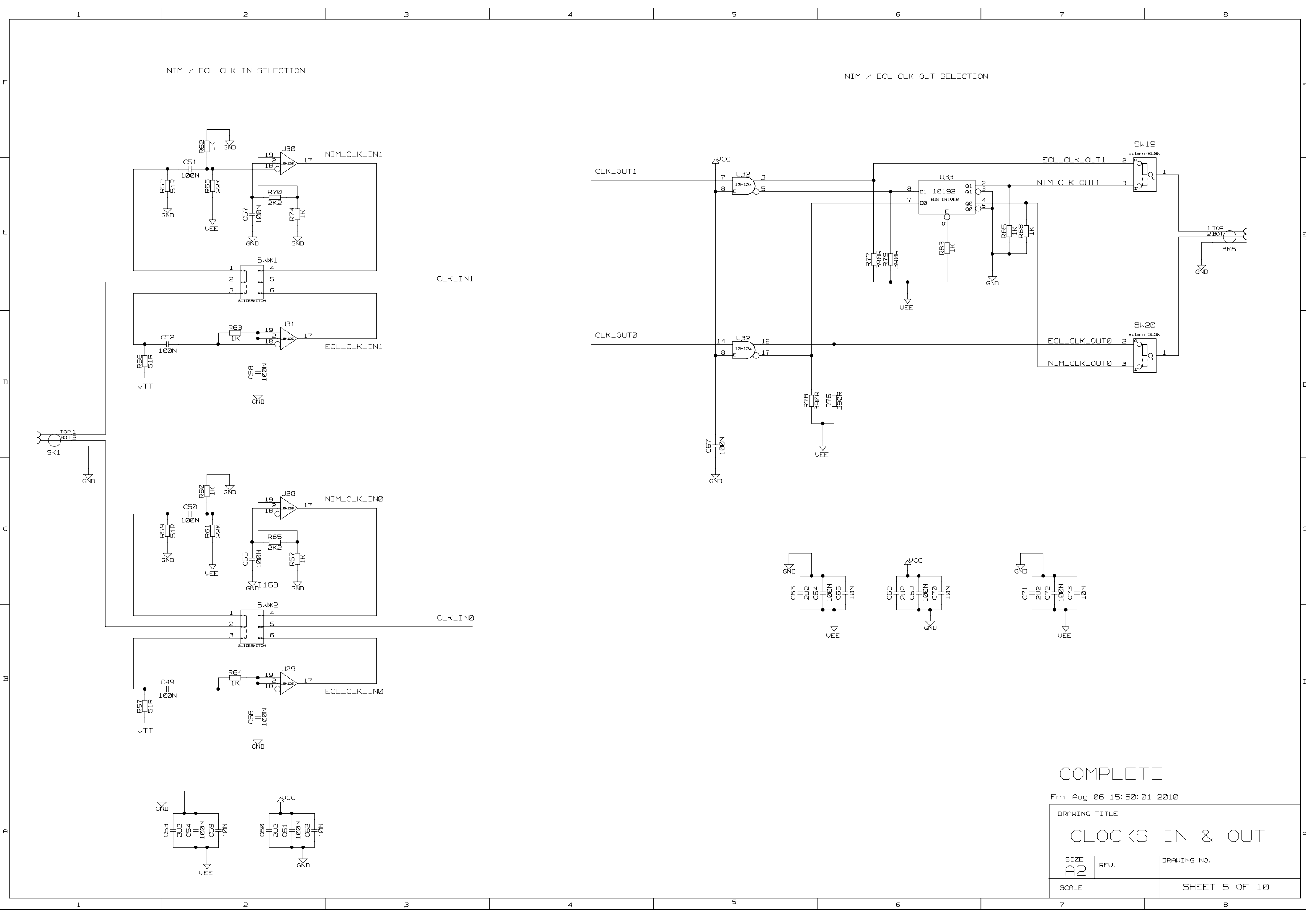
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A2		
SCALE		SHEET 3 OF 10

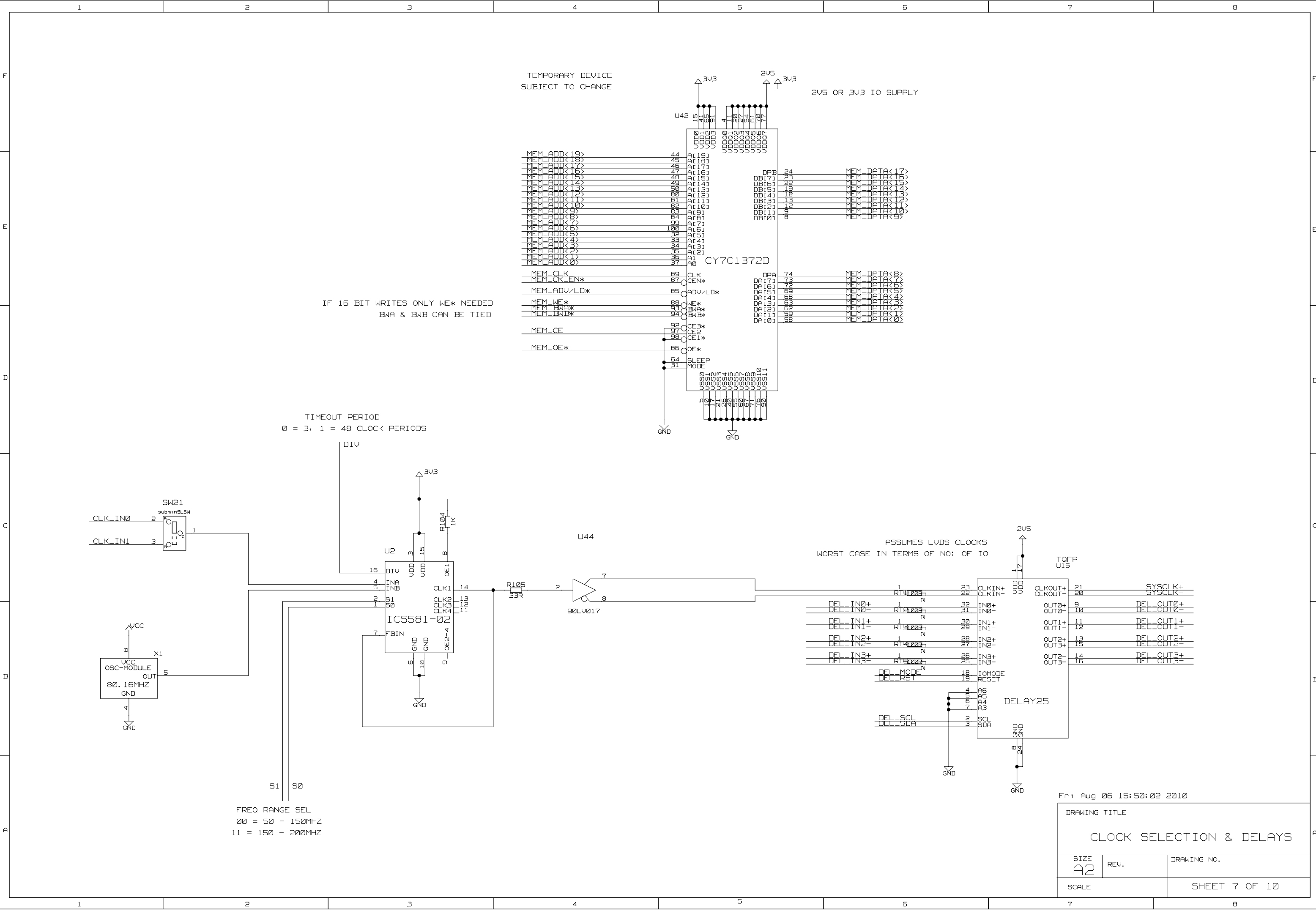


COMPLETE

Fri Aug 06 15:50:01 2010

DRAWING TITLE		
NIM / TTL OUT		
SIZE	REV.	DRAWING NO.
A2		
SCALE		SHEET 4 OF 10





TEMPORARY DEVICE
SUBJECT TO CHANGE

2V5 OR 3V3 IO SUPPLY

IF 16 BIT WRITES ONLY WE* NEEDED
BWA & BWB CAN BE TIED

TIMEOUT PERIOD
0 = 3, 1 = 48 CLOCK PERIODS

ASSUMES LVDS CLOCKS
WORST CASE IN TERMS OF NO: OF IO

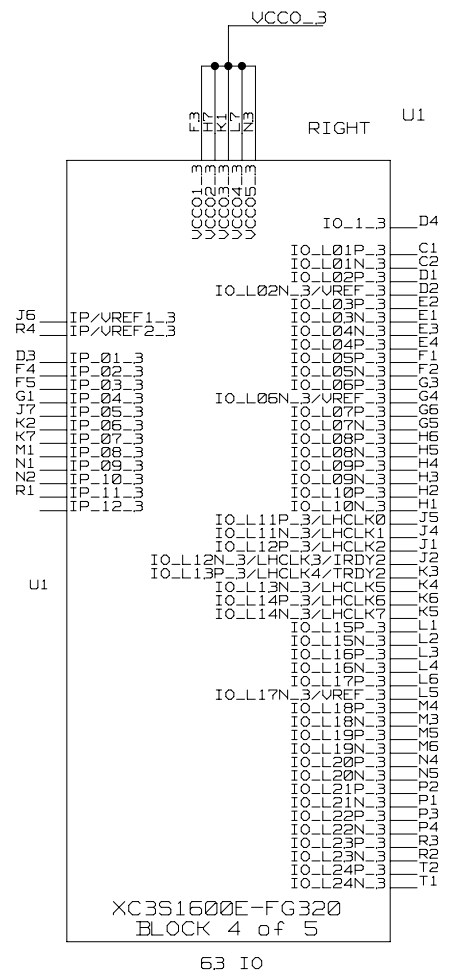
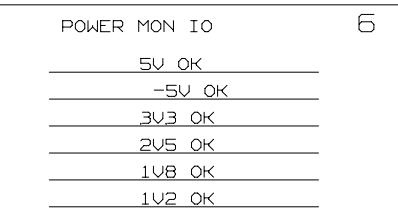
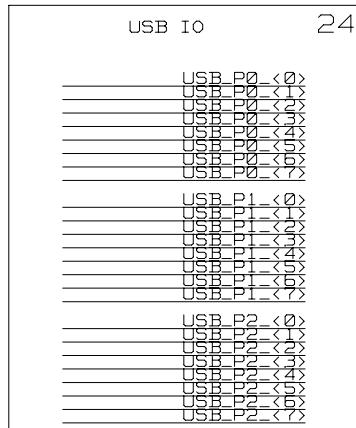
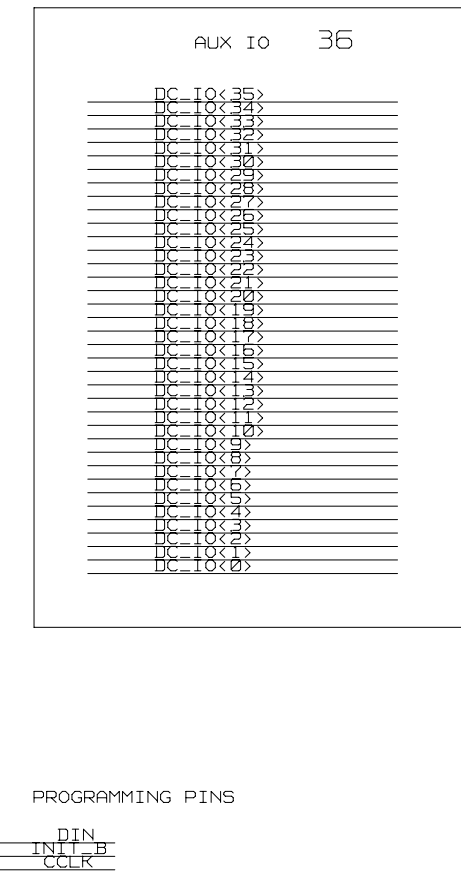
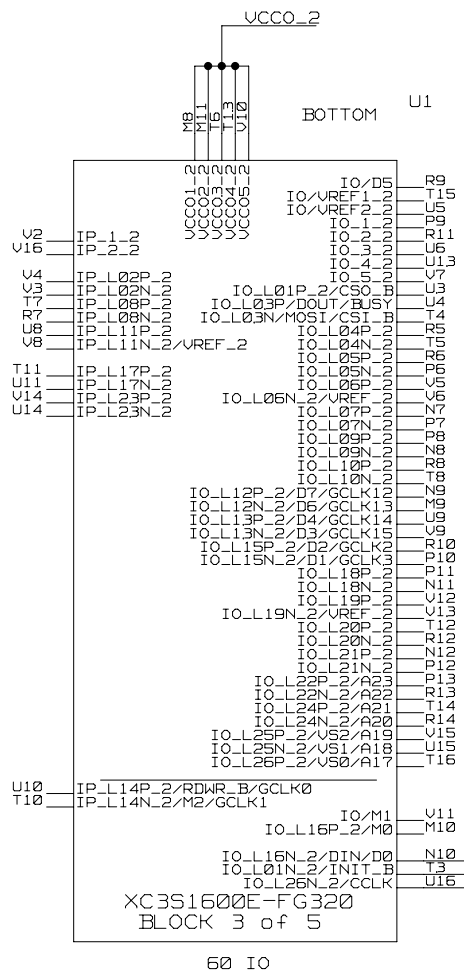
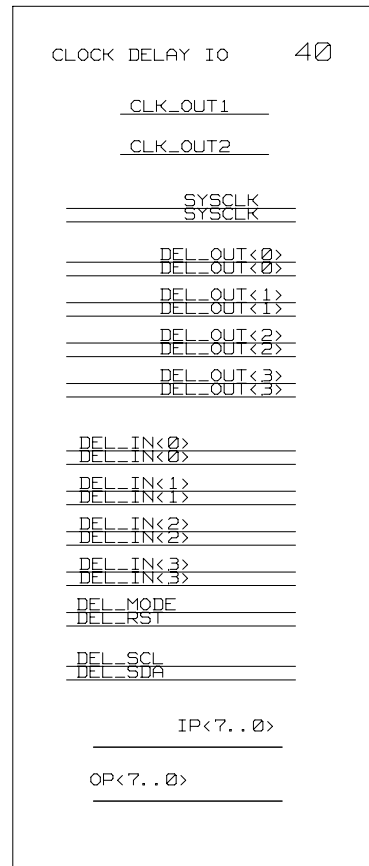
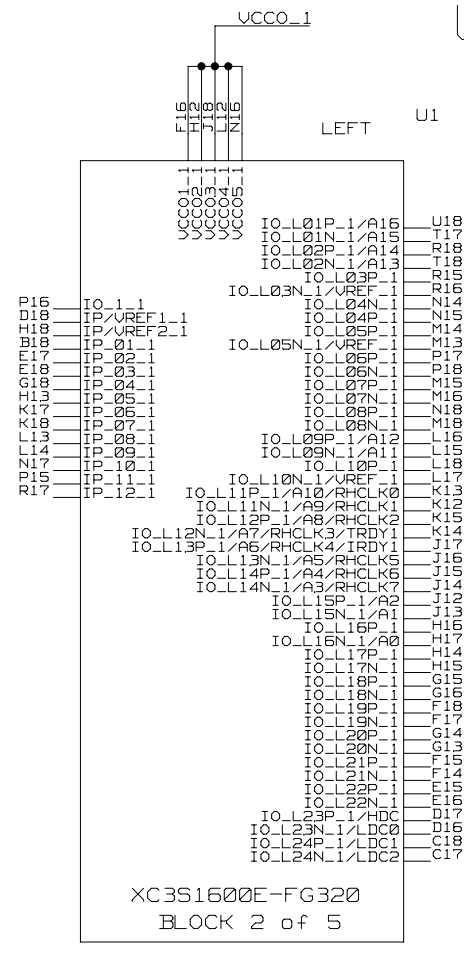
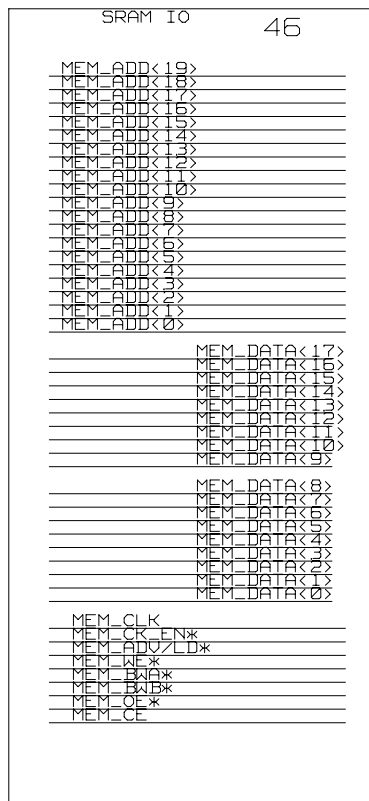
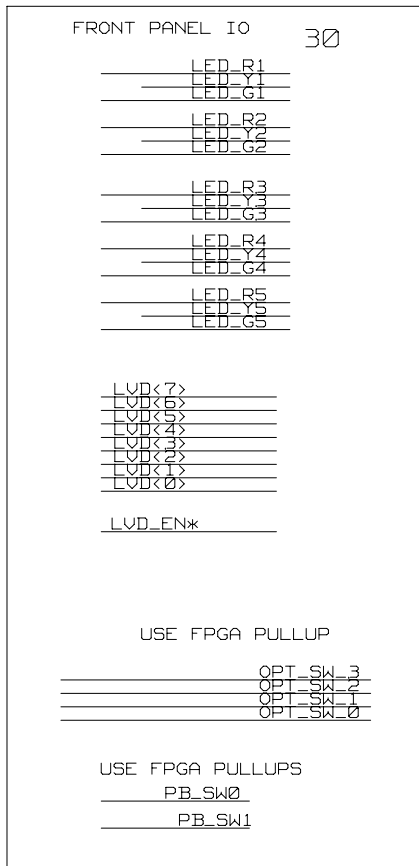
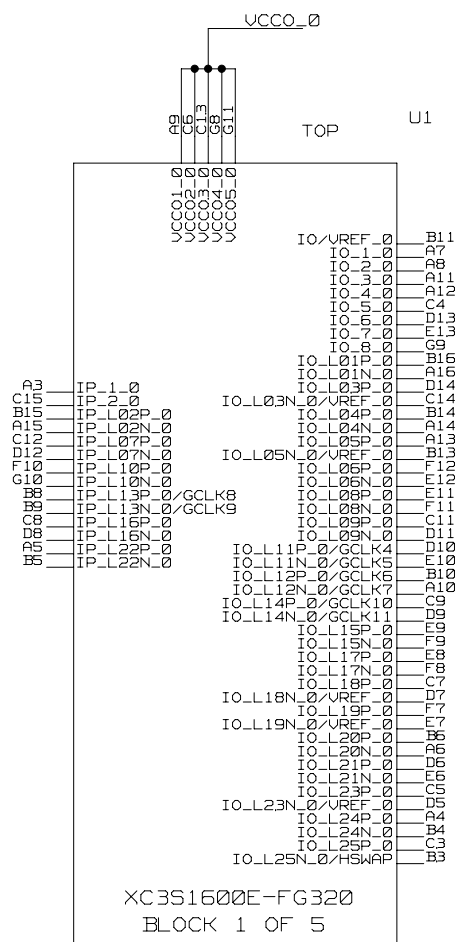
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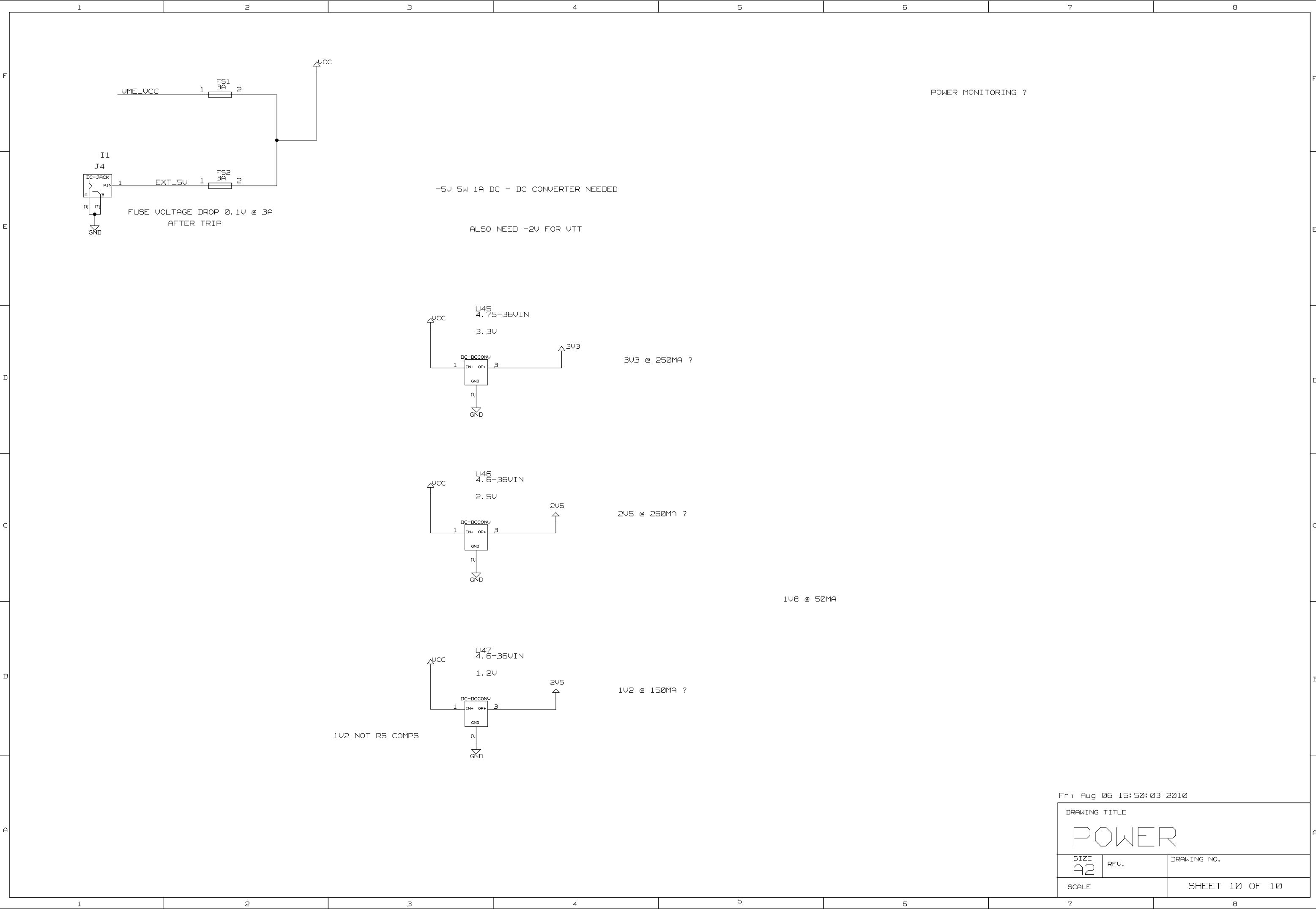
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CLOCK SELECTION & DELAYS		
SIZE A2	REV.	DRAWING NO.
SCALE		SHEET 7 OF 10

247 MAX AVAILABLE IO

USES 240 IO

THESE ARE NOT PIN
OR BANK ASSIGNMENTS
JUST A LISTING OF IOS





Fri Aug 06 15:50:03 2010		
DRAWING TITLE		
POWER		
SIZE A2	REV.	DRAWING NO.
SCALE		SHEET 10 OF 10