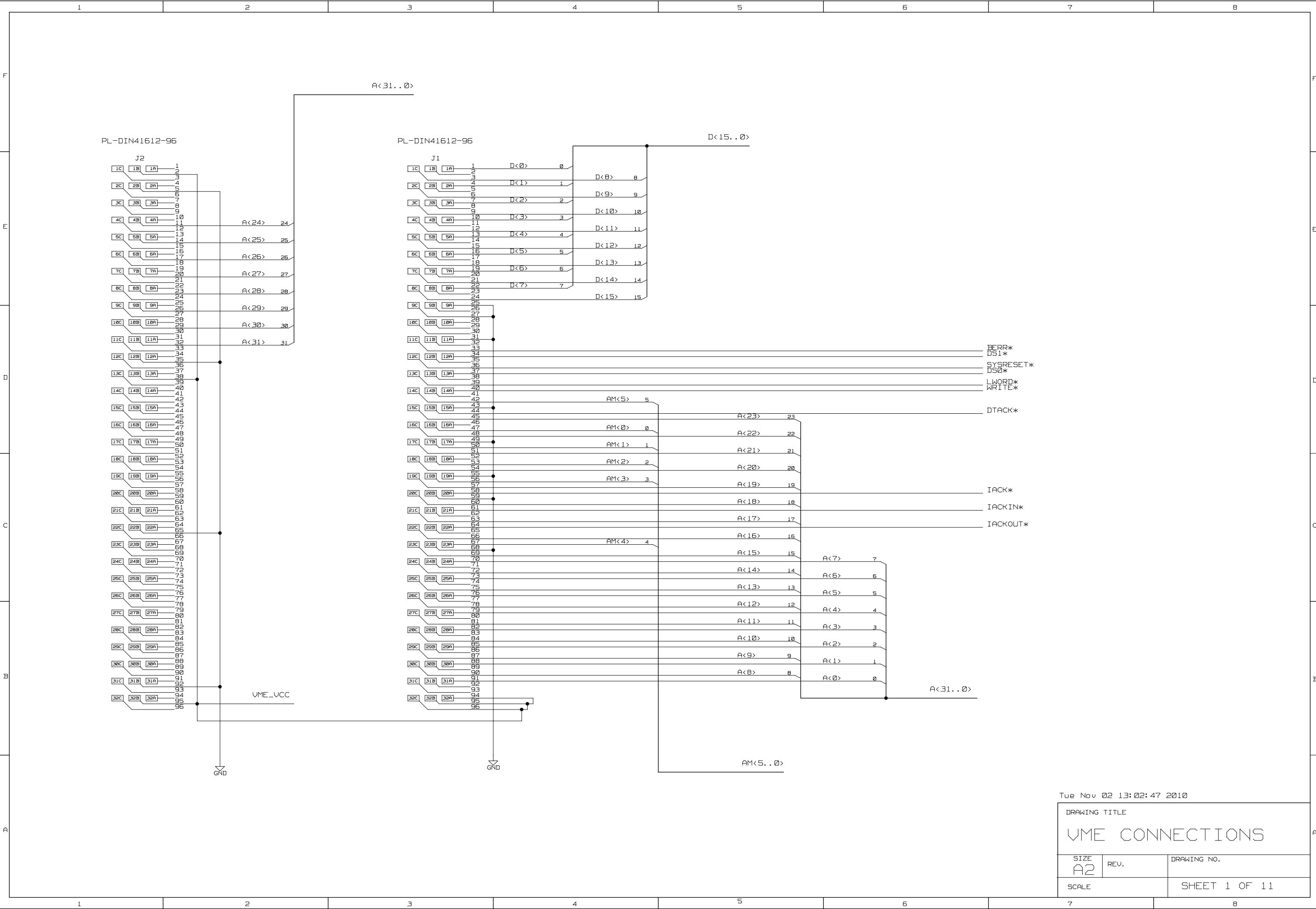
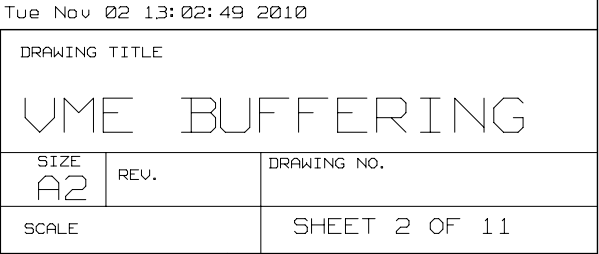
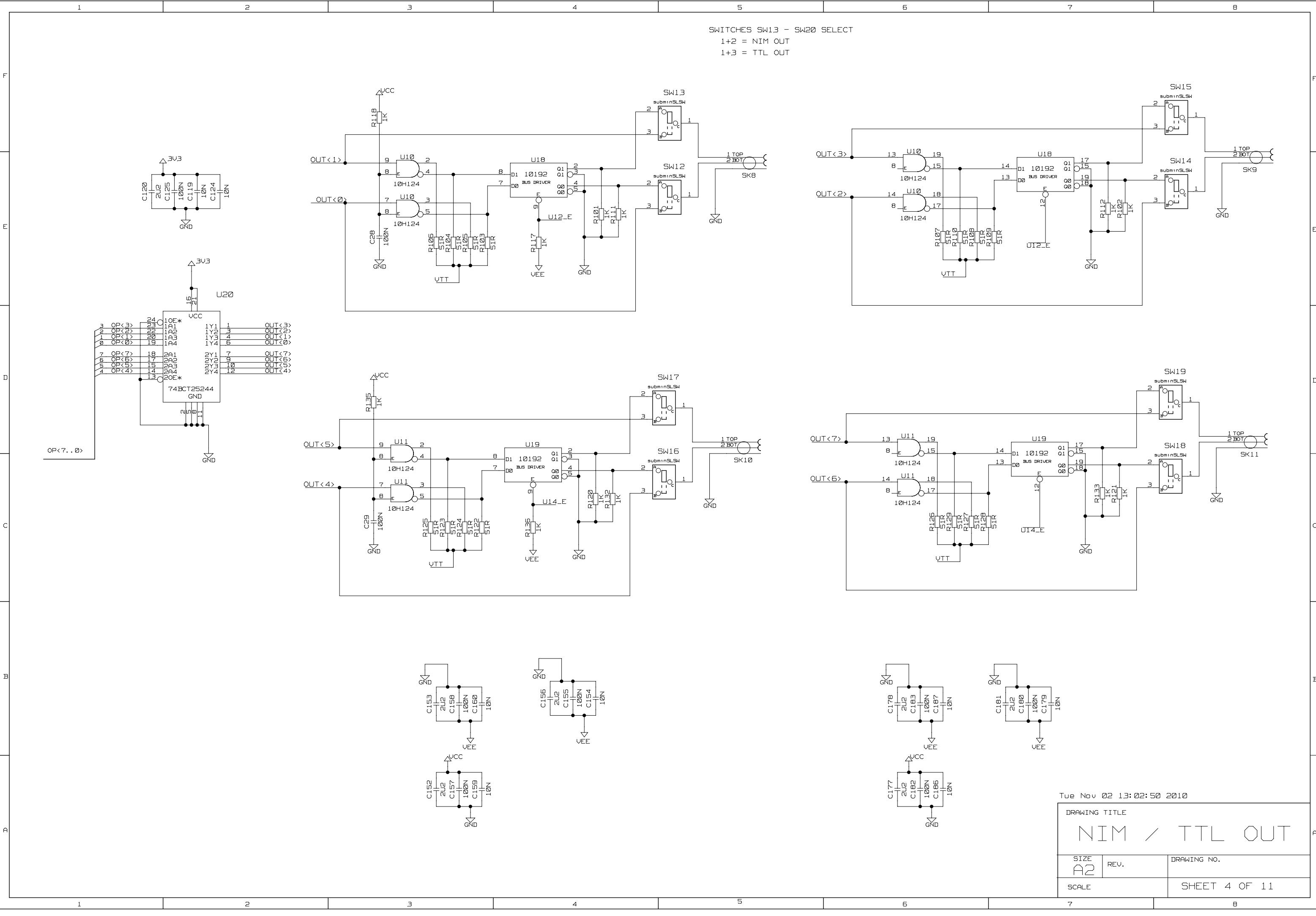


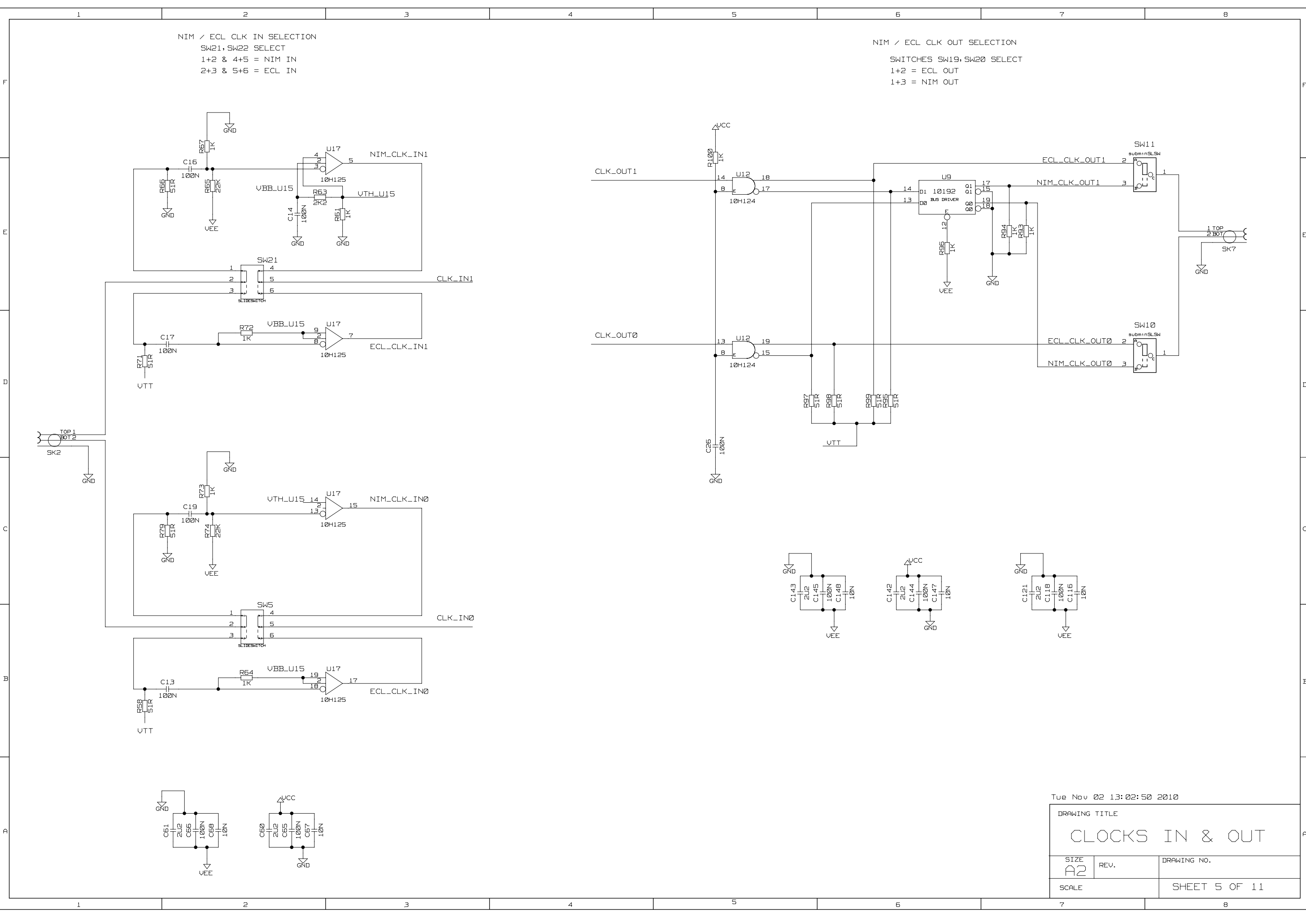






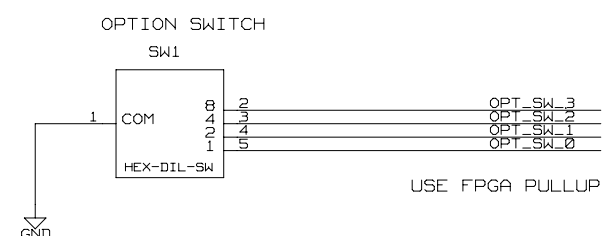
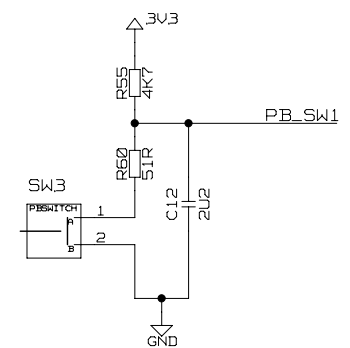
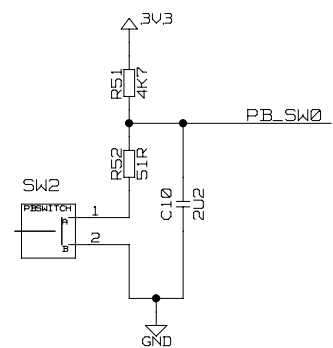
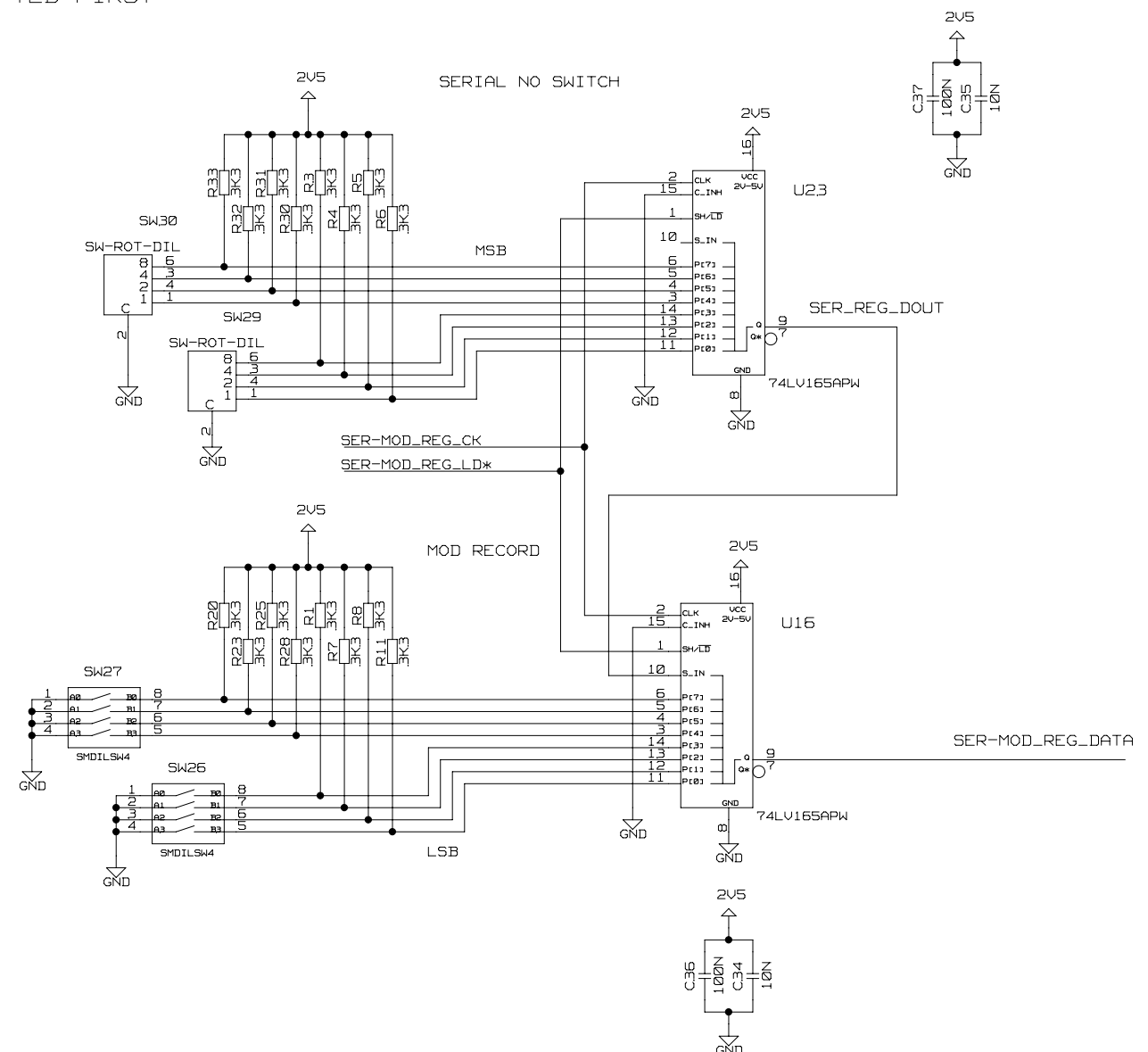
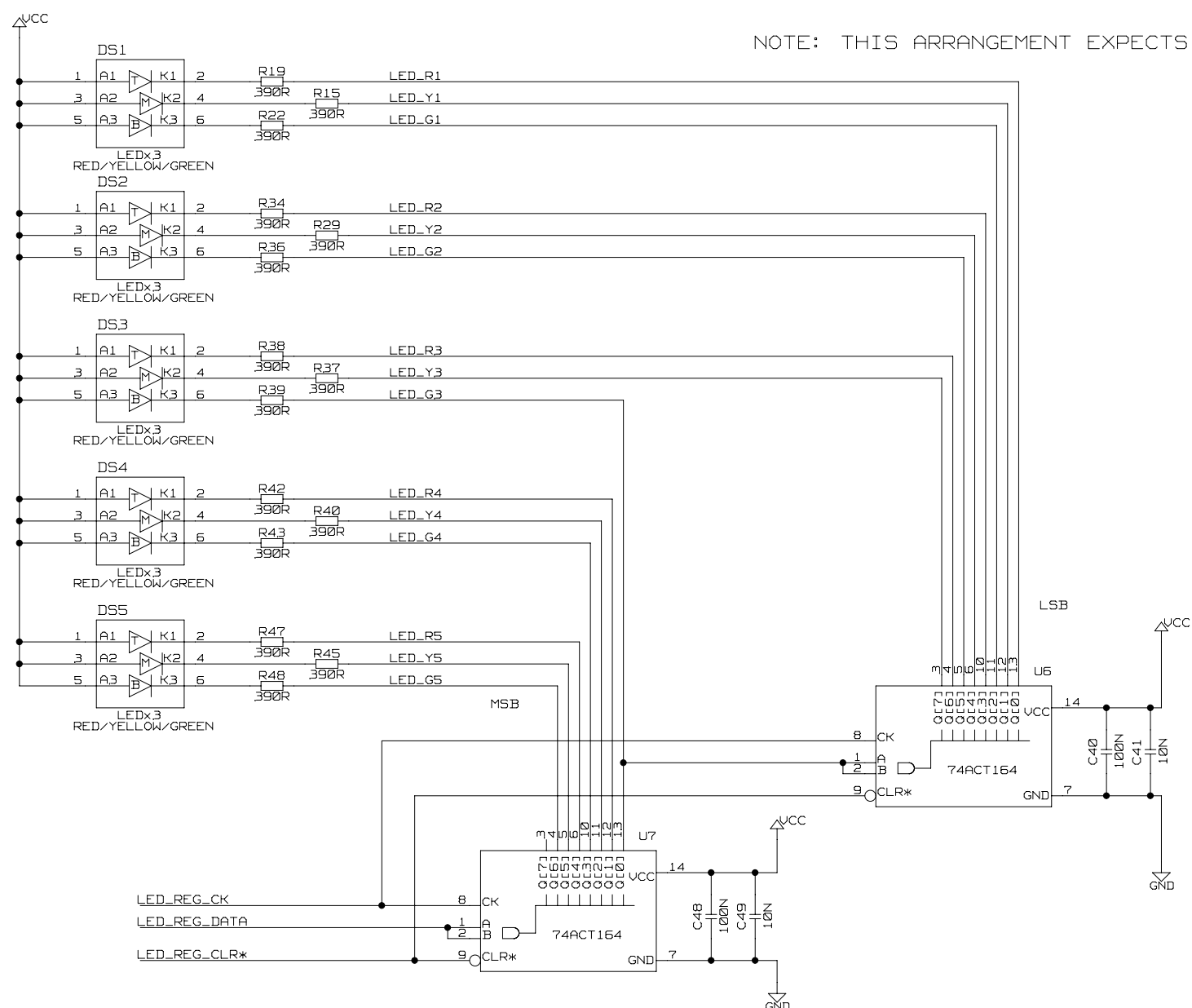
Tue Nov 02 13:02:50 2010

DRAWING TITLE		
NIM / TTL OUT		
SIZE	REV.	DRAWING NO.
A2		
SCALE	SHEET 4 OF 11	



Tue Nov 02 13:02:50 2010		
DRAWING TITLE		
CLOCKS IN & OUT		
SIZE A2	REV.	DRAWING NO.
SCALE		SHEET 5 OF 11

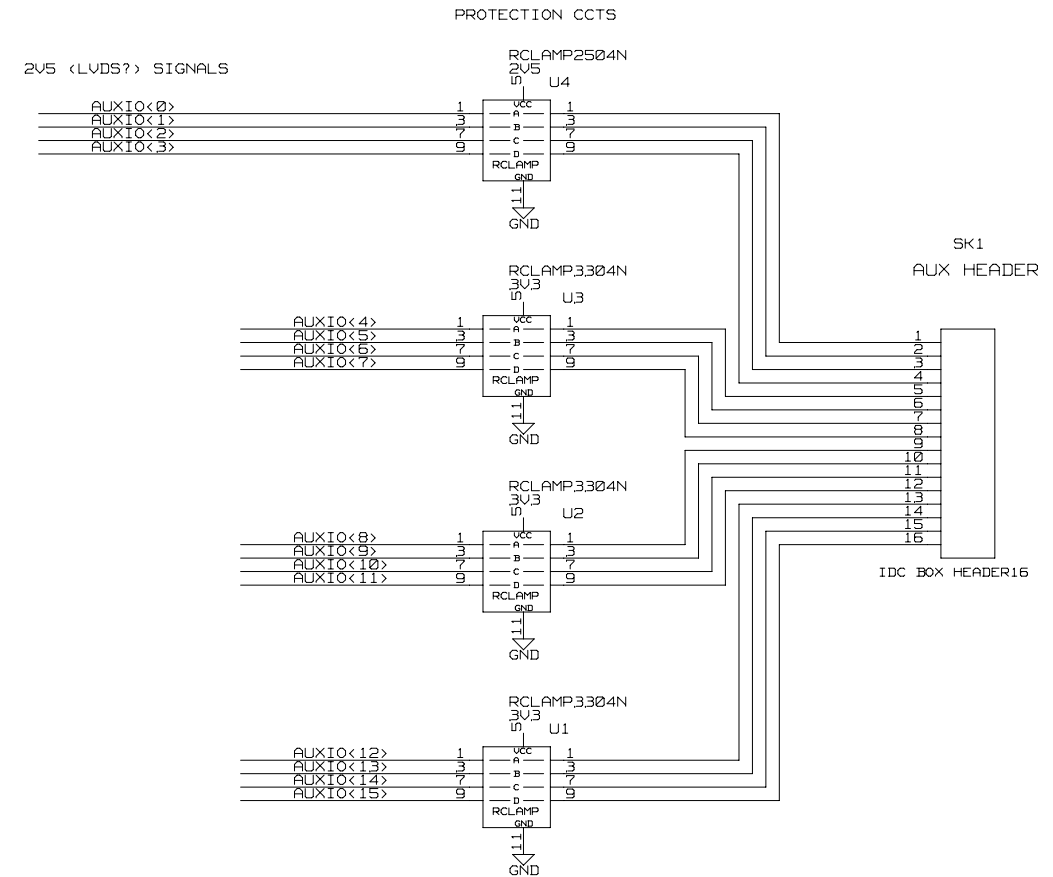
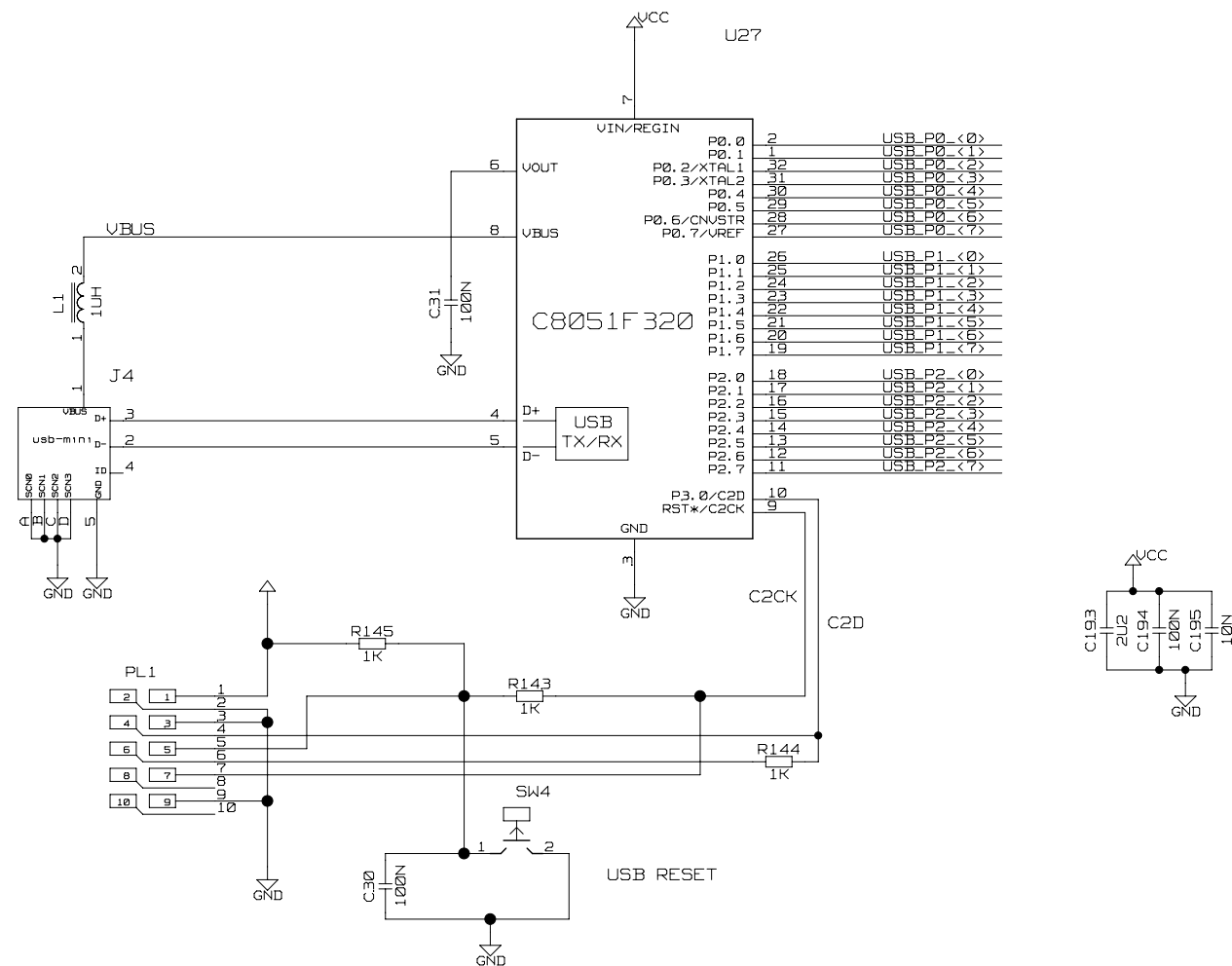
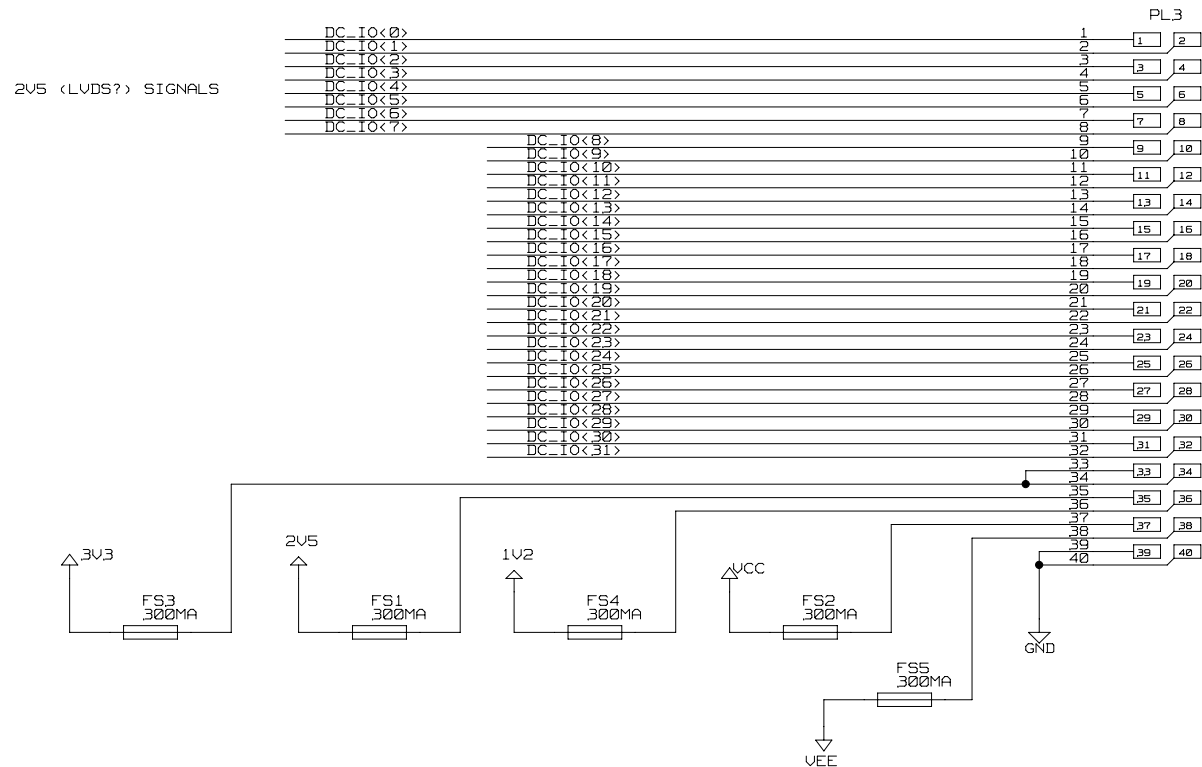
NOTE: THIS ARRANGEMENT EXPECTS LSB SHIFTED FIRST



Tue Nov 02 13:02:48 2010		
DRAWING TITLE		
FRONT PANEL - 1		
SIZE	REV.	DRAWING NO.
A2		
SCALE	SHEET 6 OF 11	

THIS IS A 40 PIN DIL UNSHROUDED HEADER SUITABLE FOR A RIBBON CABLE CONNECTOR
OR STRIAIGHT TO A DAUGHTER CARD

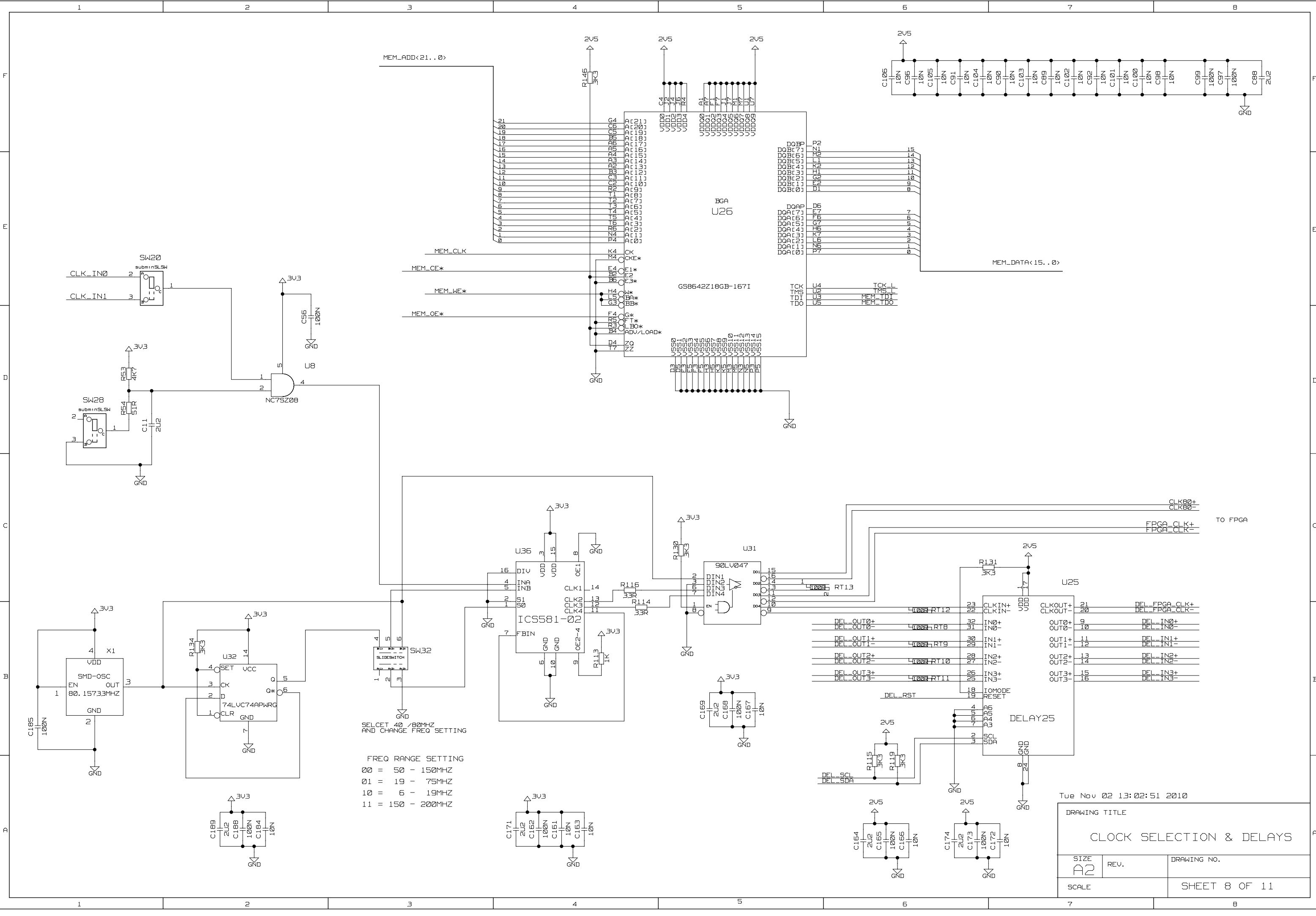
DAUGHTER CARD I/O

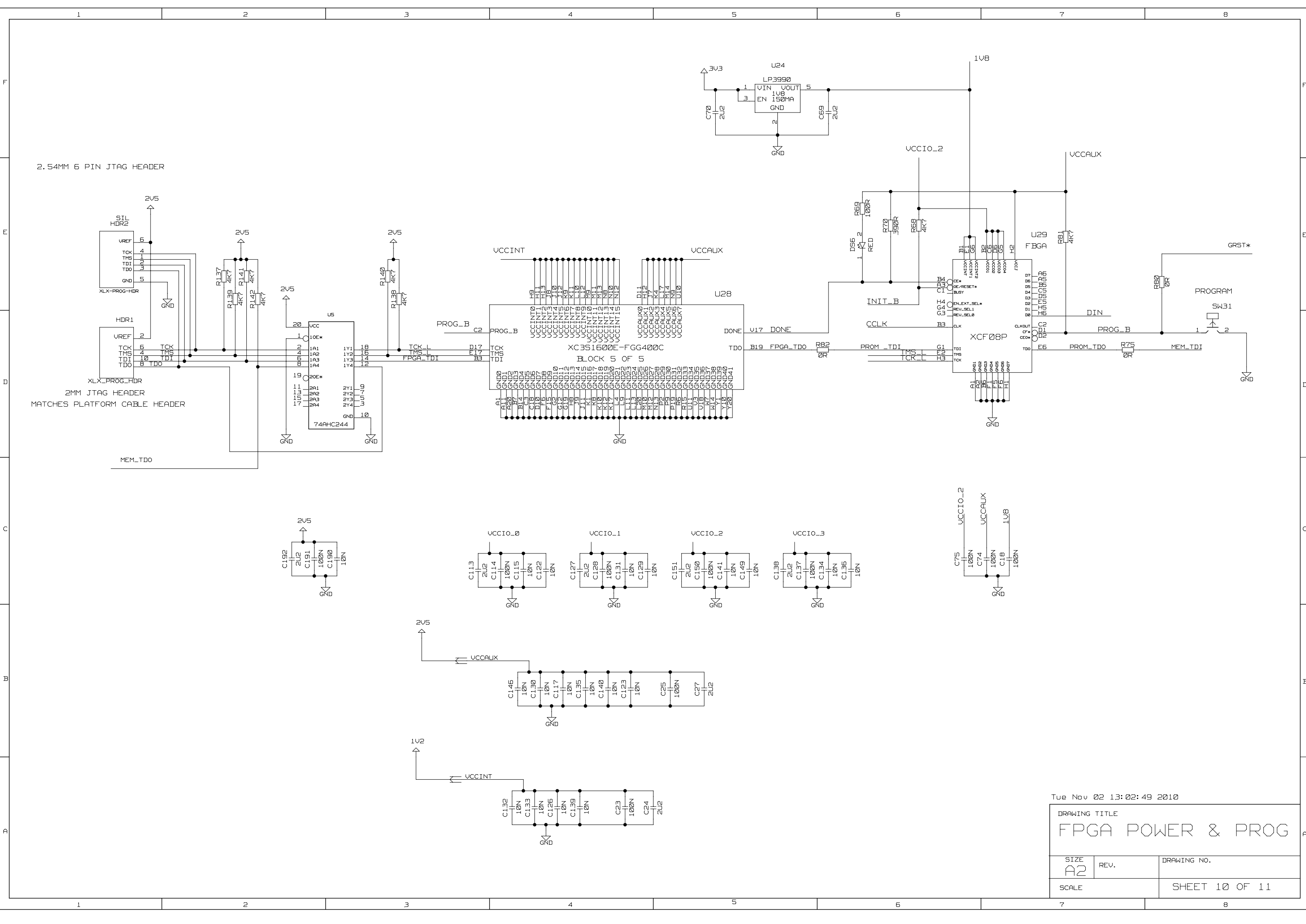


Tue Nov 02 13:02:51 2010

DRAWING TITLE
FRONT PANEL - 2

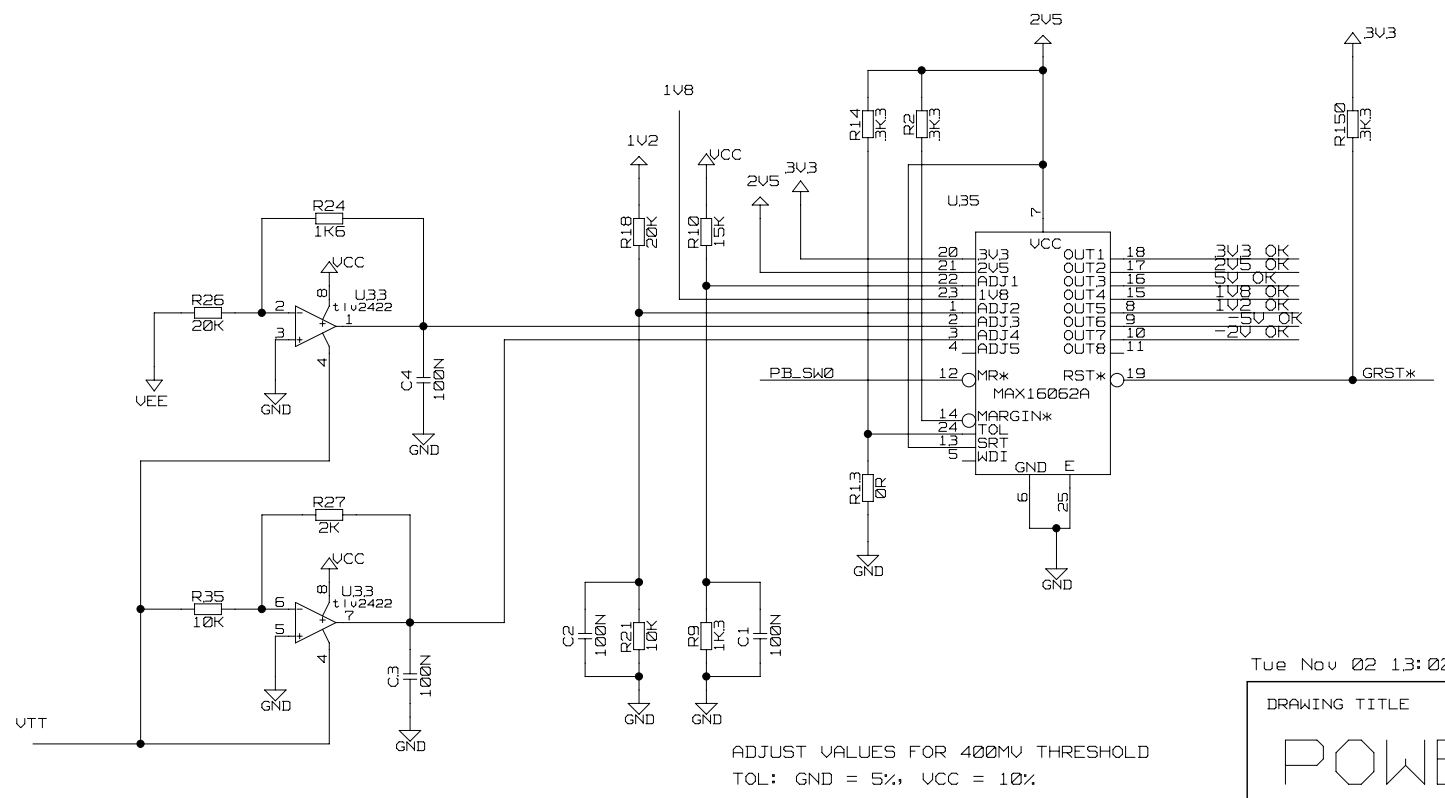
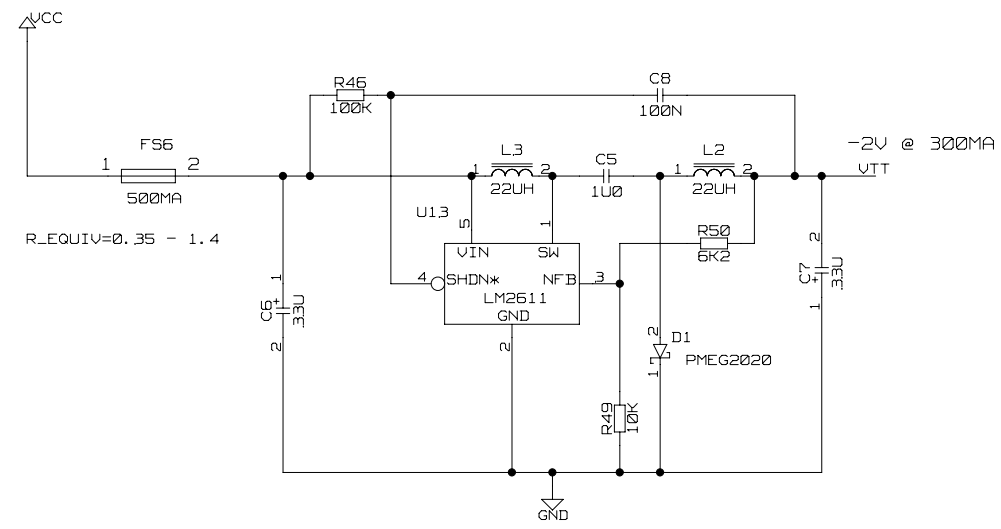
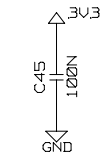
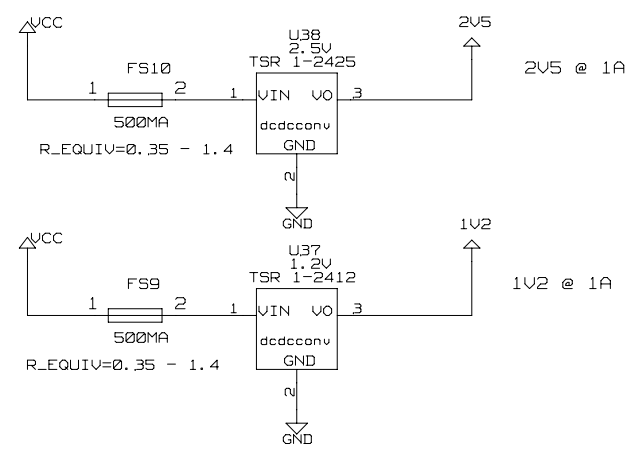
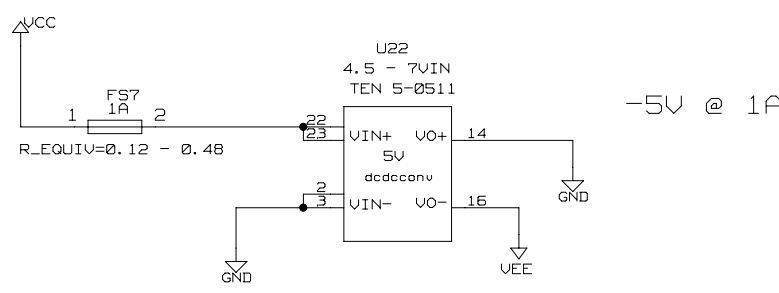
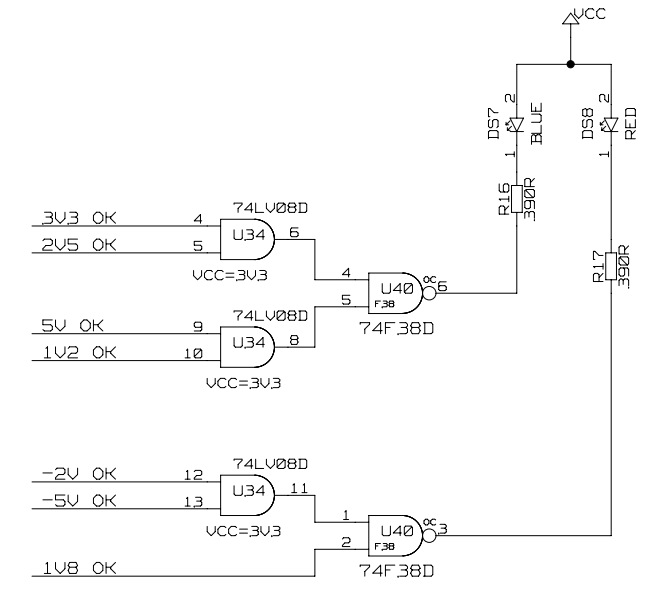
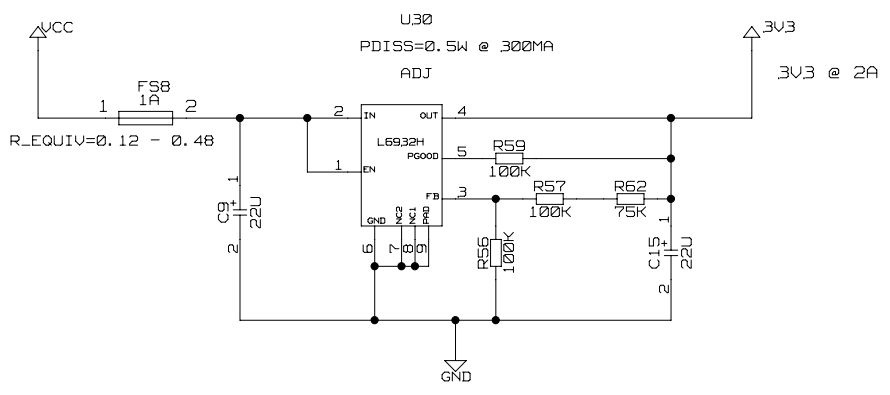
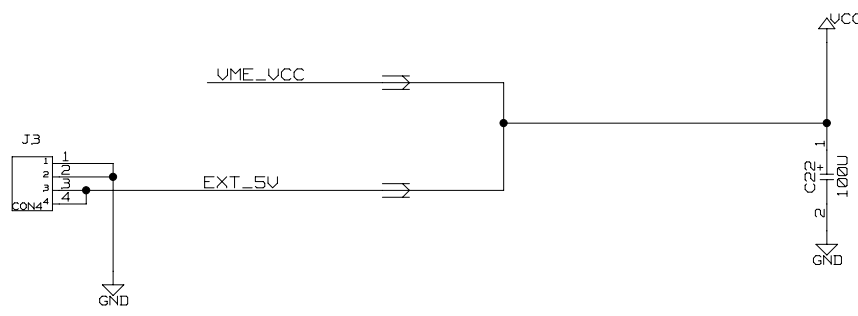
SIZE A2	REV.	DRAWING NO.
SCALE	SHEET 7 OF 11	





Tue Nov 02 13:02:49 2010

DRAWING TITLE		
FPGA POWER & PROG		
SIZE	REV.	DRAWING NO.
A2		
SCALE		SHEET 10 OF 11



GROUND CLIPS X 3

ADJUST VALUES FOR 400mV THRESHOLD
TOL: GND = 5%, VCC = 10%

Tue Nov 02 13:02:48 2010		
DRAWING TITLE		
POWER		
SIZE	REV.	DRAWING NO.
A2		
SCALE		SHEET 11 OF 11