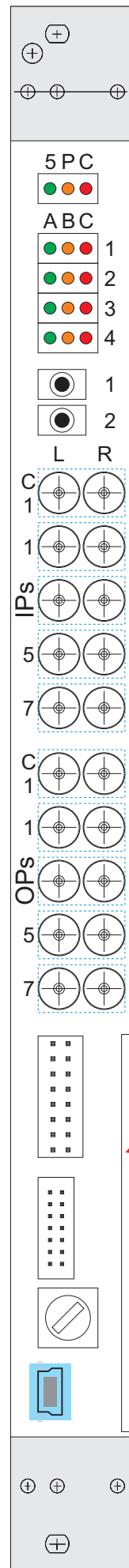


Usable Vertical Space = 217



System LEDs

General Purpose LEDs

Push-Button Switches

Clock I/Ps ICK1 & ICK2

Ip1 & Ip2

Ip3 & Ip4

Ip5 & Ip6

Ip7 & Ip8

Clock O/Ps OCK1 & OCK2

Op1 & Op2

Op3 & Op4

Op5 & Op6

Op7 & Op8

16 Pin Aux Conn

Notch for Cable

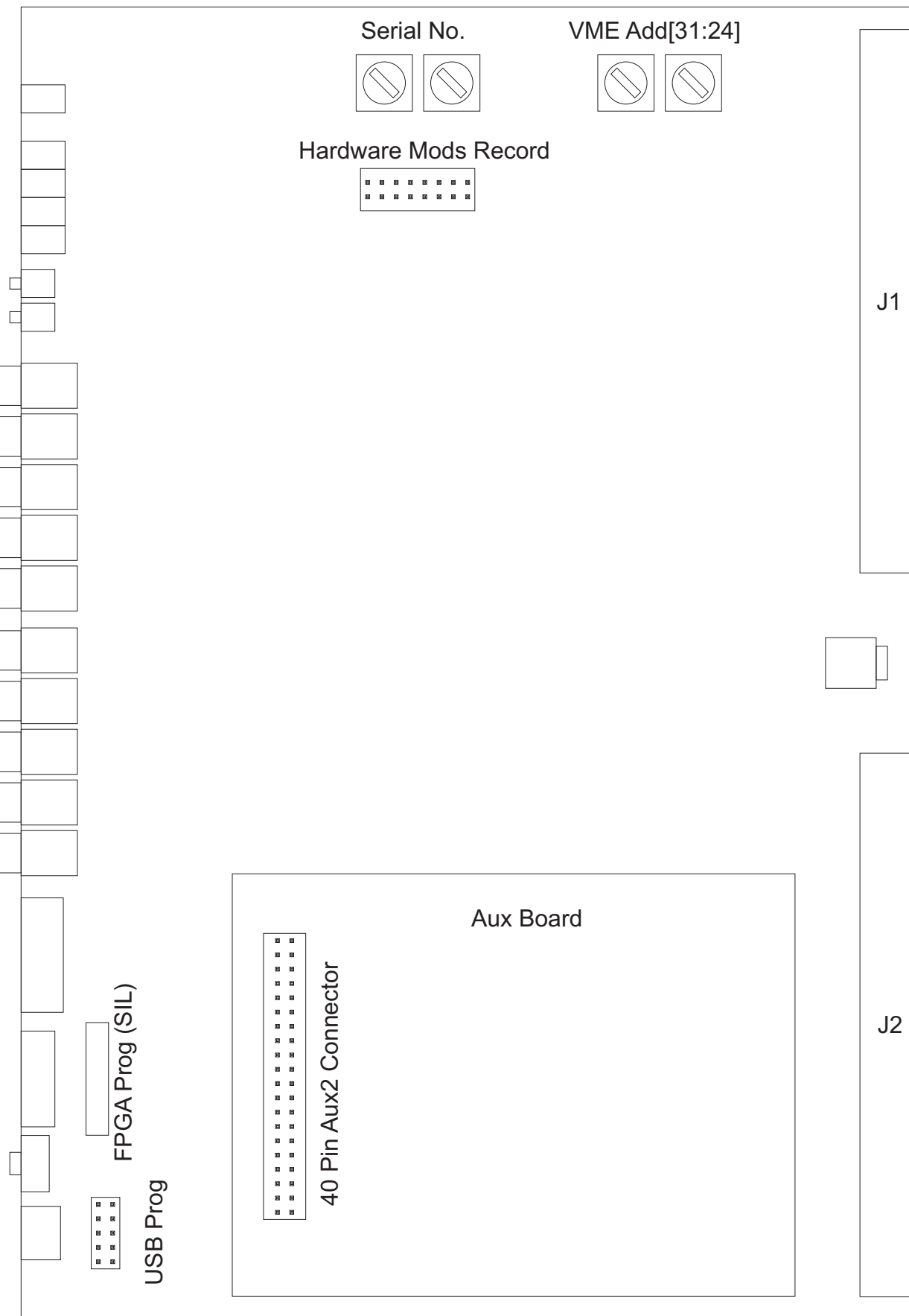
FPGA Prog Hdr

Hex Options Switch

USB Port

Inputs

Outputs



On-PCB View

Serial No.

VME Add[31:24]

Hardware Mods Record

J1

External 5V Input

J2

Aux Board

40 Pin Aux2 Connector

FPGA Prog (SIL)

USB Prog

Provisional Positions