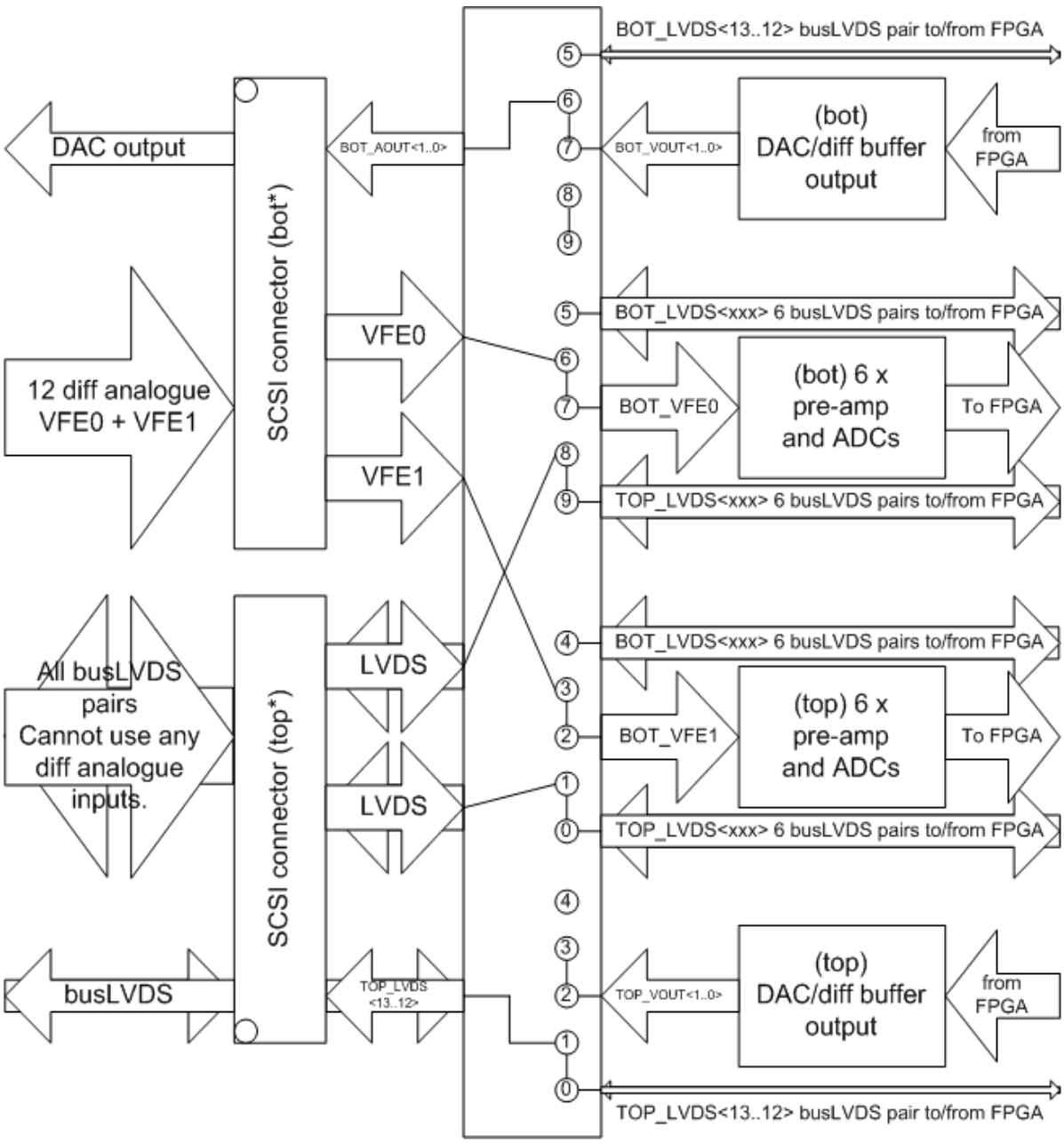
Standard 12 channel plus busLVDS configuration (ECAL)

Standard 12 way analogue cable on bottom SCSI connector, link columns 7-6, 3-2, (all ADC inputs used, Vout from bottom DAC)

this only leaves busLVDS signals for the top SCSI connector, link columns 9-8, 1-0.



A 12 channel analogue cable (bot SCSI) and full busLVDS cable (top SCSI)

A single 12 channel VFE card can be connected to a single SCSI connector (only one Vout<1..0> output is used), the second SCSI connector may be used for arbitrary busLVDS I/O including clock generation. All busLVDS connections straight from the SCSI connectors to the FPGA are not shown.