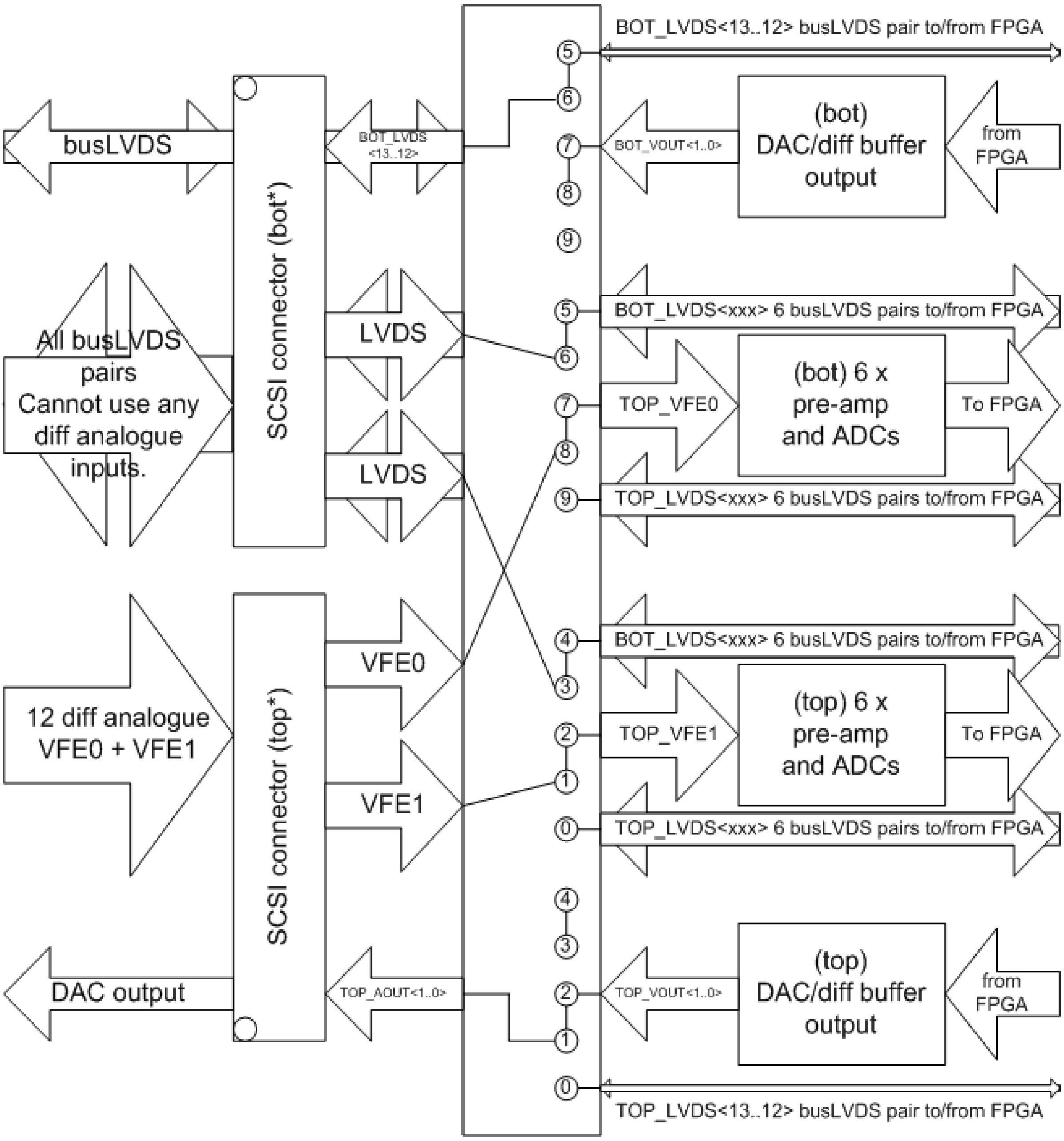
Reverse 12 channel plus busLVDS configuration - do not used this configuration always use the " standard 12"

Standard 12 way analogue cable on top SCSI connector, link columns 7-8, 1-2, (all ADC inputs used, Vout from top DAC)

this only leaves busLVDS signals for the bottom SCSI connector, link columns 5-6, 3-4.



A 12 channel analogue cable (top SCSI) and full busLVDS cable (bot SCSI)

A single 12 channel VFE card can be connected to a single SCSI connector (only one Vout<1..0> output is used), the second SCSI connector may be used for arbitrary busLVDS I/O including clock generation. All busLVDS connections straight from the SCSI connectors to the FPGA are not shown.