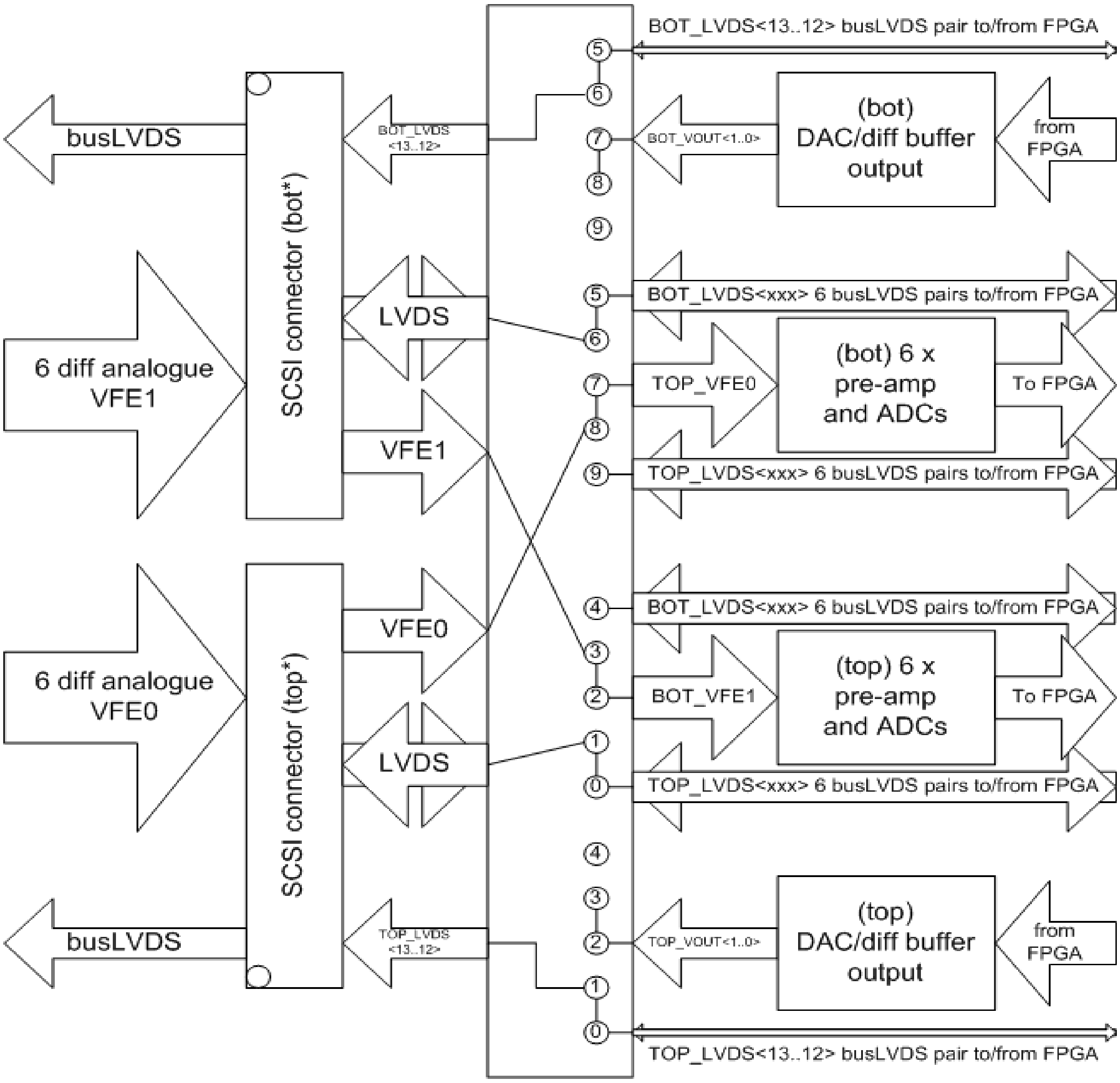
Reverse dual 6 channel configuration - do not used this configuration (because this loses the " Vout"s) always use the "standard dual 6"

VFE1 on bottom SCSI connector, link columns 3-2, 1-0, (top ADCs used, Vout connected to FPGA)

VFE0 on top SCSI connector, link columns 8-7, 6-5, (bottom ADCs used, Vout connected to FPGA)



Two "6 analogue" cables (VFE1 - bottom SCSI) - this does not work correctly.

2 six channel VFE cards can connected to a pair of SCSI connectors. There are two possible ways of doing this, a suitable convention should be set up to ensure that the physical positions of the detectors are mapped correctly.
All busLVDS connections straight from the SCSI connectors to the FPGA are not shown.