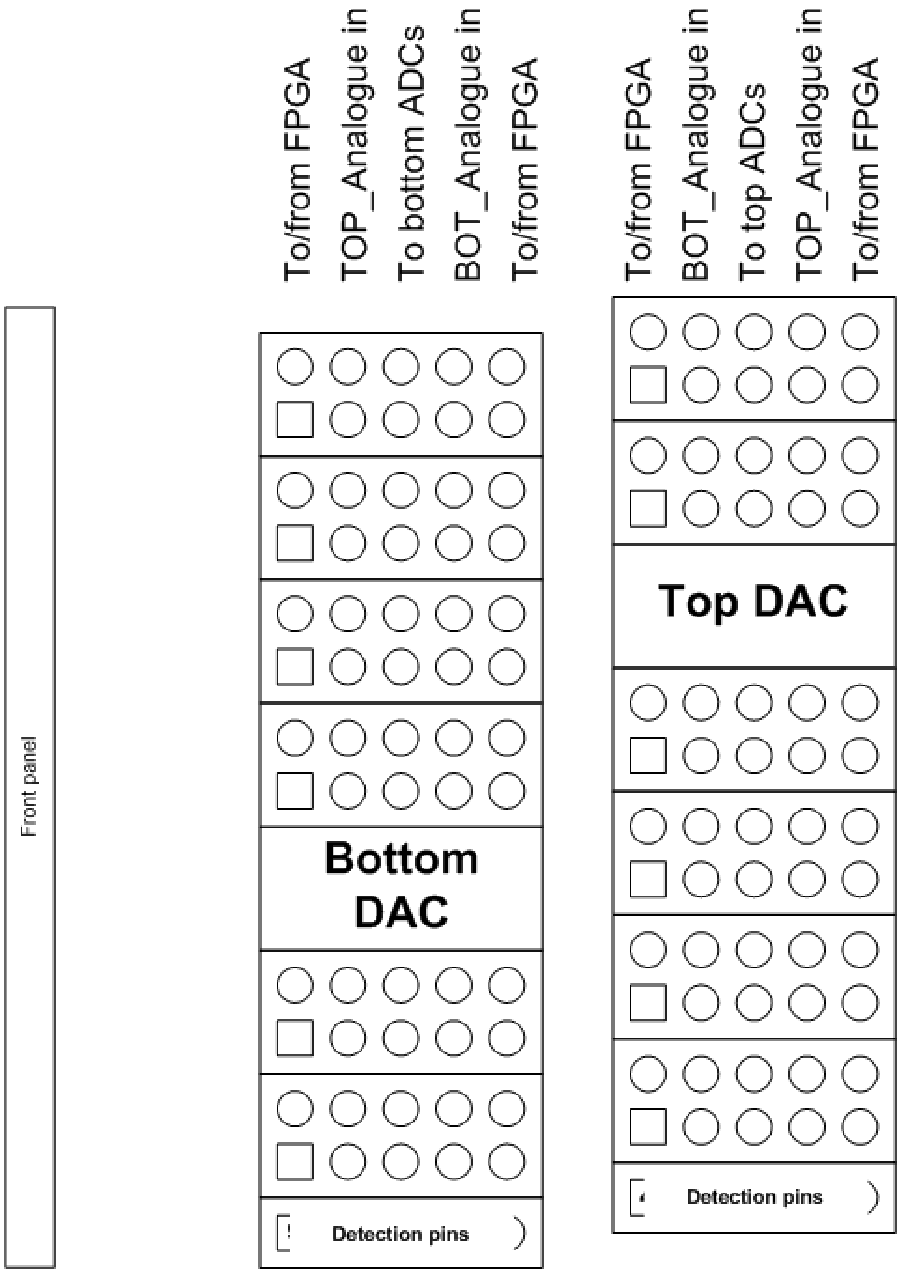
The LinkArray.

The LinkArray is found directly behind every dual SCSI connector, it is used to configure the card to work with various different cables from the three types of VFE card that will be assembled. It may also be used to completely bypass the ADC/DACs and create a completely digital card with flexible busLVDS I/O as is expected to be used on the HCAL.



The LinkArray as seen from the top of the PCB

figure above, Simplified view of link array

column descriptions

9. Connections from/to FPGA

8. Analogue connections from top SCSI connector (TOP_VFE0)

7. Analogue connections to ADCs on bottom of card

6. Analogue connections from bottom SCSI connector (BOT_VFE0)

5. Connections from/to FPGA

4. Connections from/to FPGA

3. Analogue connections from bottom SCSI connector (BOT_VFE1)

2. Analogue connections to ADCs on top of card

1. Analogue connections from top SCSI connector (TOP_VFE1)

0. Connections from/to FPGA

link descriptions

9-8, Top SCSI connector TOP_VFE0 lines are connected to FPGA.

8-7, Top SCSI connector TOP_VFE0 lines are connected to the ADCs on the bottom of the card.

7-6, Bottom SCSI connector lines BOT_VFE0 lines are connected to the ADCs on the bottom of the card.

6-5, Bottom SCSI connector lines BOT_VFE0 lines are connected to FPGA.

4-3, Bottom SCSI connector lines BOT_VFE1 lines are connected to FPGA.

3-2, Bottom SCSI connector lines BOT_VFE1 lines are connected to the ADCs on the top of the card.

2-1, Top SCSI connector TOP_VFE1 lines are connected to the ADCs on the top of the card.

1-0, Top SCSI connector TOP_VFE1 lines are connected to FPGA.

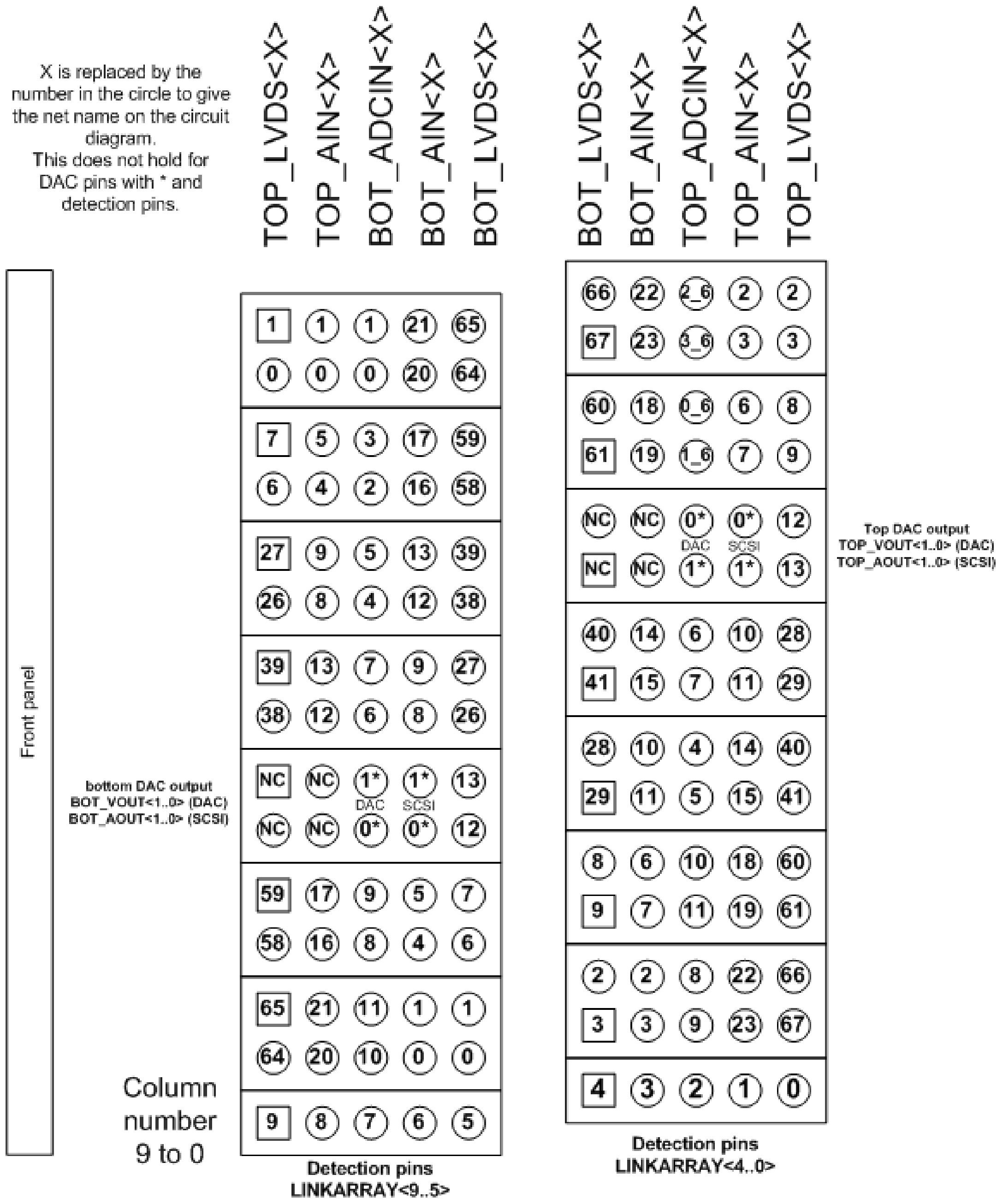
allowable links

Allowable links 9-8, 8-7, 7-6, 6-5, 4-3, 3-2, 2-1, 1-0.

Only four links can be made at any one time (see below).

No other links should ever be attempted under normal circumstances.

Full details of link array circuit labels.



The LinkArray as seen from the top of the PCB