**Structure Analogue and FPGA module circuit diagram and layout**

The calice analogue and FPGA module is built using three ADC module, a DAC module and an FPGA module with SCSI connector, LinkArray and other minor components.

A block diagram of the organisation of the modules can be seen below, indicating the net names used on the circuit diagram.

The "Module I" numbers are no longer correct and will change until the final release.

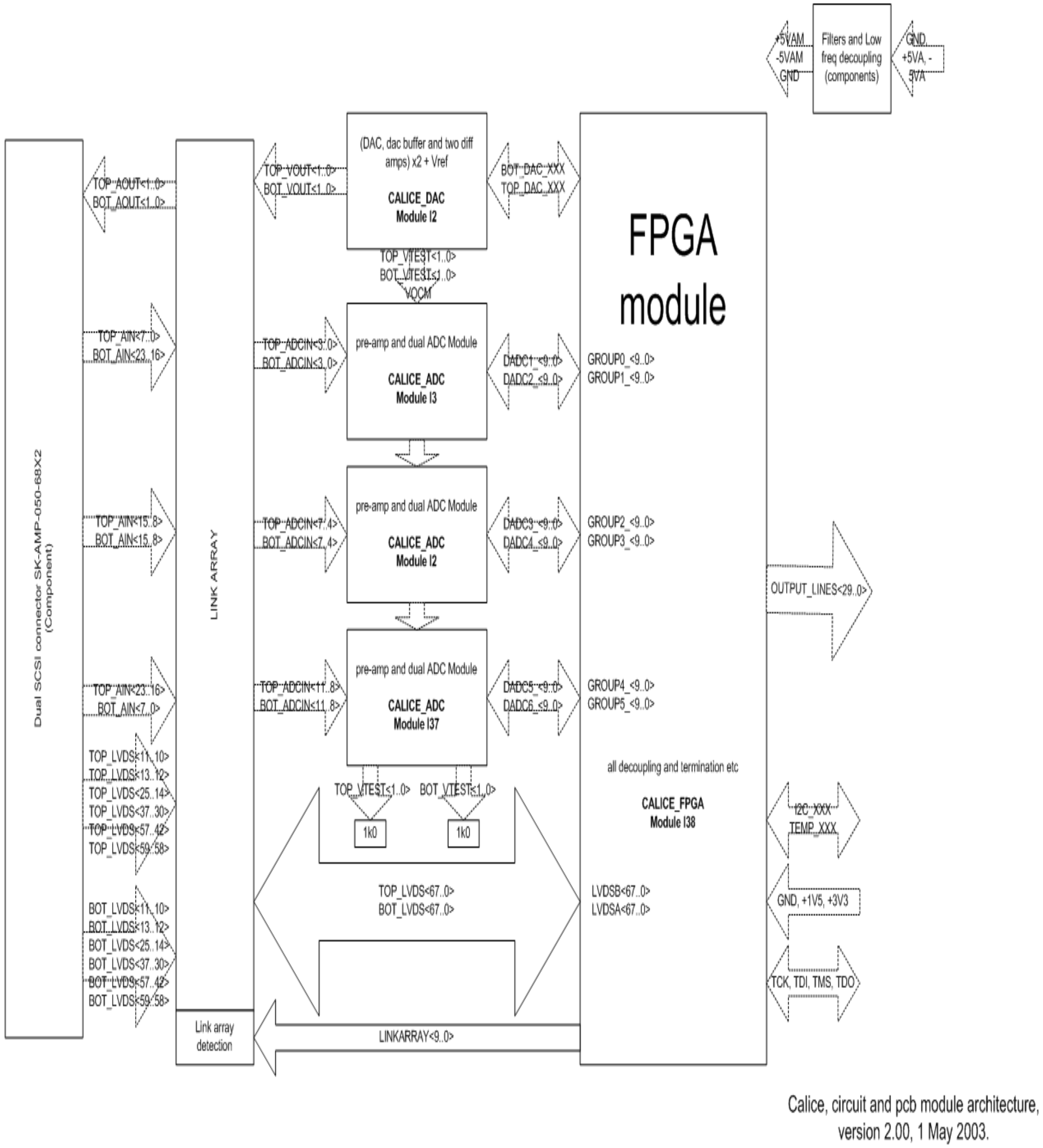


figure directly above, modular segmentation of Calice analogue and FPGA design for circuit diagram and PCB layout.