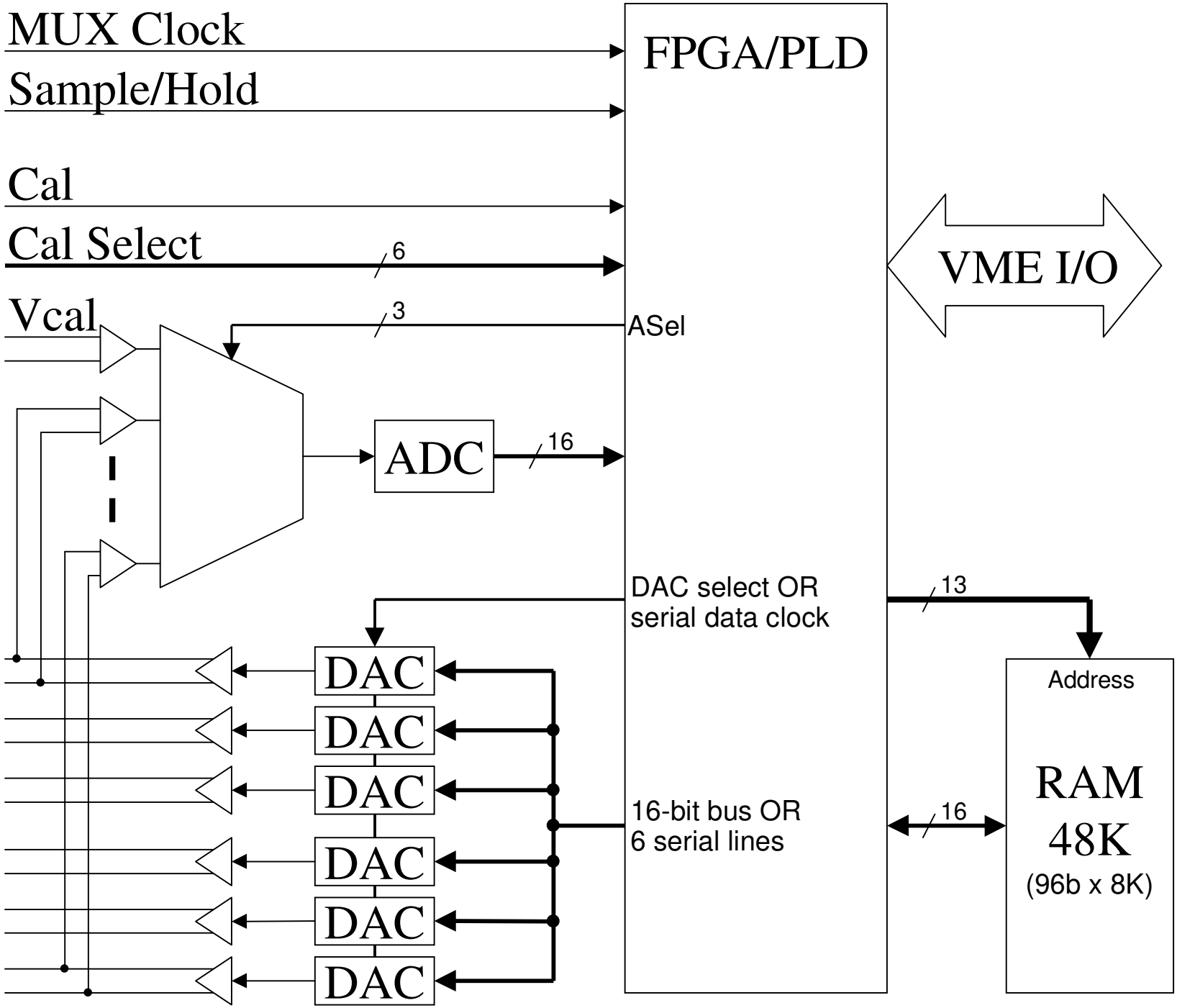
MUX Clock
Sample/Hold
Cal
Cal Select
6
Vcal
3
ASel
FPGA/PLD
VME I/O
ADC
16
DAC select OR
serial data clock
13
DAC
DAC
DAC
DAC
DAC
DAC
16-bit bus OR
6 serial lines
16
Address
RAM
48K
(96b x 8K)