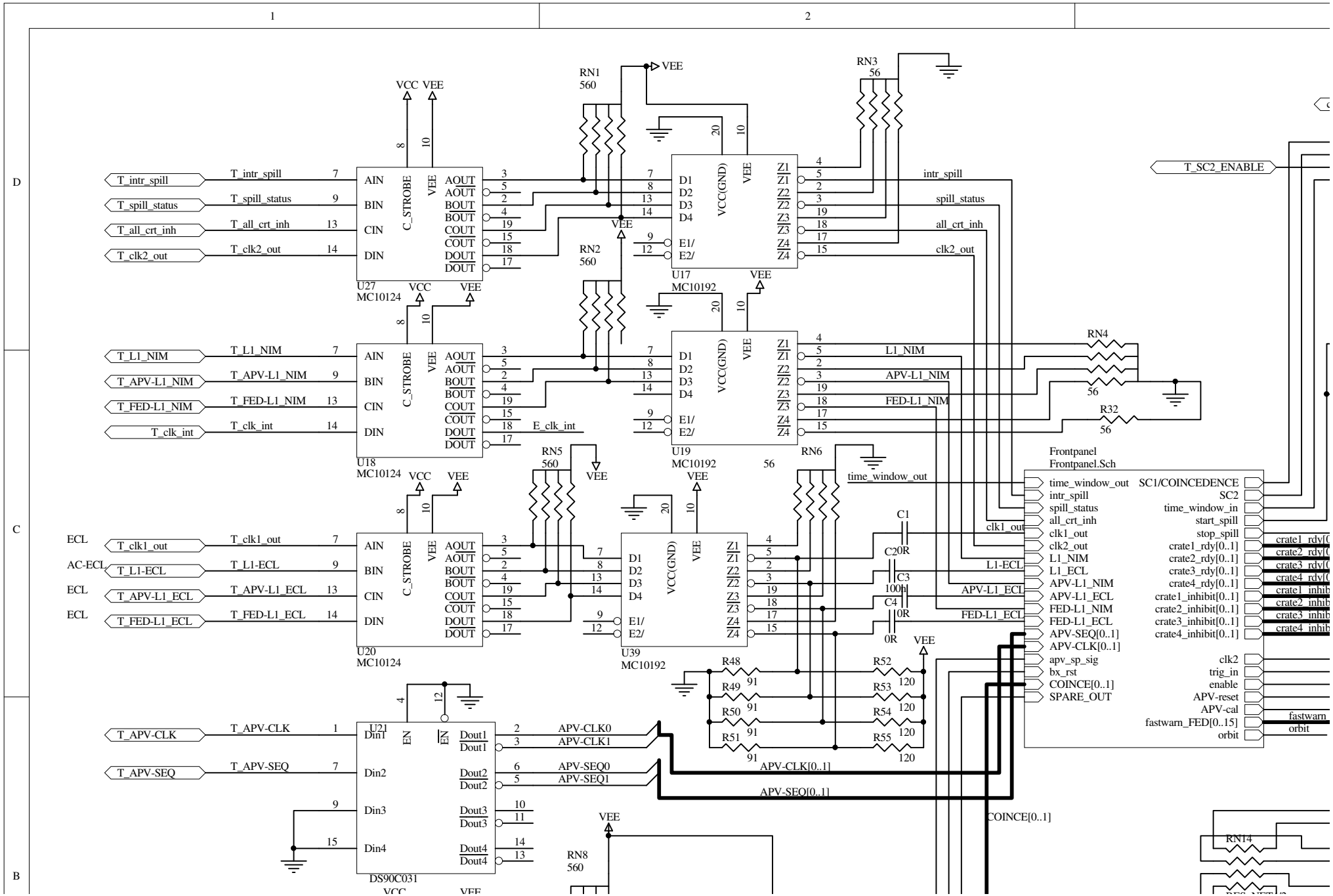
1
2
D
C
B
T_intr_spill
T_spill_status
T_all_crt_inh
T_clk2_out
T_L1_NIM
T_APV-L1_NIM
T_FED-L1_NIM
T_clk_int
ECL
T_clk1_out
AC-ECL
T_L1-ECL
ECL
T_APV-L1_ECL
ECL
T_FED-L1_ECL
T_APV-CLK
T_APV-SEQ
U27
MC10124
U18
MC10124
U20
MC10124
U21
DS90C031
U17
MC10192
U19
MC10192
U39
MC10192
RN1
560
RN2
560
RN3
56
RN4
56
RN5
560
RN6
56
RN8
560
R32
56
R48
91
R49
91
R50
91
R51
91
R52
120
R53
120
R54
120
R55
120
C1
C2
0R
C3
100n
C4
0R
VEE
VCC
intr_spill
spill_status
all_crt_inh
clk2_out
L1_NIM
APV-L1_NIM
FED-L1_NIM
E_clk_int
time_window_out
clk1_out
L1-ECL
APV-L1_ECL
FED-L1_ECL
APV-CLK0
APV-CLK1
APV-SEQ0
APV-SEQ1
APV-CLK[0..1]
APV-SEQ[0..1]
COINCE[0..1]
T_SC2_ENABLE
Frontpanel
Frontpanel.Sch
time_window_out
intr_spill
spill_status
all_crt_inh
clk1_out
clk2_out
L1_NIM
L1_ECL
APV-L1_NIM
APV-L1_ECL
FED-L1_NIM
FED-L1_ECL
APV-SEQ[0..1]
APV-CLK[0..1]
apv_sp_sig
bx_rst
COINCE[0..1]
SPARE_OUT
SC1/COINCEDENCE
SC2
time_window_in
start_spill
stop_spill
crate1_rdy[0..1]
crate2_rdy[0..1]
crate3_rdy[0..1]
crate4_rdy[0..1]
crate1_inhibit[0..1]
crate2_inhibit[0..1]
crate3_inhibit[0..1]
crate4_inhibit[0..1]
clk2
trig_in
enable
APV-reset
APV-cal
fastwarn_FED[0..15]
orbit
fastwarn
RN14