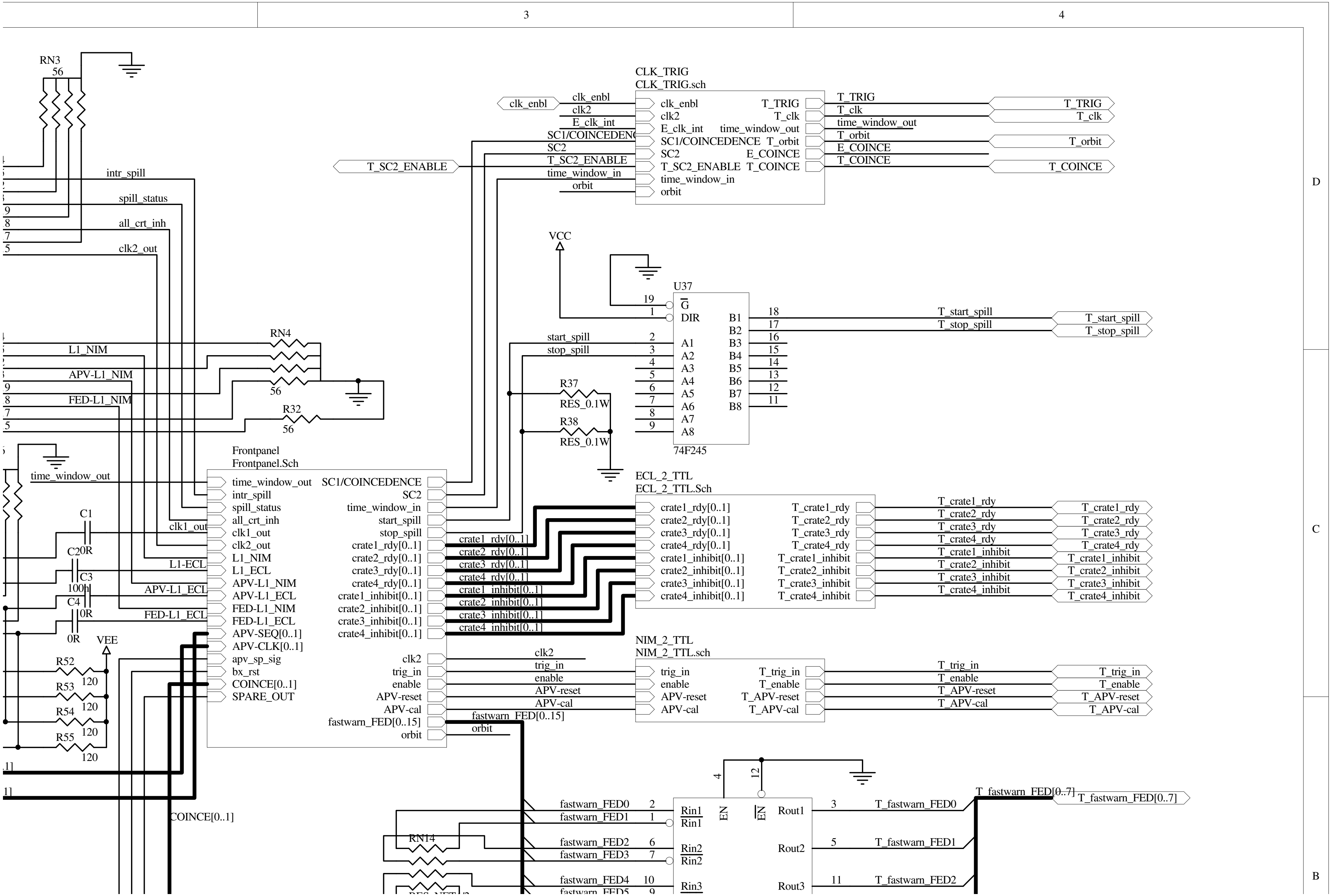

3
4
D
C
B
RN3
56
intr_spill
spill_status
all_crt_inh
clk2_out
CLK_TRIG
CLK_TRIG.sch
clk_enbl
clk2
E_clk_int
SC1/COINCEDENCE
SC2
T_SC2_ENABLE
time_window_in
orbit
T_TRIG
T_clk
time_window_out
T_orbit
E_COINCE
T_COINCE
VCC
U37
G
DIR
A1
A2
A3
A4
A5
A6
A7
A8
B1
B2
B3
B4
B5
B6
B7
B8
74F245
start_spill
stop_spill
T_start_spill
T_stop_spill
R37
RES_0.1W
R38
RES_0.1W
RN4
56
R32
56
L1_NIM
APV-L1_NIM
FED-L1_NIM
Frontpanel
Frontpanel.Sch
time_window_out
intr_spill
spill_status
all_crt_inh
clk1_out
clk2_out
L1_NIM
L1_ECL
APV-L1_NIM
APV-L1_ECL
FED-L1_NIM
FED-L1_ECL
APV-SEQ[0..1]
APV-CLK[0..1]
apv_sp_sig
bx_rst
COINCE[0..1]
SPARE_OUT
SC1/COINCEDENCE
SC2
time_window_in
start_spill
stop_spill
crate1_rdy[0..1]
crate2_rdy[0..1]
crate3_rdy[0..1]
crate4_rdy[0..1]
crate1_inhibit[0..1]
crate2_inhibit[0..1]
crate3_inhibit[0..1]
crate4_inhibit[0..1]
clk2
trig_in
enable
APV-reset
APV-cal
fastwarn_FED[0..15]
orbit
C1
C20R
C3
100n
C4
0R
0R
L1-ECL
APV-L1_ECL
FED-L1_ECL
clk1_out
VEE
R52
120
R53
120
R54
120
R55
120
COINCE[0..1]
ECL_2_TTL
ECL_2_TTL.Sch
T_crate1_rdy
T_crate2_rdy
T_crate3_rdy
T_crate4_rdy
T_crate1_inhibit
T_crate2_inhibit
T_crate3_inhibit
T_crate4_inhibit
NIM_2_TTL
NIM_2_TTL.sch
trig_in
enable
APV-reset
APV-cal
T_trig_in
T_enable
T_APV-reset
T_APV-cal
fastwarn_FED[0..15]
orbit
fastwarn_FED0
fastwarn_FED1
fastwarn_FED2
fastwarn_FED3
fastwarn_FED4
fastwarn_FED5
RN14
Rin1
Rin1
Rin2
Rin2
Rin3
EN
EN
Rout1
Rout2
Rout3
T_fastwarn_FED0
T_fastwarn_FED1
T_fastwarn_FED2
T_fastwarn_FED[0..7]