

**BOC-ROD-TIM REVIEW (JULY 31 TO AUG. 1, 2000)
SCT AND PIXEL OFF-DETECTOR WORKSHOP (AUG. 2, 2000)**

**Place: LBNL
Bldg.: 2, Conf. Rm.: 100B**

August 1, 2000, 15:30 to 15:50 hrs :

TIM SCHEMATICS AND LAYOUT

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- **TIM Schematics representation**
- **Components used**
 - **PLDs vs. Discrete**
 - **+5V vs. +3V**
 - **ECL vs. PECL**
 - **Power requirements**
- **External Inputs & Outputs**
 - **Front Panel Layout & LEDs**
 - **Front Panel Interfaces**
 - **Backplane Interfaces**
- **TIM Clocks**
 - **Stand Alone & Machine clock**
 - **Clocks Flow Schematics**
 - **Clocks Delays**
 - **TTC Fast Commands Flow**
 - **Reset**
- **TIM PCB Layout**
 - **Overall Layout**
 - **PCB layers**
 - **PECL Clocks**

TIM-SCH-00