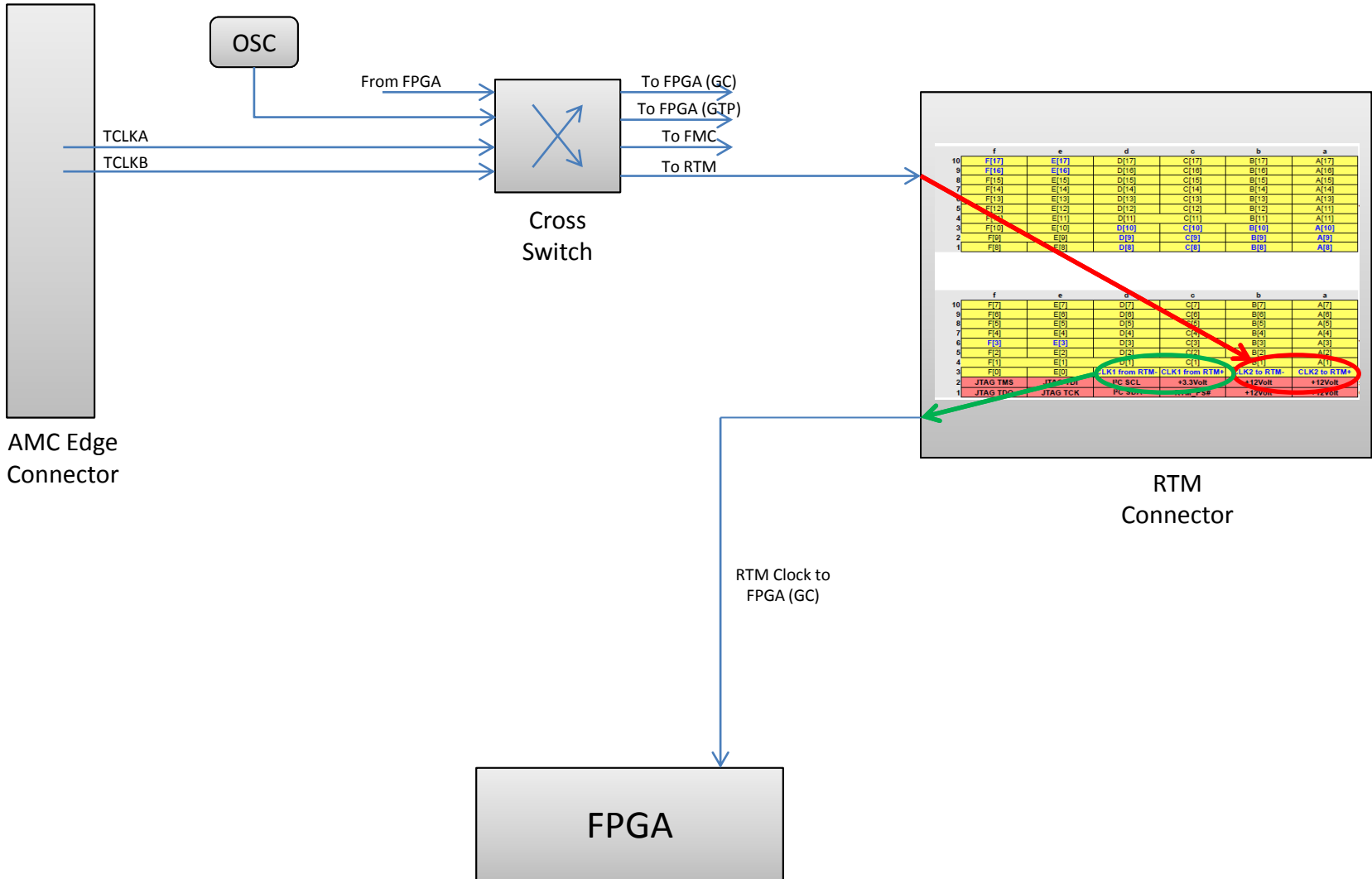
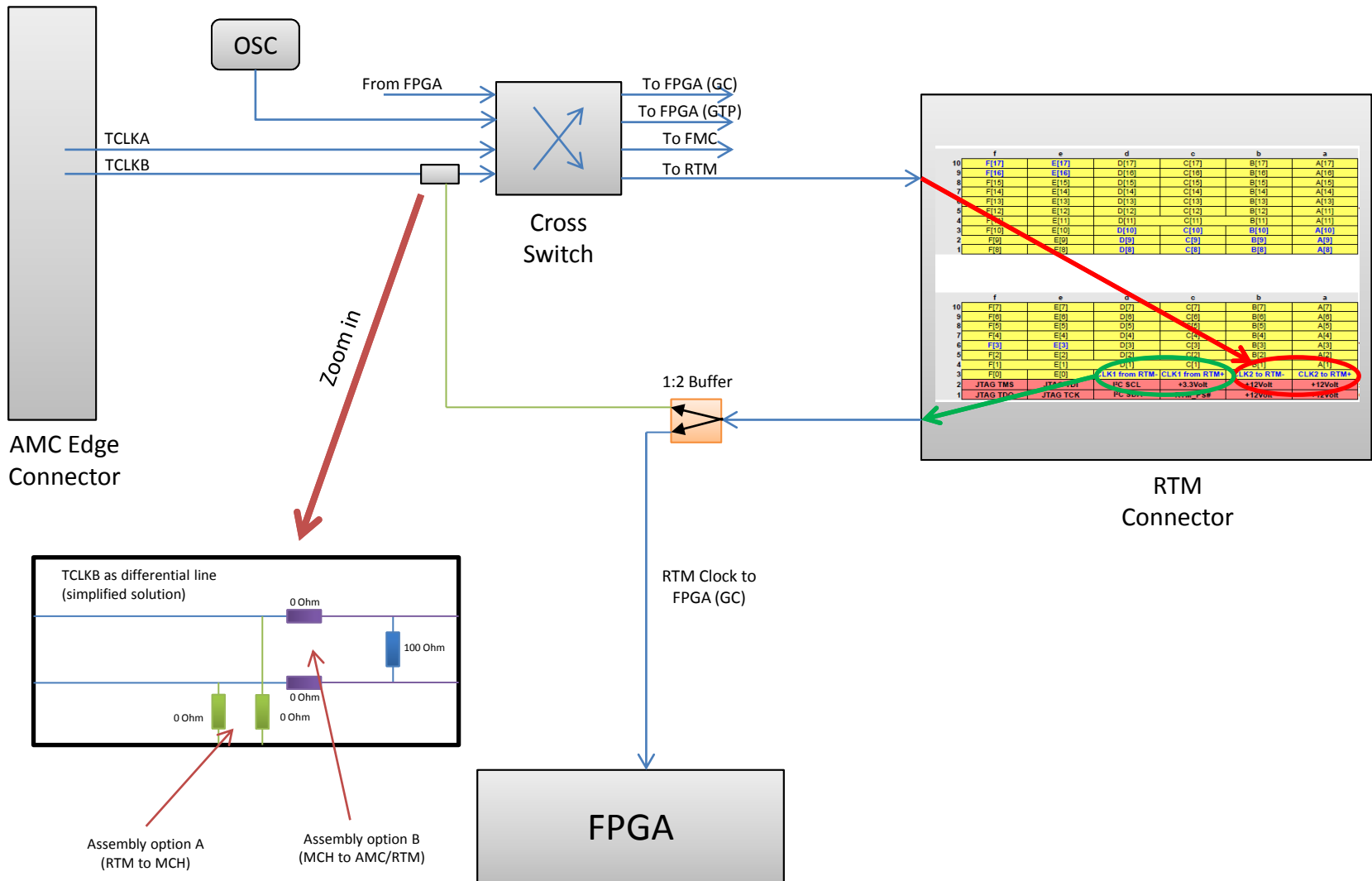


Current Clock connections



Bi-directional TCLKB proposal



Comments

- Only one pair of 0 Ohm resistor should be assembled
- 1:2 Buffer should really separate the two outputs
 - No reflections or short circuits should be seen on the other output