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## Meeting of 2D detector DAQ and XFEL timing groups.

Nov 12-13<sup>th</sup>, 2008

## Day 1: Timing specifics, Wednesday 12<sup>th</sup> Nov., Building/Room 55A/110

14:00 Aim of the meeting

C.Youngman

14:10 Description and status of XFEL timing system

K.Rehlich

How does the system work?

Define the nomenclature used.

What is connected to what using what media?

What clocks and signals are distributed?

What telegram data is defined?

What are the resolutions and jitters?

Will the configuration change train-to-train?

Etc.,

14:40 Discussion

14:50 Experiments timing receiver board description:

A.Hidvegi

What is the form factor (assume AMC)?

How do the experiments receive triggers and telegrams (backplane requirements)?

What is the status of the board?

Who is providing what (boards, software,...)?

What is the time line of development/production?

Will other form factors be provided, e.g. inserting into PC?

Etc.

15:20 Discussion

15:30 Requirements from the experiments

C.Youngman

Do the experiments need more than start train, pulse clock, bunch occupancy telegrams, etc?

How is non delivery of an entire train notification performed?

How is non delivery of a pulse or many pulses performed?

Restate resolution and jitters required?

How do the experiments know that the timing system is incorrect (not connected,

not working, etc.)

15:50 Discussion

16:00 Requirements from the Train Builder

J.Coughlan

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16:20 Discussion

16:30 Post Dresden decisions w.r.t. crate standards

K.Rehlich

What crates are recommended for use in the Lab.

## Day 2: 2D detector control issues, Thursday 13th Nov., building/room AER19/5.21

09:00 Pros and cons of separate control and data handling P.Goettlicher

09:20 Pros and cons of combined control and data handling A.Kugel

09:40 Discussion

10:30 How to proceed

Discussion

Define which signals, clocks and data have to be distributed?

Define the senders and receivers of the above (C&C, TB, FEE)?

What connection media, connectors and protocol are to be used?

How does the entire system get debugged during running?

Standalone running requirements (calibration, bunch occupancy handling, etc.)

How is the system monitored?

Time line for C&C development

What do experiments do when nothing is available?

Etc.