

## **FEE, TB and CC FPGA Meeting**

Wednesday, 15<sup>th</sup> February 2012  
Patrick Gessler

### **Present**

J. Coughlan, B. Fernandes, T. Gerlach, P. Gessler, Rob Halsall

### **Next Meeting**

**29<sup>th</sup> February 2012, 11:00 CET**

### **Open Points**

- **Collect final change request for DAMC2**
- **Collect feedback for VETO document**

### **Minutes**

#### **1. Feedback for VETO Specification**

- Thomas sent text for the DSSC implementation of the VETO system, Patrick will check if there is something missing
- John is currently preparing the LPD text and will provide it till the next meeting
- No feedback from AGIPD and no one present at the meeting
  - o Ulrich Trunk (ASIC) is looking into the document
  - o Patrick will get in contact with Manfred Zimmer and Peter Goettlicher

#### **2. Preparation for the Slice test**

- Ongoing work on TB firmware
- 10G switch is required in order to do the one-to-many data path tests

#### **3. Status of the projects**

- LDP FEE
  - o 20 new FEMs back from production and tested
  - o 11 will be for LPD + the already existing 6 → 1MPixel + 1 spare
  - o Rob is working on the interfacing to the Clock and Control system
- TB
  - o All changes implemented in PCB layout → will be finished this week
  - o Next week final checks in Design office → out for manufacturing end next week
  - o The first two boards expected by end of March/beginning of April
  - o All components are already there
  - o There was one week delay as one person was sick
- DSSC
  - o Ongoing work of complete system (I/O board + custom FPGA board as Patchpanel + CC)
  - o Implementation and test on communication and configuration of I/O board with custom FPGA board
  - o Thomas explained, that the whole system test is important and he would like to do the

clock and jitter test not on isolated small test modules, but the whole system

- The jitter test with CC output will be done later
- It might be required to bring the DAMC2 board back to XFEL for developments, but not before eastern
- Patrick tries to avoid it by finding another one to be borrowed from DESY

#### **4. Participation in Conferences**

- LPD
  - Will visit TWEPP
  - Other not planned yet
- DSSC
  - Preparing abstract for IEEE Real Time Conference

#### **5. AoB**

- none

#### **Decisions and actions**

- Get in contact with AGIPD for VETO feedback (Patrick)
- Preparation of the VETO text for LPD (John)
- Check if there is a DAMC2 in DESY available for tests (Patrick)

#### **Topics on Hold**

- Redesign/changes on CC RTM could wait till Summer 2012