

FEE, TB and CC FPGA Meeting

Wednesday, 29th February 2012
Patrick Gessler

Present

J. Coughlan, B. Fernandes, T. Gerlach, P. Gessler, Erdem Motuk, Matthew Wing

Next Meeting

28th March 2012, 11:00 CET

Open Points

- **Collect feedback for VETO document**

Minutes

1. Input for VETO Specification document

- Patrick checked DSSC text and has only two remarks
 - o What happens with VETOs close to the maximum number of bunches?
 - o Got error messages from Word → please send also as PDF and DOC
 - o Thomas will send the document again in doc and PDF format
 - o Thomas will also discuss with Florian about what happens if a VETO is received within the final 128 bunches
- John sent the LPD version yesterday and today again, as there were problems with the mailing list
- Patrick will put the text in the VET specification document and sends an email, if there are further questions
- For AGIPD Peter Goettlicher is preparing the text and will send it to Patrick as soon as possible

2. Planning of the 10GbE Workshop

- Possible date is the 24th to 25th of May
- From 12:00 to 12:00 next day
- 10GbE / 40GbE +
- Most likely to be done at XFEL
- Dates seem to fit to all present at the meeting. People to join are
 - o Bruno, Patrick, Chris, Rob, Erdem, Thomas
- Patrick will start a list of topics and possible people/talks

3. Preparation for the Slice test

- Nothing to report

4. Status of the projects

- TB
 - o The design went out yesterday
 - o Prototypes expected in first week of April

- DSSC
 - Further investigations on signal problems on IO board
 - Now well access is possible due to the new set up test environment
 - Currently finalizing the abstract for the IEEE RT conference
 - Soon starting the measurements with the PLL and CC as input
- C&C
 - The jitter measurements done recently are put in a document
 - It will be sent around this week
 - Additional test done with 20m cable and removed biasing resistors for data and clock lines
 - This seems to work fine with no influence of long series of zeros or ones on data
 - Removing this resistors would involve changes on the FEEs
 - A test should be done with the FEM for LPD
 - It has to be clarified between John and Erdem, when and how this could be done

5. AoB

- The next planned meeting at the 14th March is canceled. Next meeting will be on the 28th March.

Decisions and actions

- Sent DSSC VETO text in doc and PDF format to Patrick (Thomas)
- Include VETO texts in VETO specification (Patrick)
- Change behavior of mailing list (accept all mails + reply to person) (Patrick)
- Preparations for 10GbE Workshop (Patrick)
- Send around jitter measurement report (Erdem)

Topics on Hold

- Redesign/changes on CC RTM could wait till Summer 2012