

# LPD firmware: timing interface

TB and C&C meeting

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# Contents

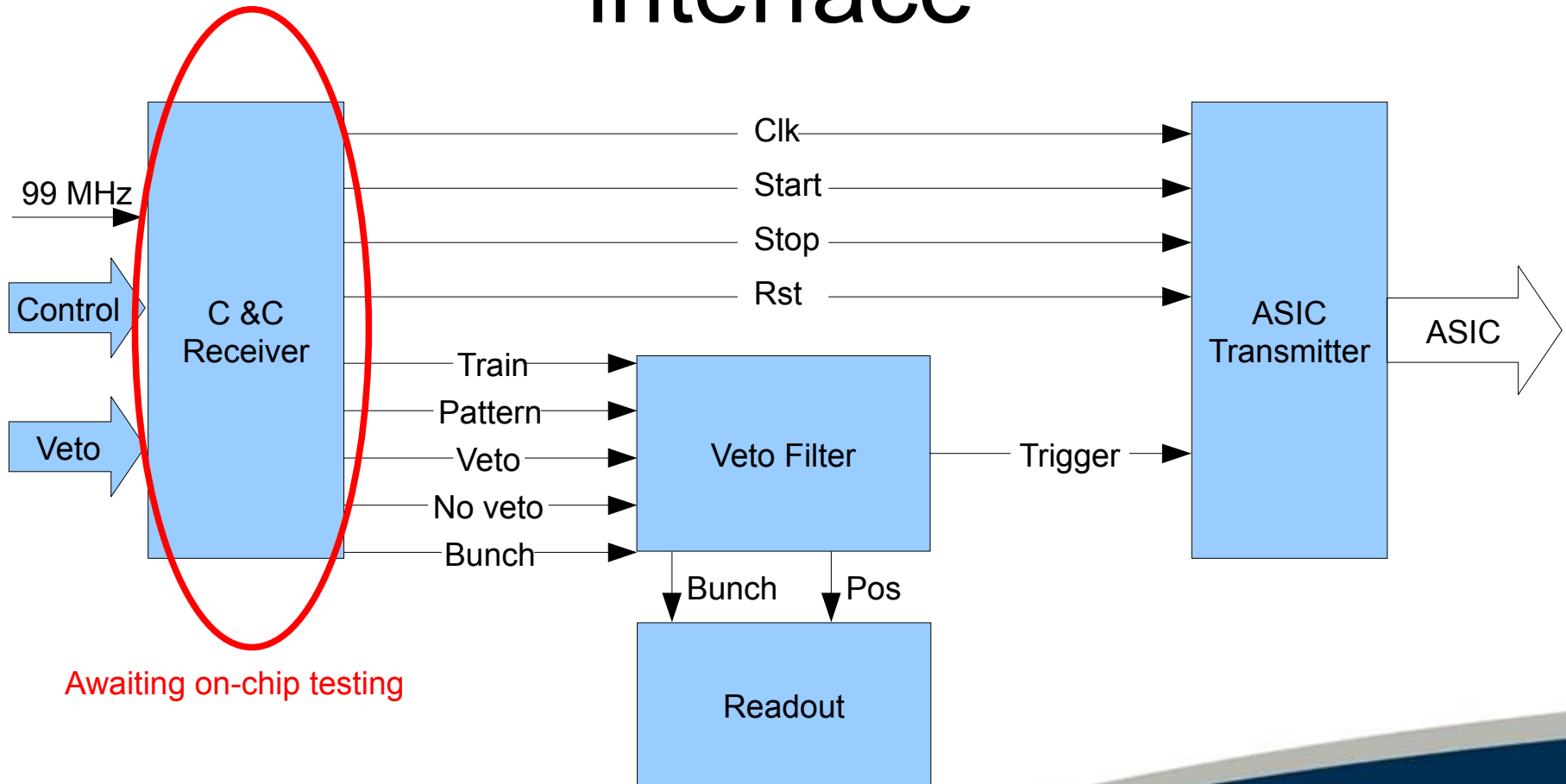
Timing interface

Clock and control receiver

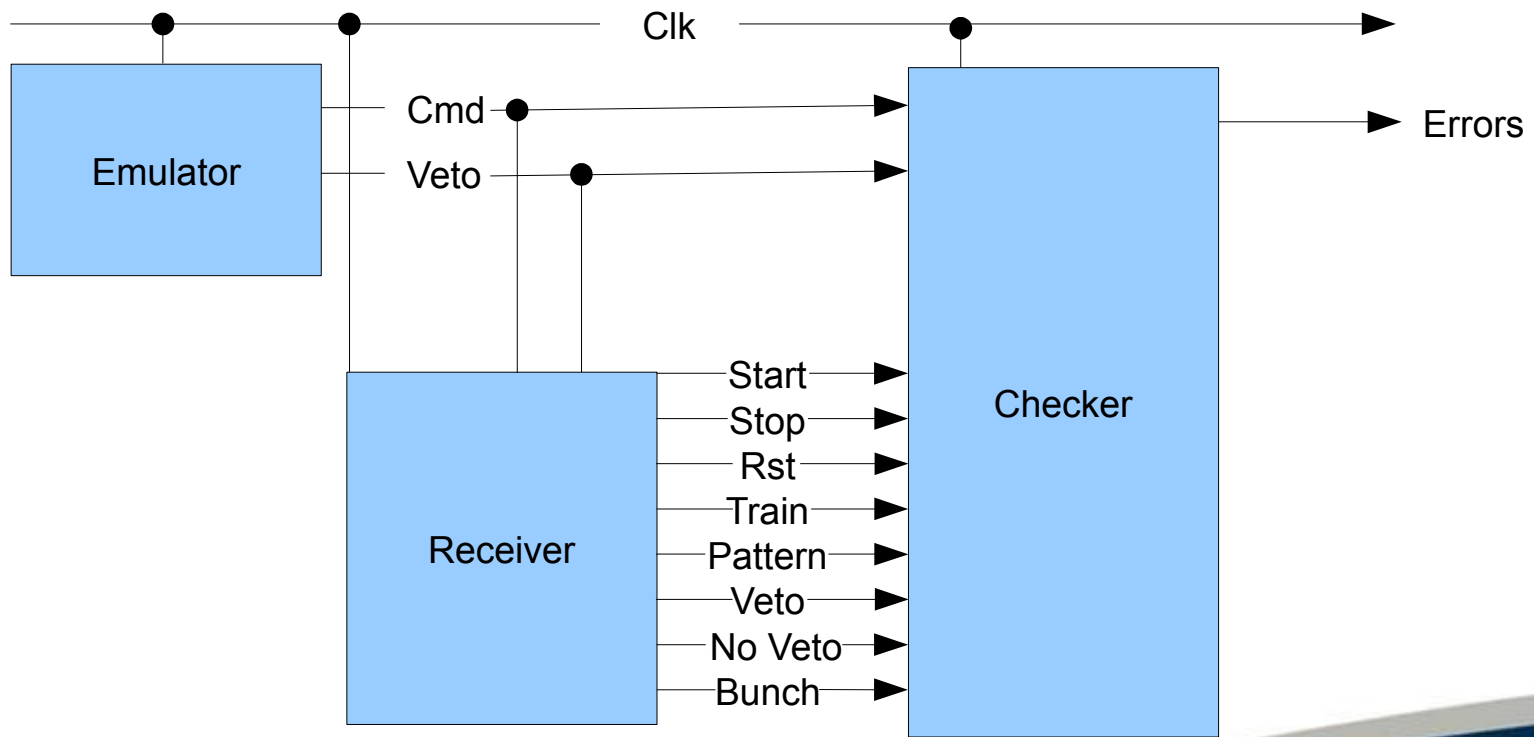
Clock and control emulator



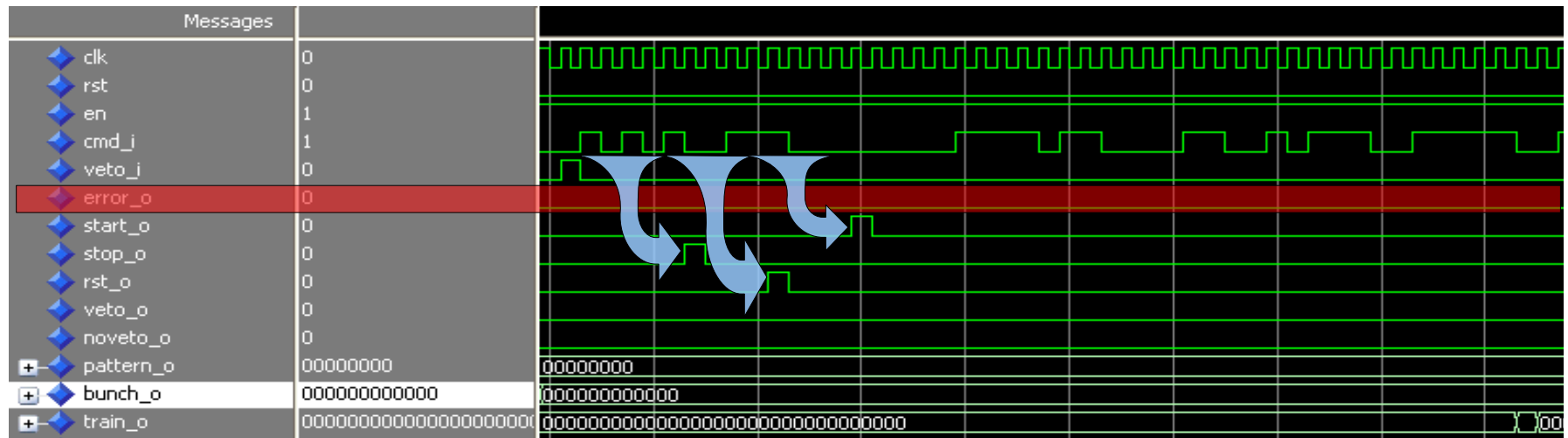
# Block diagram: timing interface



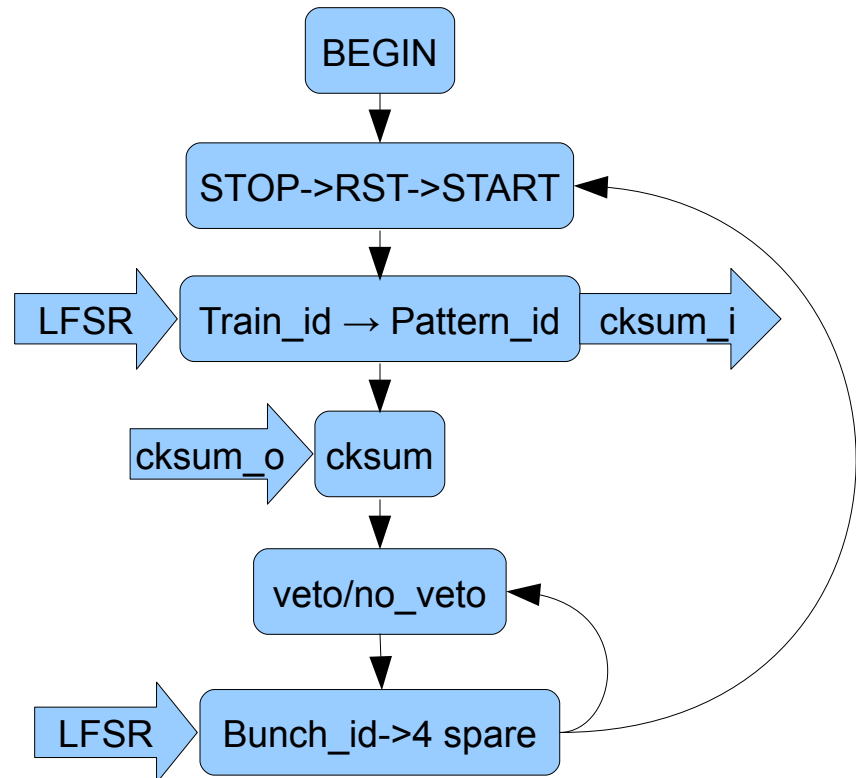
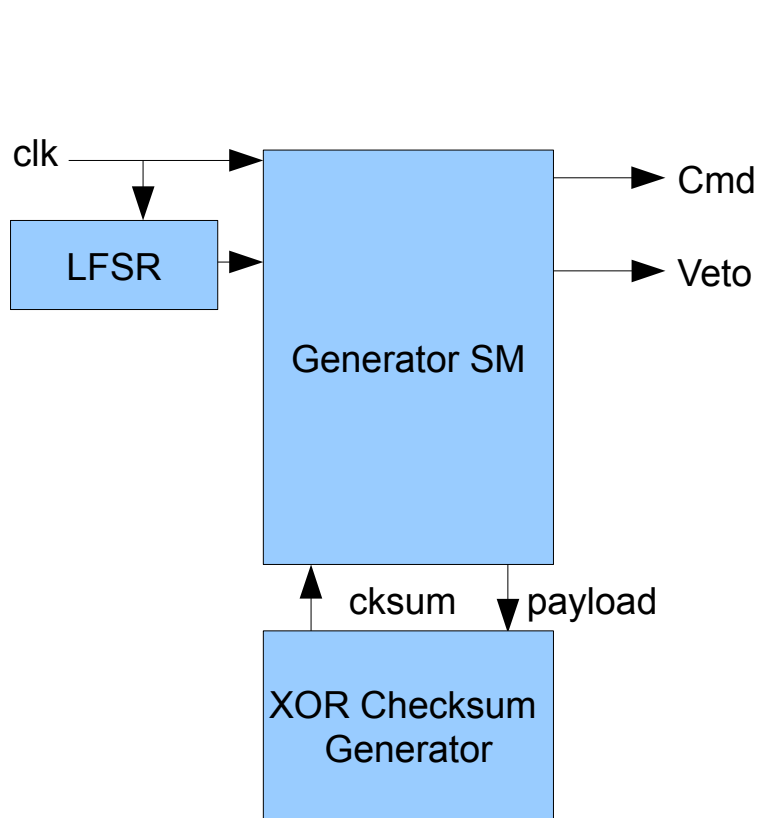
# Block diagram: self checking receiver



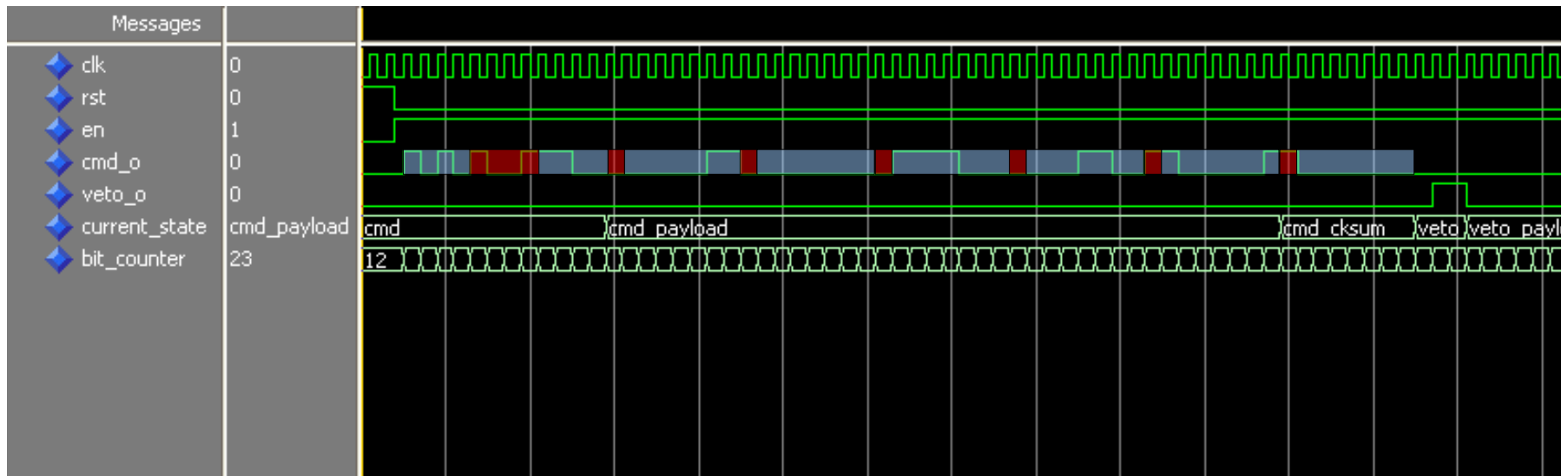
# Simulation: self checking receiver



# Block diagram emulator



# Simulation: emulator



# Aims

Complete testing on emulator &  
receiver

Test emulator and receiver on V5  
dev board

Add further controls for the  
emulator

Complete the timing interface





# Conclusion

Have a robust timing receiver

Have created stable test bench  
environment for simulation

Good prospects of completing the  
remainder soon



# spares



# Command State Machine

