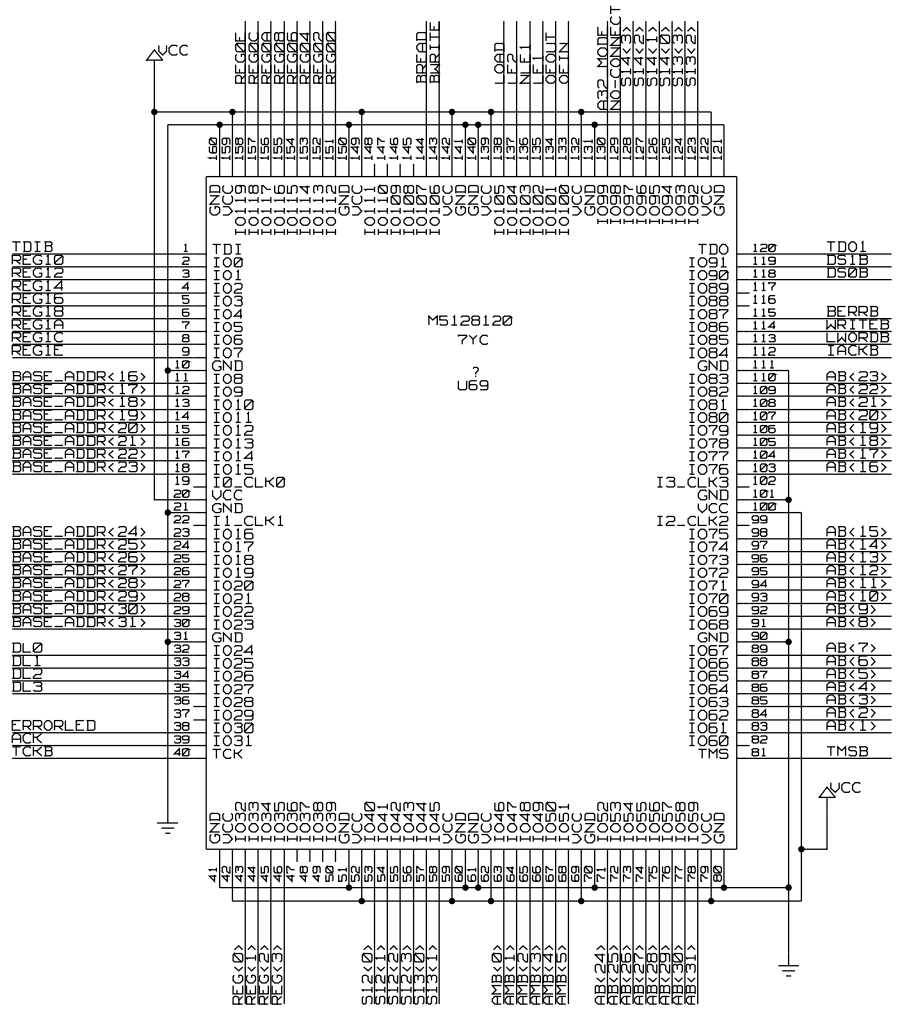
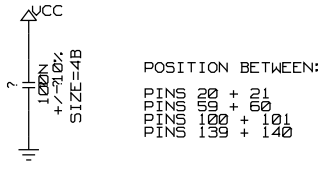
	1	2	3	4	5	6	7	8	9	10
A	2-TE-0103-001-10-A	THIS DRAWING CONFORMS TO B.S. 3939								

PLD1 - VME INTERFACE



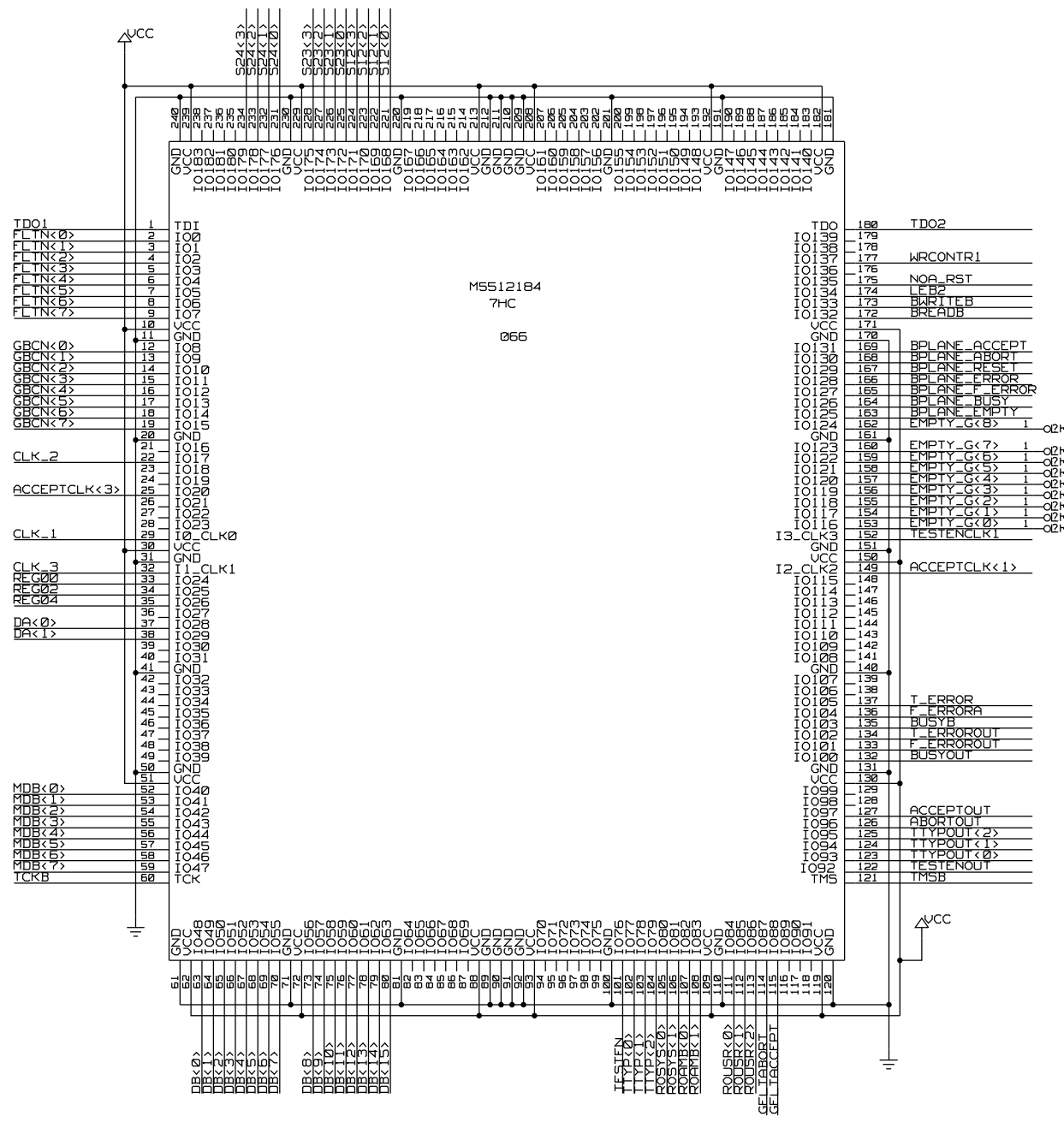
DECOUPLING



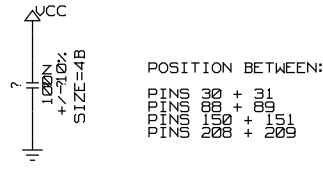
POSITION BETWEEN:

PINS 20 + 21
PINS 60 + 61
PINS 100 + 101
PINS 139 + 140

PLD2 - GFLT REGS



DECOUPLING



POSITION BETWEEN:

PINS 30 + 31
PINS 80 + 81
PINS 150 + 151
PINS 200 + 201

ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS
A	06-06-1999	- - - -	D. J. BECKETT	M. POSTRANECKY	M. POSTRANECKY	

USED ON: ZEUS MVD

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RUTHERFORD APPLETON LABORATORY, CHILTON, OXON OX11 0QX.

TITLE

PC3151M CIRCUIT

C+C MASTER

A 2-TE-0103-001-10-A

TOTAL NO. OF SHEETS 11

A 2-TE-0103-001-10-A