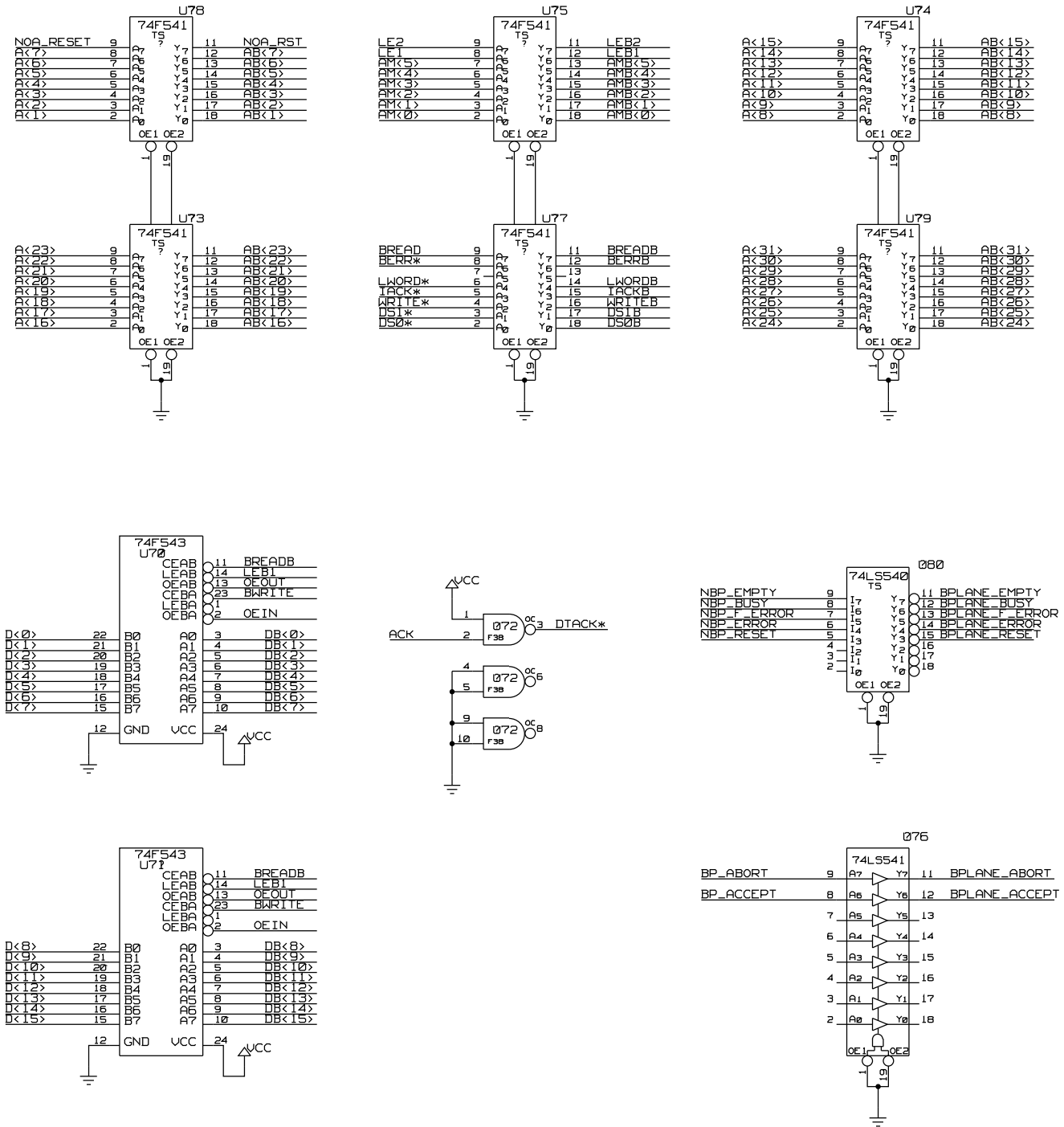
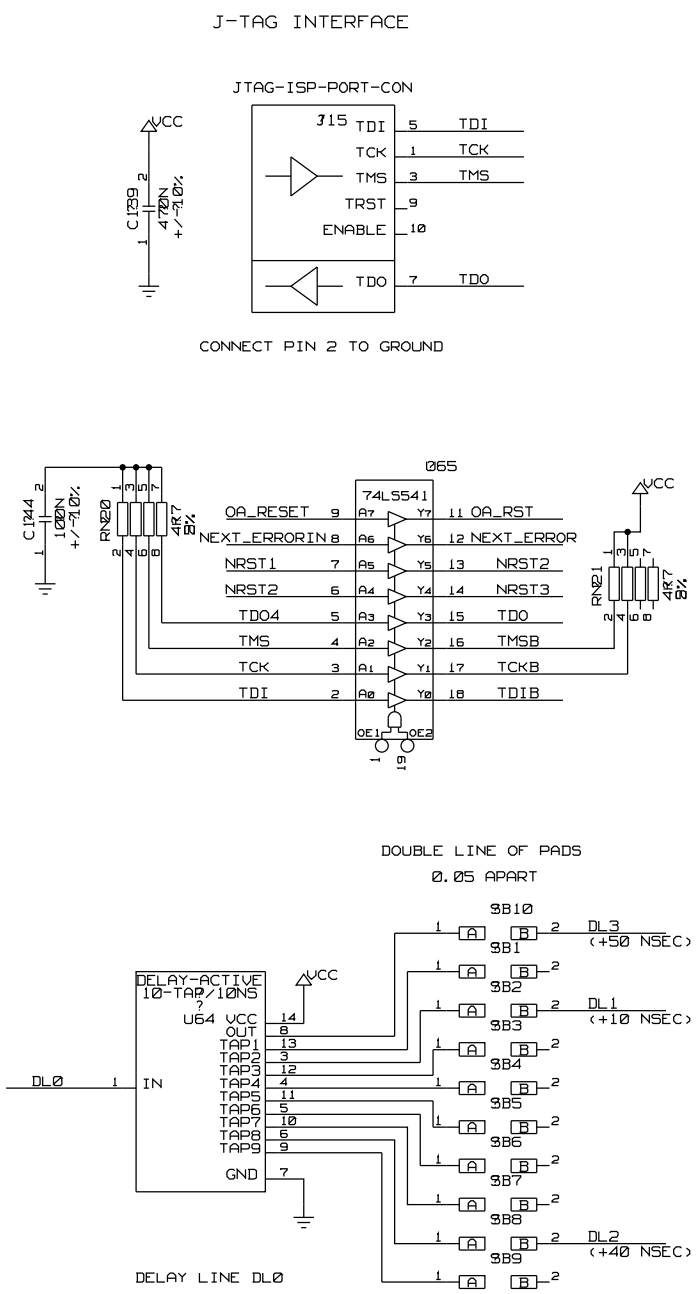
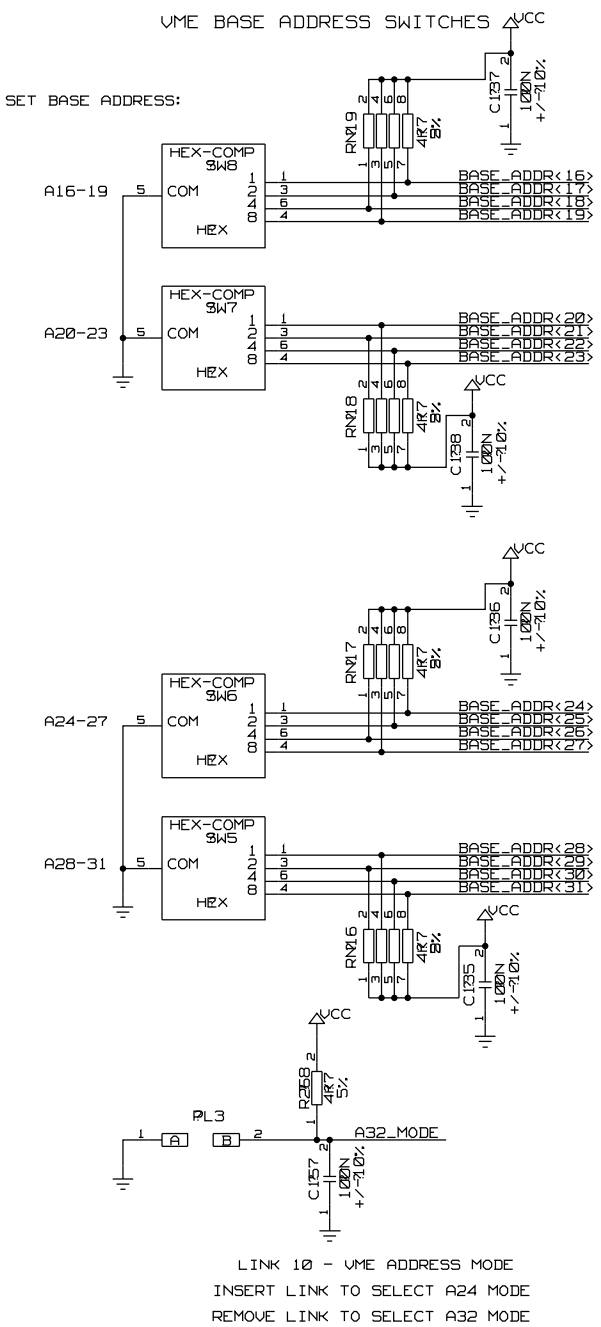
| | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|
| | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 |
| A | 2-TE-0103-001-02-A | THIS DRAWING CONFORMS TO B.S. 3939 | | | | | | | | |

# VME AND BACKPLANE BUFFERS




# VME DELAYS AND BASE SELECT




DECOUPLING

ALL CHIPS IN

DECOUPLED SOCKETS

TO VCC

| ISSUE | DATE | MOD NO. | DRN BY. | CHKD. | APPD. | STATUS |
|---|---|---|---|---|---|---|
| A | 06-06-1999 | - - - - | D. J. BECKETT | M. POSTRANECKY | M. POSTRANECKY | |

USED ON ZEUS MVD

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CCLRC COUNCIL FOR THE CENTRAL LABORATORY OF THE RESEARCH COUNCILS

RUTHERFORD APPLETON LABORATORY, CHILTON, OXON OX11 0QX.

TITLE

# PC3151M CIRCUIT

## C+C MASTER

A 2-TE-0103-001-02-A

TOTAL NO. OF SHEETS 11

A 2-TE-0103-001-02-A