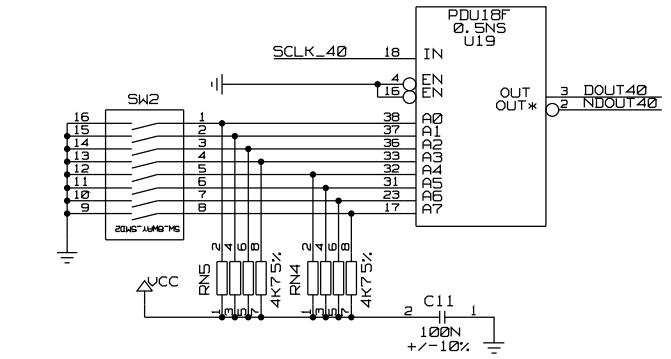
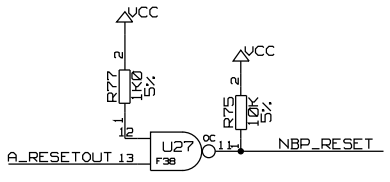
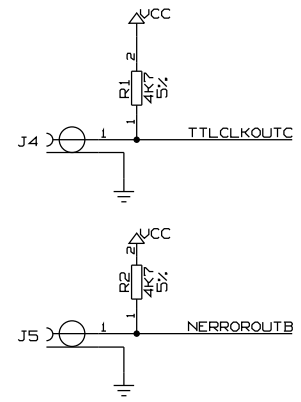
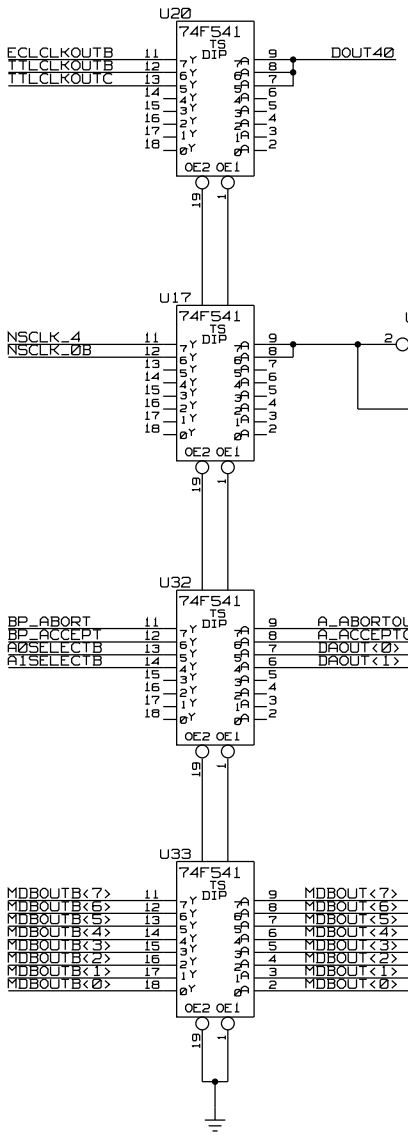
SET ADC CLOCK PHASE



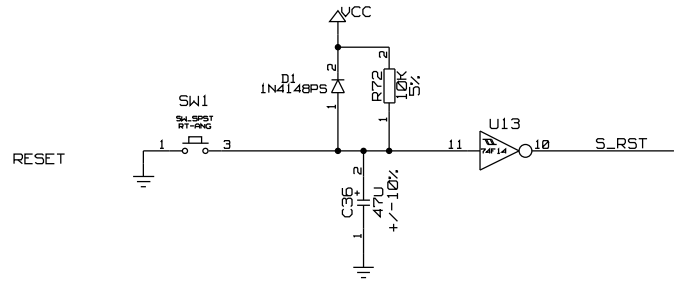


EXTERNAL TTL OUTPUTS





RESET SWITCH



DECOUPLING

ALL CHIPS IN

DECOUPLED SOCKETS

TO VCC

ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS
A	10-08-1999	- - - -	D. J. BECKETT	M. POSTRANECKY	M. POSTRANECKY	

USED ON ZEUS MVD

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COUNCIL FOR THE CENTRAL LABORATORY OF THE RESEARCH COUNCILS
RUTHERFORD APPLETON LABORATORY, CHILTON, OXON OX11 0QX.

TITLE

PC3156M CIRCUIT

C+C SLAVE

A | 2-TE-0103-005-05-A

TOTAL NO. OF SHEETS 7

A | 2-TE-0103-005-05-A