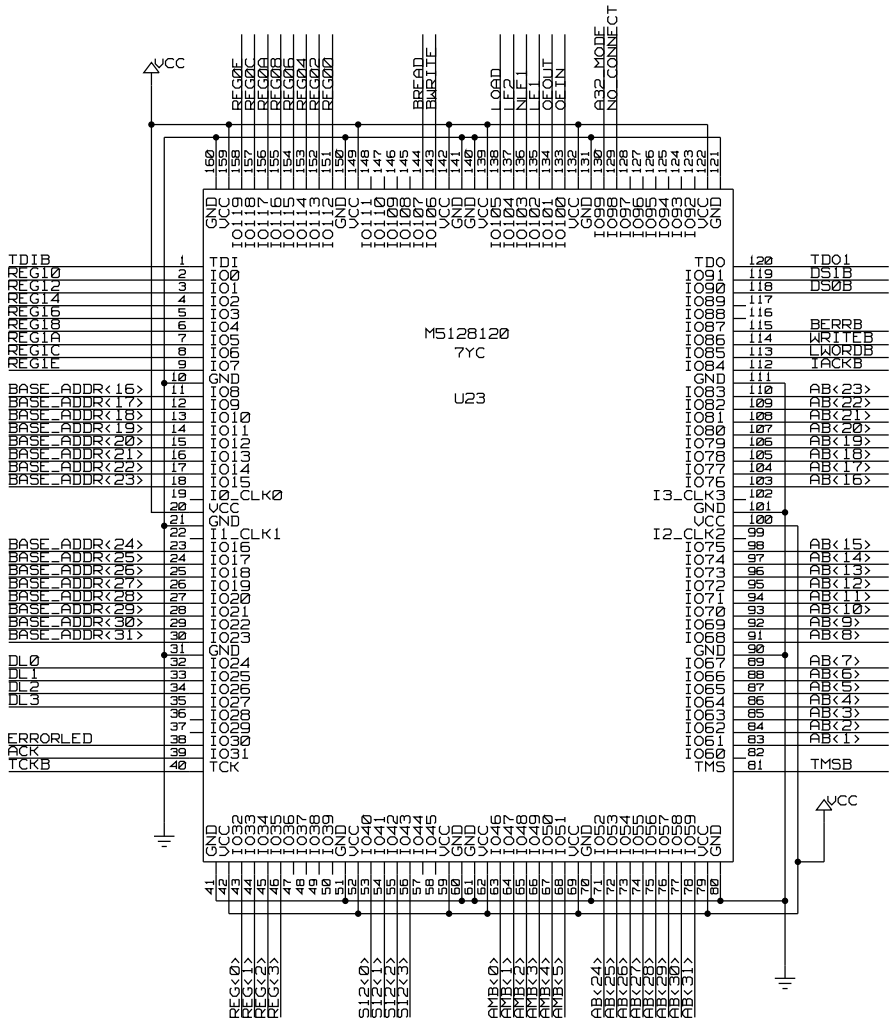
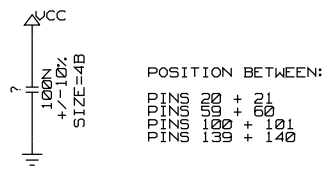
A	2-TE-0103-005-07-A	THIS DRAWING CONFORMS TO B.S. 3939

PLD1 - VME INTERFACE



DECOUPLING



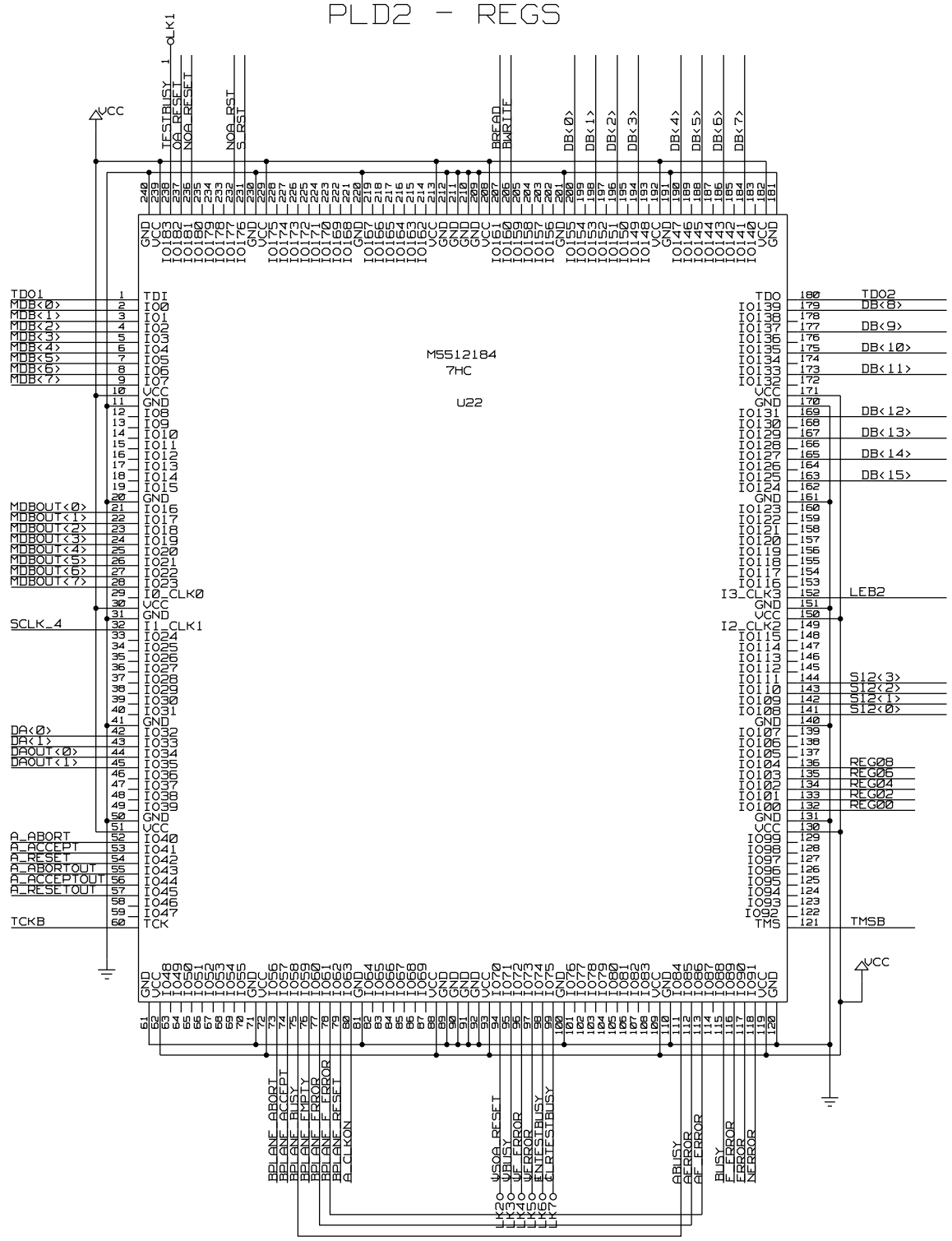
POSITION BETWEEN:

PIN5 20 + 21
PIN5 60 + 61
PIN5 100 + 101
PIN5 139 + 140

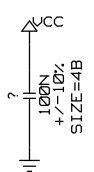
DECOUPLING

ALL CHIPS IN
DECOUPLED SOCKETS
TO VCC

PLD2 - REGS



DECOUPLING



POSITION BETWEEN:

PIN5 30 + 31
PIN5 80 + 89
PIN5 150 + 151
PIN5 208 + 209

ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS
A	10-08-1999	- - - -	D. J. BECKETT	M. POSTRANECKY	M. POSTRANECKY	

USED ON: ZEUS MVD

© CCLRC 1999

CCLRC
COUNCIL FOR THE CENTRAL LABORATORY OF THE RESEARCH COUNCILS
RUTHERFORD APPLETON LABORATORY, CHILTON, OXON OX11 0QX.

TITLE

PC3156M CIRCUIT

C+C SLAVE

A 2-TE-0103-005-07-A

TOTAL NO. OF SHEETS 7