

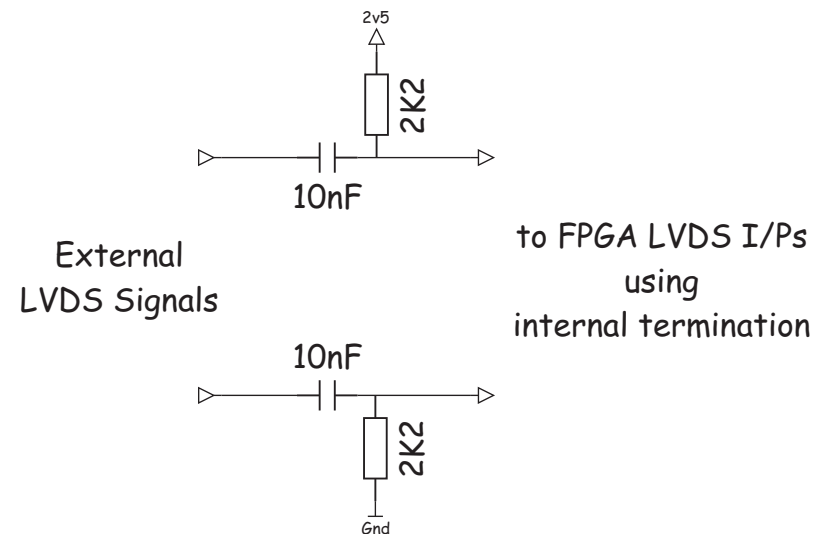
AC Coupling circuit:

repeat for each LVDS I/P

This version uses external R_{TERM}

NOTES:

- * This must be a clean 2.5 V supply:
preferably separate regulator
- ** Choose R_T to match trace Z_{DIFF}
Should be close to 100R
for full LVDS swing



AC Coupling circuit:

using internal R_{TERM} ***

NOTE:

- ***trim R_{TERM} to match line Z_0 .
Use the Xilinx mechanism for this -
relies on 2 external resistors
for the I-O bank being used
and expert use of .UCF file

2009-04-23

Reduced cct

AC Coupling