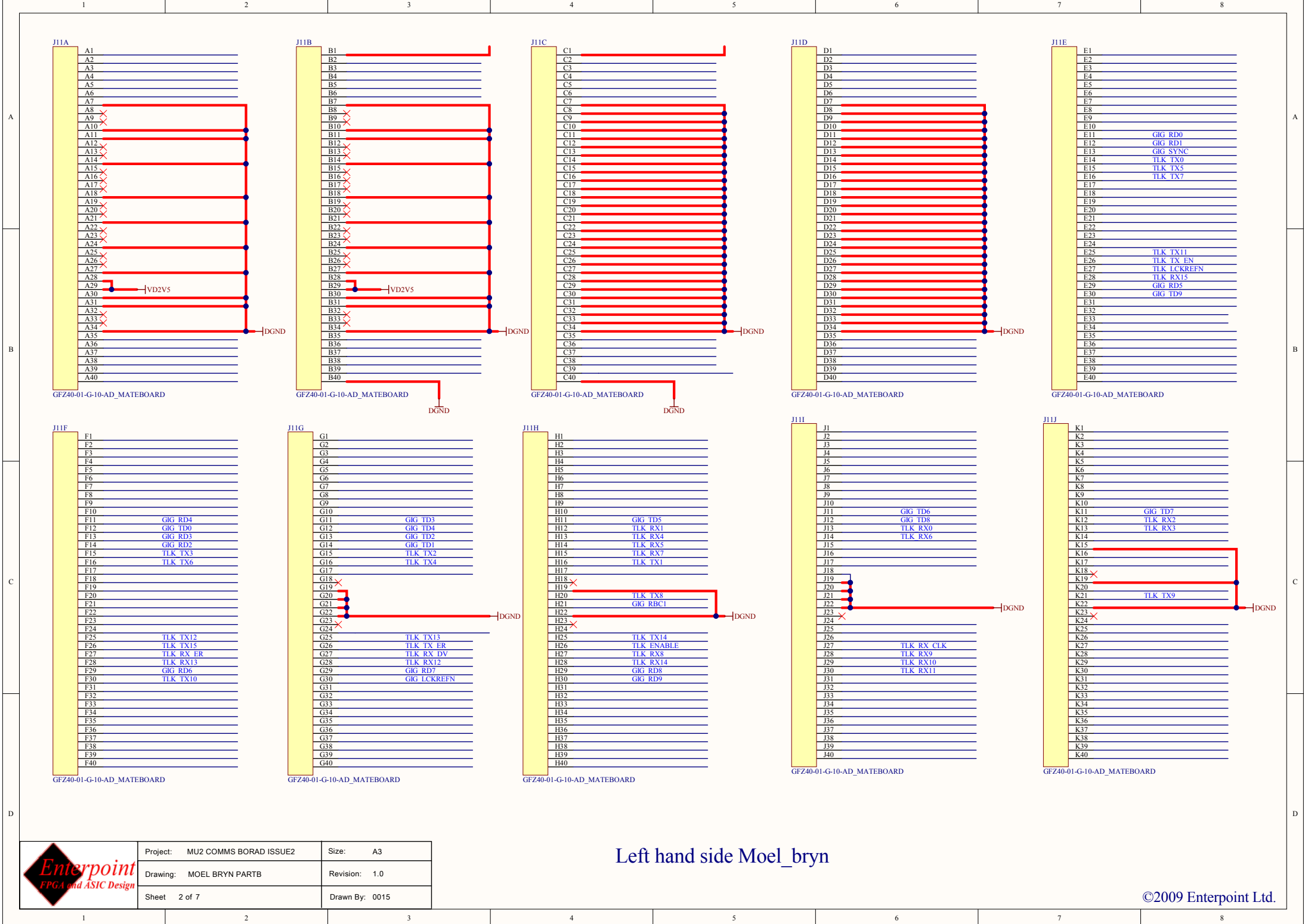


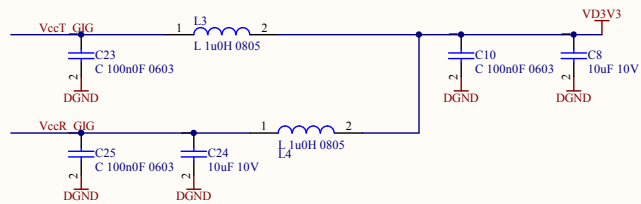
Project: MU2 COMMS BORAD ISSUE2	Size: A3
Drawing: MOEL BRYN PARTA	Revision: 1.0
Sheet 1 of 7	Drawn By: 0015

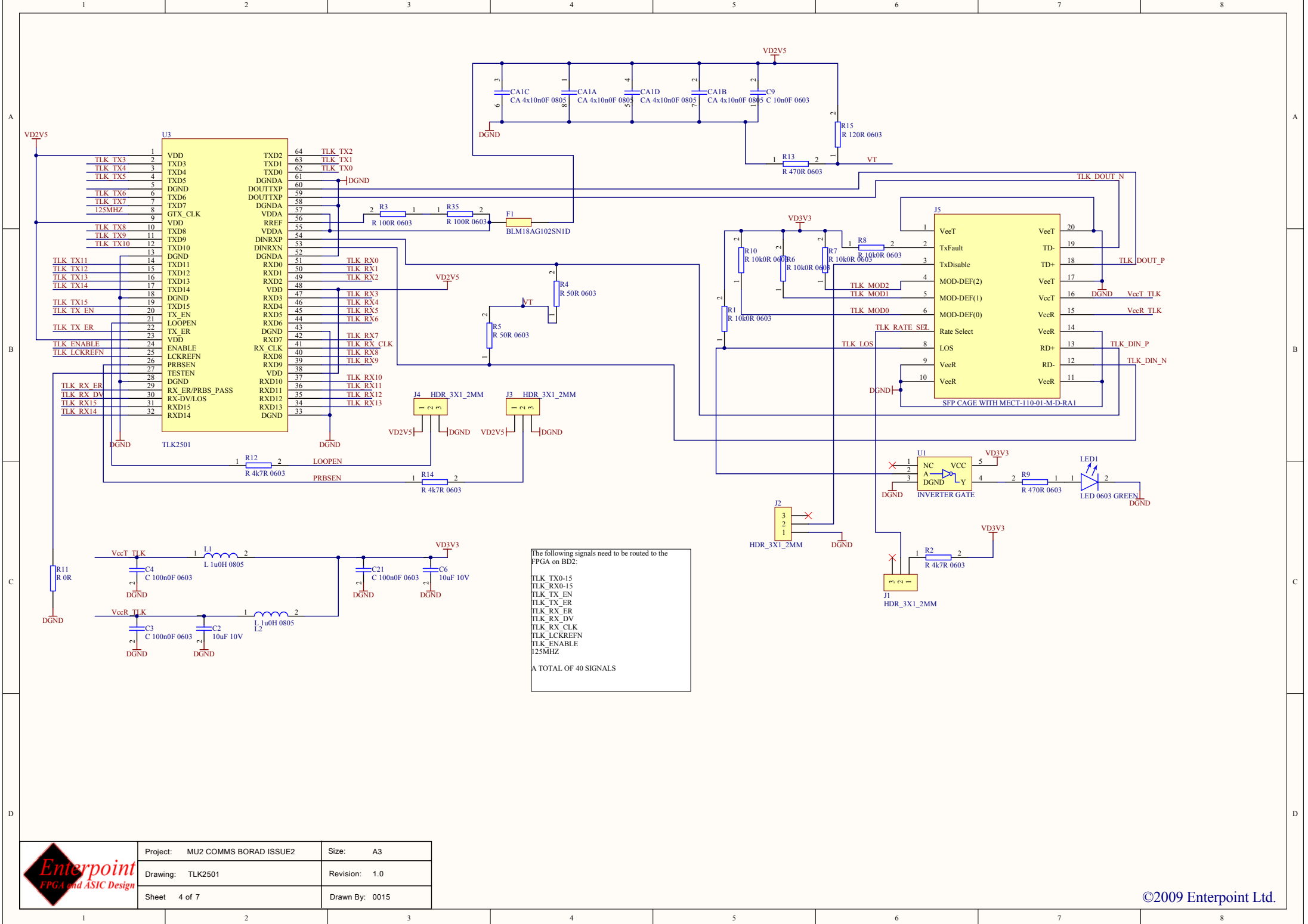
Right hand side Moel-bryn



Project:	MU2 COMMS BORAD ISSUE2	Size:	A3
Drawing:	MOEL BRYN PARTB	Revision:	1.0
Sheet	2 of 7	Drawn By:	0015

Left hand side Moel_bryn





Project:	MU2 COMMS BORAD ISSUE2	Size:	A3
Drawing:	TLK2501	Revision:	1.0
Sheet	4 of 7	Drawn By:	0015

A

B

C

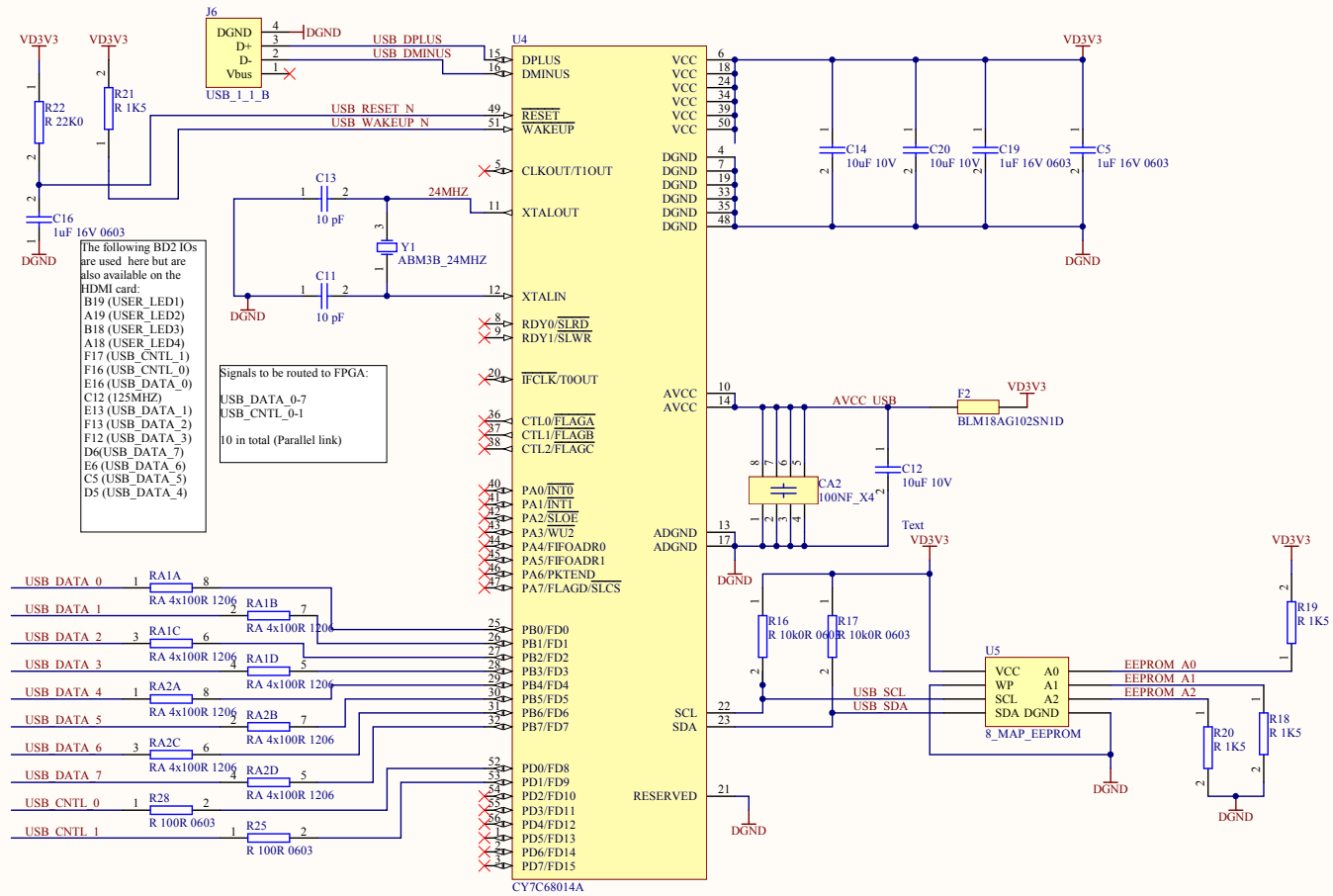
D

A

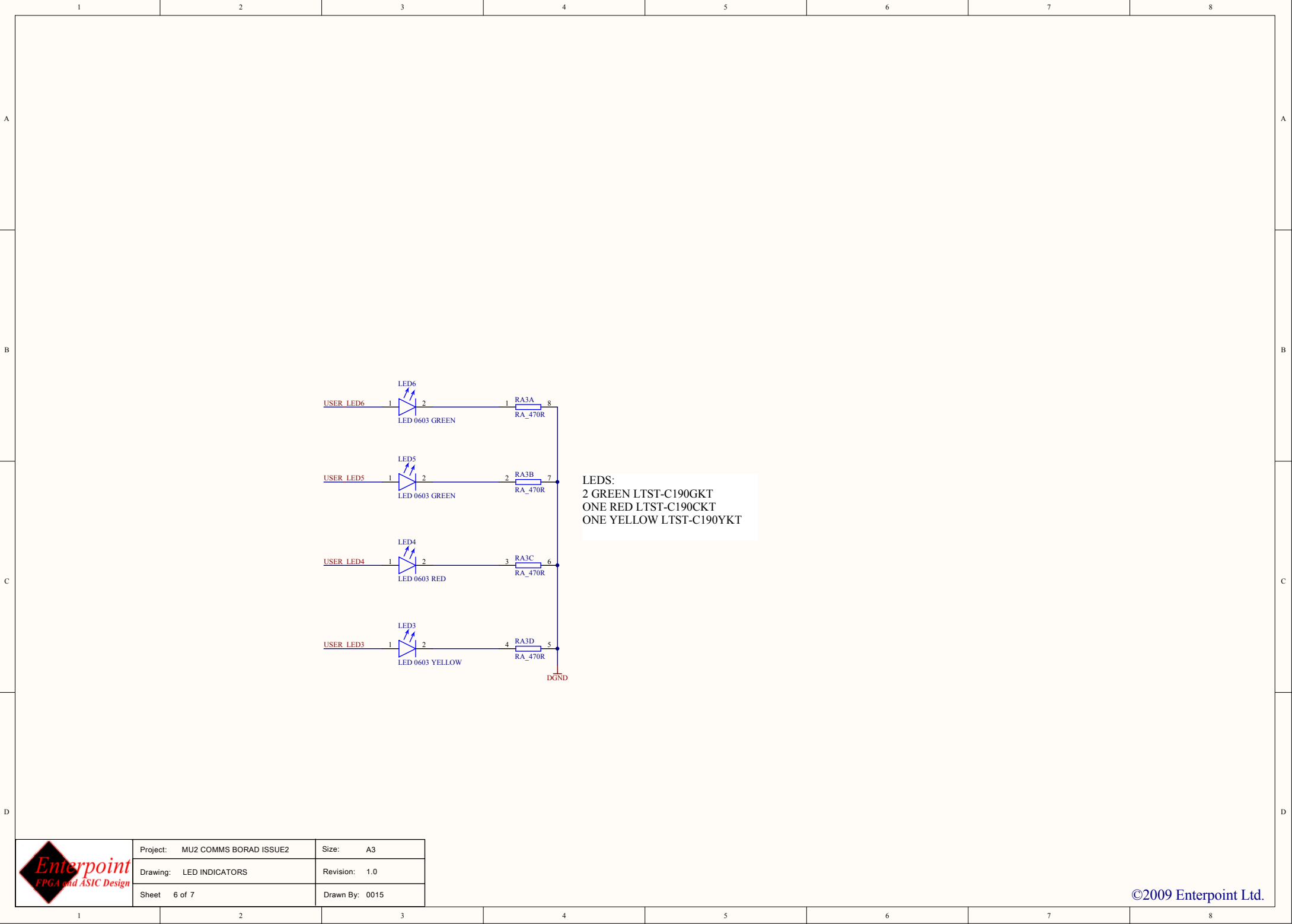
B

C

D



Project: MU2 COMMS BORAD ISSUE2	Size: A3
Drawing: USB2 INTERFACE	Revision: 1.0
Sheet 5 of 7	Drawn By: 0015



Project:	MU2 COMMS BORAD ISSUE2	Size:	A3
Drawing:	LED INDICATORS	Revision:	1.0
Sheet	6 of 7	Drawn By:	0015

A

B

C

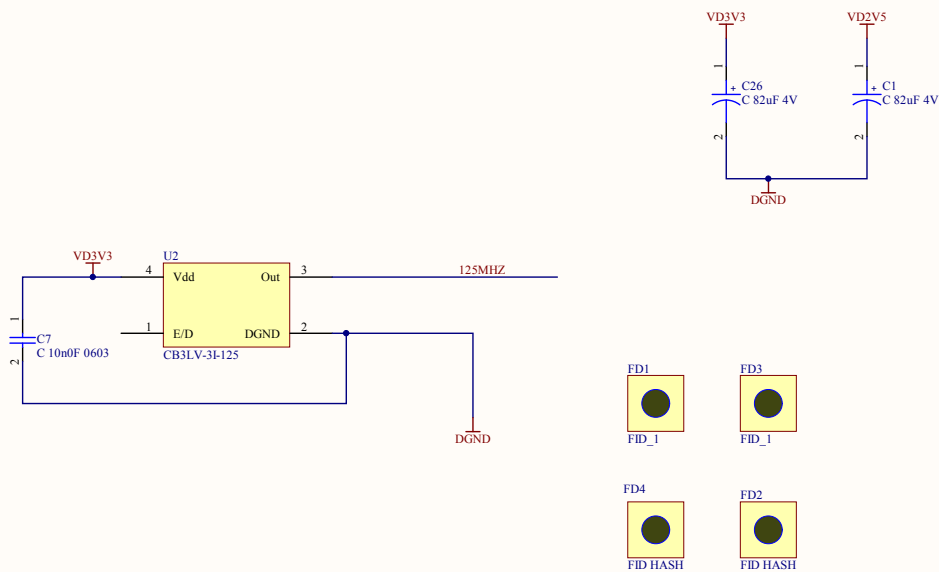
D

A

B

C

D



Project:	MU2 COMMS BORAD ISSUE2	Size:	A3
Drawing:	OSCILLATOR	Revision:	1.0
Sheet	7 of 7	Drawn By:	0015

Layer Stack Up Detail for: Swinyard1.PcbDoc

Layer

Name

Top Layer

Ground Plane

Core Voltage

Track Layer 1

UREF selection & IUS

Track Layer 2

Aux Voltage

Track Layer 3

Bank Voltage

Track Layer 4

Q09 Power Plane & Programming Voltage

Bottom Layer

